TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (L²-π-MOS V)

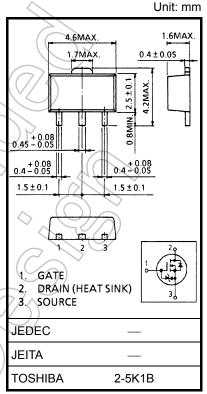
2SK2963

DC-DC Converter, Relay Drive and Motor Drive Applications

- 4-V gate drive
- Low drain-source ON-resistance: RDS (ON) = 0.5Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 1.2 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 100 \text{ V)}$
- Enhancement mode: $V_{th} = 0.8 \text{ to } 2.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

tics	Symbol	Rating	Unit
	V_{DSS}	100	À
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		100	V
	V_{GSS}	±20	V
DC (Note 1)	I _D	_(1)	Α
Pulse (Note 1)	I _{DP}	3	
1	PD	0.5	<
(Note 2)	P _D (1.5	W
energy (Note 3)	EAS	137	mJ
	IAR	1 (A
nergy (Note 4)	EAR	0.05	
	Tch	150	→°C
nge	T _{stg}	-55 to 150	°C
	DC (Note 1) Pulse (Note 1) (Note 2) energy (Note 3)	$\begin{array}{c c} & V_{DSS} \\ S = 20 \text{ k}\Omega) & V_{DGR} \\ \hline & V_{GSS} \\ \hline DC & (Note 1) & I_{D} \\ \hline Pulse & (Note 1) & I_{DP} \\ \hline & & P_{D} \\ \hline & (Note 2) & P_{D} \\ \hline energy & (Note 3) & EAS \\ \hline hergy & (Note 4) & EAR \\ \hline & T_{ch} \\ \hline \end{array}$	V _{DSS} 100 V _{DGR} 100 V _{DGR} 100 V _{DGS} ±20 DC (Note 1) I _D 1 Pulse (Note 1) I _{DP} 3 P _D 0.5 (Note 2) P _D 1.5 energy (Note 3) E _{AS} 137 I _{AR} 1 energy (Note 4) E _{AR} 0.05 T _{Ch} 150 1



Weight: 0.05 g (typ.)

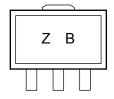
- Note 1: Ensure that the channel temperature does not exceed 150°C.
- Note 2: Mounted on a ceramic board (25.4 mm × 25.4 mm × 0.8 mm)
- Note 3: $V_{DD} = 25 \text{ V}$, $T_{Ch} = 25^{\circ}\text{C}$ (initial), L = 221 mH, $R_G = 25 \Omega$, $I_{AR} = 1 \text{ A}$
- Note 4: Repetitive rating: pulse width limited by maximum junction temperature.
- Note 5: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient	R _{th (ch-a)}	250	°C/W

Marking



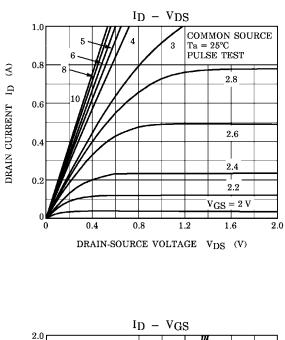
(The two digits represent the part number.)

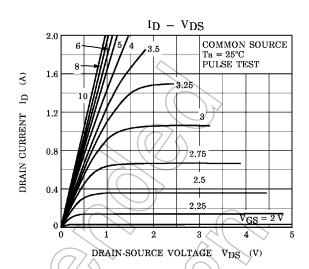
Electrical Characteristics (Ta = 25°C)

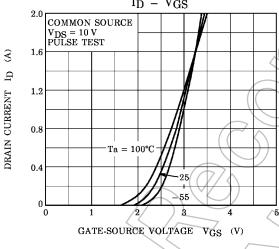
				\smile			
Characteris	stics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_		±10	μΑ
Drain cut-off current		I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V	_	4	100	μΑ
Drain-source breakdow	vn voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	100	7/	<u>></u> —	V
Gate threshold voltage	!	V _{th}	V _{DS} = 10 V, I _D = 1 mA	0.8	D) //	2.0	V
Drain-source ON resistance		-	V _{GS} = 4 V, I _D = 0.5 A		0.65	0.95	Ω
		R _{DS} (ON)	$V_{GS} = 10 \text{ V}, I_D = 0.5 \text{ A}$	7	0.5	0.7	
Forward transfer admit	tance	Y _{fs}	$V_{DS} = 10 \text{ V, } I_D = 0.5 \text{ A}$	0.6	1.2	_	S
Input capacitance		C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		140	_	pF
Reverse transfer capac	citance	C _{rss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$) —	20	_	pF
Output capacitance		Coss	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	45	_	pF
Switching time	Rise time	t _r	10 V I _D = 0.5 A VOUT		8		
	Turn-on time	ton	VGS 0 V		13		ns
	Fall time		<u>v</u> <u>v</u>		45		115
	Turn-off time	toff	V _{DD} ≈ 50 V Duty ≤ 1%, t _W = 10 μs		175		
Total gate charge (gate-source plus gate-	-drain)	Qg	$V_{DD} \approx 80 \text{ V, } V_{GS} = 10 \text{ V, } I_D = 1 \text{ A}$	_	6.3	_	nC
Gate-source charge		Qgs	$V_{DD} \approx 80 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 1 \text{ A}$	_	4.3	_	nC
Gate-drain ("miller") ch	arge	Q _{gd}	$V_{DD} \approx 80 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 1 \text{ A}$	_	2		nC

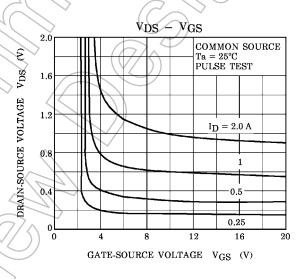
Source-Drain Ratings and Characteristics (Ta = 25°C)

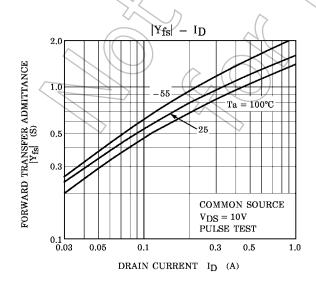
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR	_	_		1	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_			3	Α
Forward voltage (diode)	V _{DSF}	$I_{DR} = 1 \text{ A}, V_{GS} = 0 \text{ V}$		_	-1.5	V
Reverse recovery time	t _{rr}	$I_{DR}=1~A,~V_{GS}=0~V,~dI_{DR}/dt=50~A/\mu s$		80	_	ns
Reverse recovery charge	Q _{rr}	$I_{DR}=1~A,~V_{GS}=0~V,~dI_{DR}/dt=50~A/\mu s$	_	140	_	μС

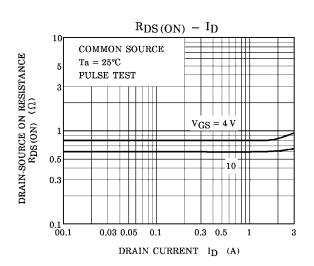




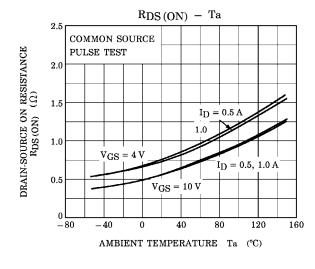


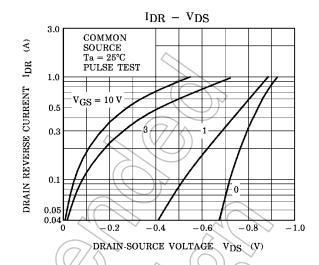


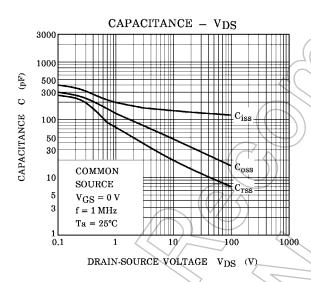


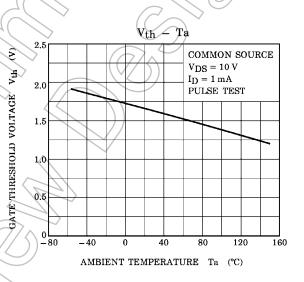


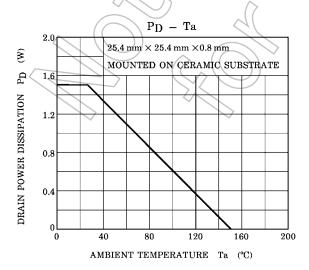
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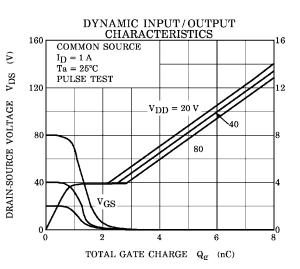


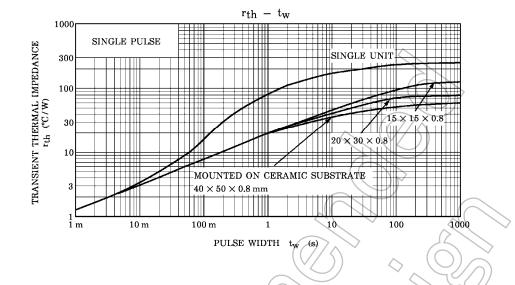


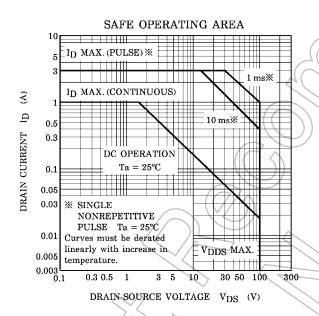


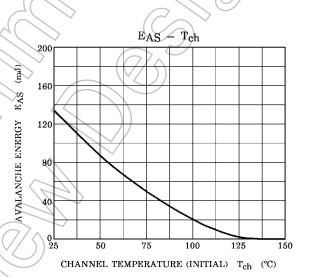


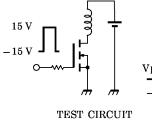


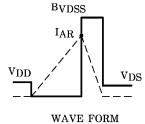












$$R_G = 25 \Omega$$

 $V_{DD} = 25 V$. $L = 221 \text{ mH}$ EAS

$$\begin{array}{ll} R_G = 25~\Omega \\ V_{DD} = 25~V,~L = 221~mH \end{array} \quad E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot (~\frac{B_{VDSS}}{B_{VDSS} - V_{DD}}) \end{array}$$

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