TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

# 2SK2698

# DC-DC Converter, Relay Drive and Motor Drive Applications

• Low drain–source ON resistance : RDS (ON) = 0.35  $\Omega$  (typ.)

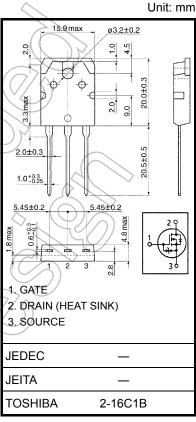
• High forward transfer admittance :  $|Y_{fs}| = 11 \text{ S (typ.)}$ 

• Low leakage current  $: I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 500 \text{ V)}$ 

• Enhancement mode :  $V_{th} = 2.0 \text{ to } 4.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$ 

#### Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	(Unit )
Drain-source voltage		$V_{DSS}$	500	V
Drain-gate voltage (Ro	<sub>SS</sub> = 20 kΩ)	$V_{DGR}$	500	V
Gate-source voltage		$V_{GSS}$	±30	> ∨
Drain current	DC (Note 1)	I <sub>D</sub>	15	Α
	Pulse (Note 1)	I <sub>DP</sub>	60	A
Drain power dissipation	n (Tc = 25°C)	PD	150	/_w
Single pulse avalanche	e energy (Note 2)	EAS	630	mJ
Avalanche current		IAR	15	A
Repetitive avalanche e	nergy (Note 3)	(EAR))	15	μŊ
Channel temperature		Tch	150	°C
Storage temperature ra	ange	T <sub>stg</sub>	-55~150	°C



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics Symbol	Max	Unit
Thermal resistance, channel to case Rth (ch-c)	0.833	°C/W
Thermal resistance, channel to ambient R <sub>th (ch-a)</sub>	50	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 4.76 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 15 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

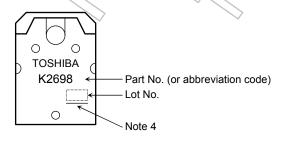
### **Electrical Characteristics (Ta = 25°C)**

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage co	urrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±25 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Gate-source br	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cu	irrent	I <sub>DSS</sub>	V <sub>DS</sub> = 500 V, V <sub>GS</sub> = 0 V	7	_	100	μΑ
Drain-source b	reakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	500	/	_	V
Gate threshold	voltage	$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	))~	4.0	V
Drain-source O	N resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 7.0 A	) <u> </u>	0.35	0.4	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 7.0 A	6)	11	_	S
Input capacitance		C <sub>iss</sub>		_	2600	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	280	_	pF
Output capacitance		Coss		_	880	_	
Switching time	Rise time	t <sub>r</sub>	$V_{\rm GS} = 7A$ $V_{\rm out}$ $V_{\rm DD} = 210V$	- (	50	<u> </u>	- ns
	Turn-on time	t <sub>on</sub>			85	) –	
	Fall time	t <sub>f</sub>			65	_	
	Turn-off time	t <sub>off</sub>	Duty ≤1%, t <sub>w</sub> =10μs	) -	260	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	58	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$	_	36	_	nC
Gate-drain ("miller") Charge		Q <sub>gd</sub>		_	22	_	

### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR	<u> </u>	_	_	15	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	<del>-</del>	-	-	60	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 15 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 15 A, V <sub>GS</sub> = 0 V		400		ns
Reverse recovery charge	Qrr	dl <sub>DR</sub> / dt = 100 A / μs		4.3	_	μC

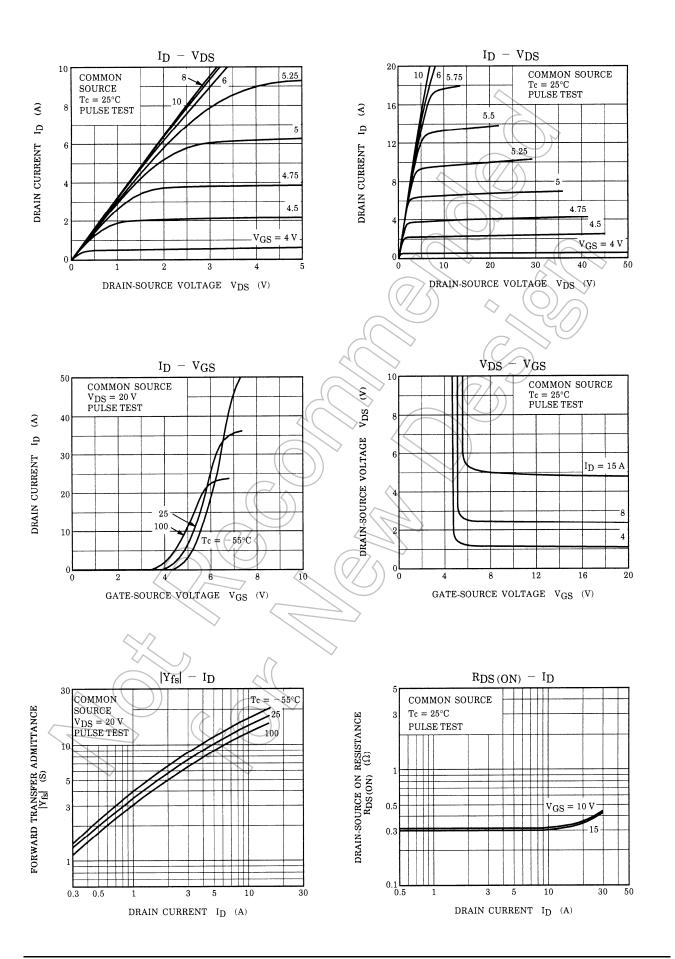
#### Marking



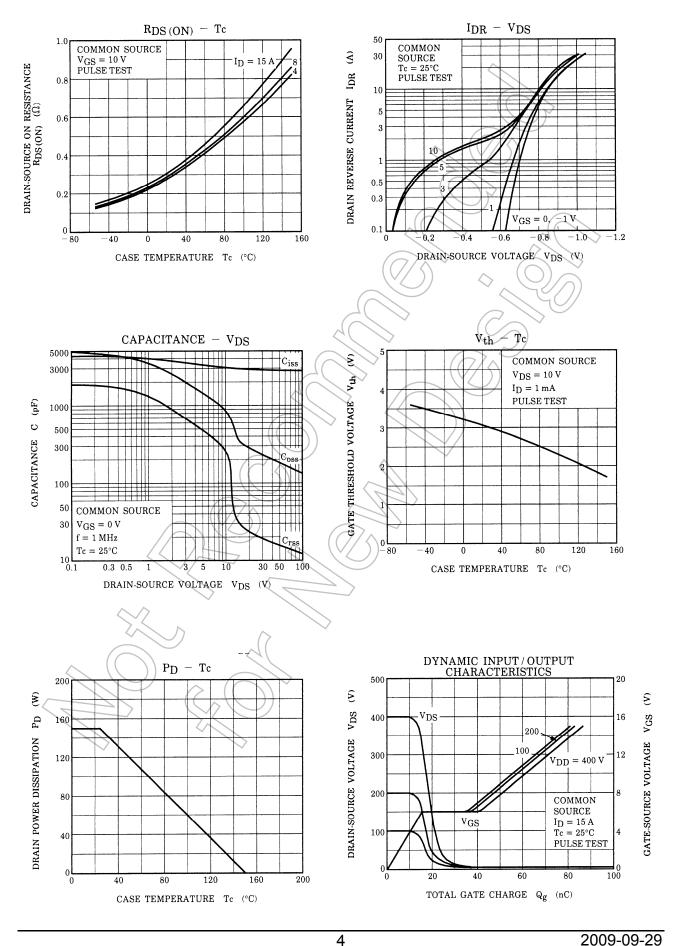
Note 4: A line under a Lot No. identifies the indication of product Labels.

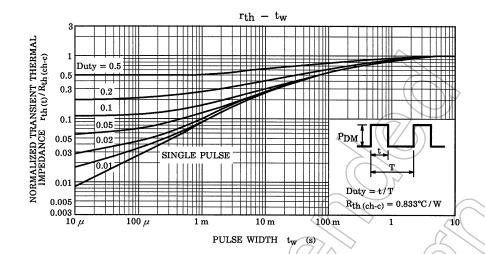
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

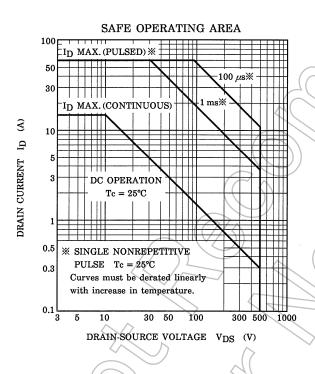
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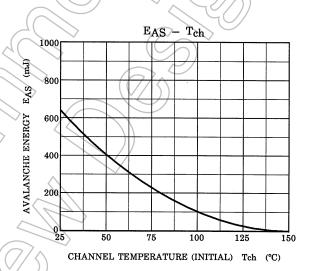


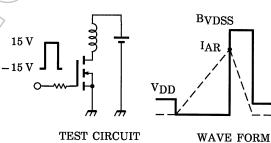
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$$R_G = 25 \Omega$$
  
 $V_{DD} = 90 \text{ V}, L = 4.76 \text{ mH}$ 

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^{2} \cdot \left( \frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

 $v_{DS}$ 

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