TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (L^2 - π -MOSV)

2SK2312

Chopper Regulator, DC-DC Converter and Motor Drive Applications

• 4-V gate drive

• Low drain-source ON resistance $: RDS(ON) = 13 \text{ m}\Omega \text{ (typ.)}$

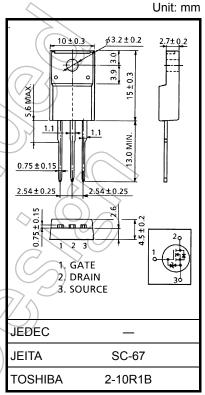
 $\bullet~$ High forward transfer admittance ~ : $|\,Y_{fs}\,|$ = 40 S (typ.)

• Low leakage current $: IDSS = 100 \mu A (max) (VDS = 60 V)$

• Enhancement mode : $V_{th} = 0.8 \text{ to } 2.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	60	V
Drain-gate voltage (R	_{GS} = 20 kΩ)	V_{DGR}	60	V
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1)	ΙD	45	Α
	Pulse (Note 1)	I _{DP}	180	A
Drain power dissipatio	n (Tc = 25°C)	PD	45	<
Single pulse avalanche	e energy (Note 2)	EAS	701	mJ
Avalanche current		IAR	45	A
Repetitive avalanche	energy (Note 3)	EAR	4.5	/mJ
Channel temperature		T _{ch}	150	/_e
Storage temperature r	ange	T _{stg}	-55 to 150	√°C



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics Symbol	Max	Unit
Thermal resistance, channel to case Rth (ch-c)	2.78	°C / W
Thermal resistance, channel to ambient Rth (ch-a)	62.5	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 25 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), $L = 471 \,\mu\text{H}$, $R_G = 25 \,\Omega$, $I_{AR} = 45 \,\text{A}$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

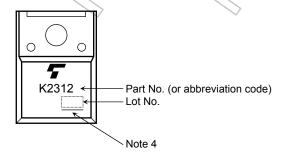
Electrical Characteristics (Ta = 25°C)

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V	-	_	100	μΑ
Drain-source br	reakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	60	_	-	V
Gate threshold	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	0.8	_	2.0	V
Drain-source ON resistance		D	V _{GS} = 4 V, I _D = 25 A	1))19	25	mΩ
		R _{DS} (ON)	V _{GS} = 10 V, I _D = 25 A) 	13	17	
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 25 A	28	40	-	S
Input capacitano	ce	C _{iss}			3350	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	550	-	pF
Output capacitance		Coss		_	1600	_	
Switching time	Rise time	t _r	V _{GS} _{0V}	- (25	\ \ \	
	Turn-on time	t _{on}			55) —	
	Fall time	t _f	4,7,7,7	7	60		ns
	Turn-off time	t _{off}	$\begin{array}{c} V_{DD} = 30V \\ Duty \leq 1\%, t_{W} = 10 \mu\text{s} \end{array}$) -	180	_	
Total gate charge (Gate-source plus gate-drain)		Qg			110	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 48 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 45 \text{ A}$		70	_	nC
Gate-drain ("miller") charge		Q _{gd}			40	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR	<u> </u>	_	_	45	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	-	-	180	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 45 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 45 A, V _{GS} = 0 V	_	120	_	ns
Reverse recovered charge	Qrr	dI _{DR} / dt = 50 A / μs		0.2	_	μC

Marking



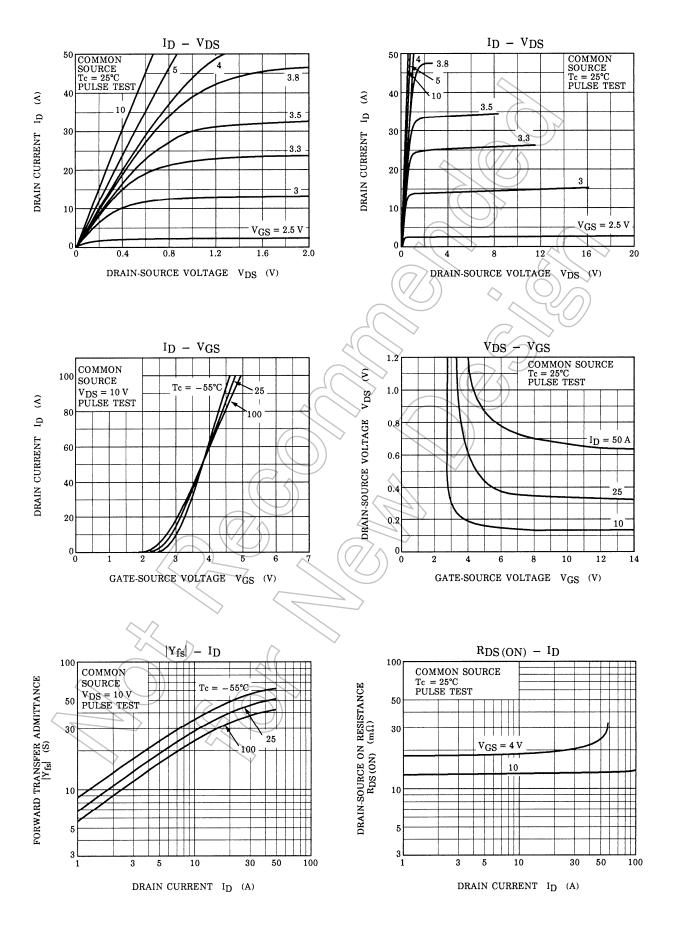
Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

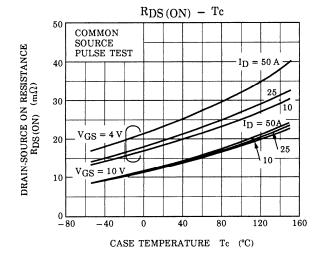
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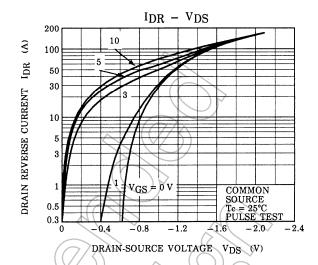
certain hazardous substances in electrical and electronic equipment.

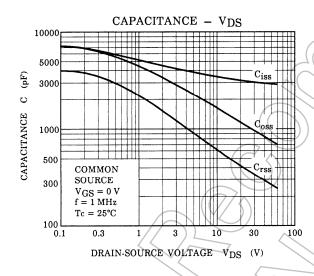
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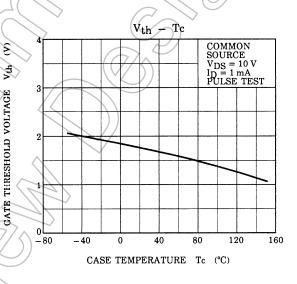


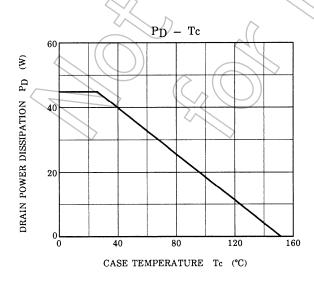
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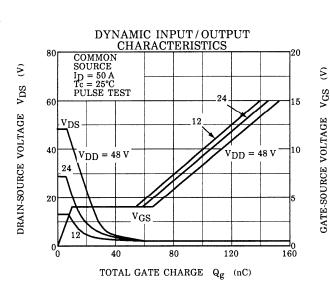




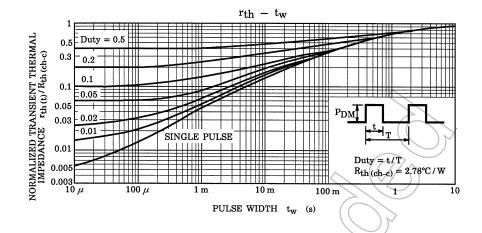


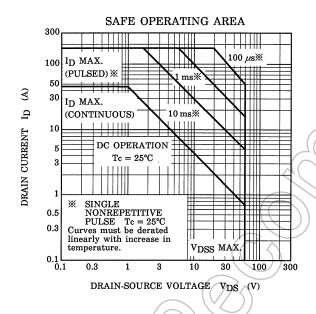


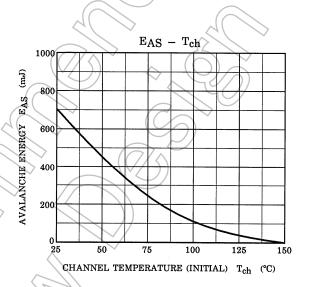


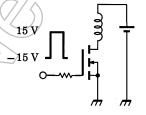


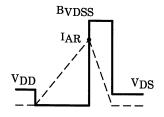
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TEST CIRCUIT

WAVE FORM

$$R_G = 25 \Omega$$

 $V_{DD} = 25 V, L = 471 \mu H$

$$EAS = \frac{1}{2} \cdot L \cdot I^{2} \cdot \left(\frac{BVDSS}{BVDSS - VDD} \right)$$

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