TOSHIBA Transistor Silicon NPN Epitaxial Type

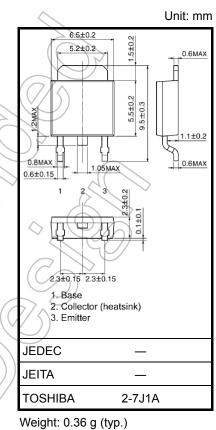
2SC6000

High Speed Switching Applications DC-DC Converter Applications

- High DC current gain: $h_{FE} = 250$ to 400 (IC = 2.5 A)
- Low collector-emitter saturation: V_{CE} (sat) = 0.18 V (max)
- High speed switching: $t_f = 13 \text{ ns} (typ)$

usolute Maximum Ratings (Ta = 25°C)						
Characteristics		Symbol	Rating	Unit		
Collector-base voltage		V _{CBO}	120	$\langle \mathbf{v} \rangle$		
Collector-emitter voltage		V _{CEX}	120	V		
Collector-emitter voltage		V _{CEO}	50	v		
Emitter-base voltage		V _{EBO}	6	v		
Collector current	DC	Ι _C	7.0	^		
	Pulse	I _{CP}	10.0			
Base current		IB	0.5	A		
Collector power dissipation	Tc = 25°C	Pc	20	w		
Junction temperature		(T)	150	°C		
Storage temperature range		T _{stg}	-55 to 150),¢		





Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

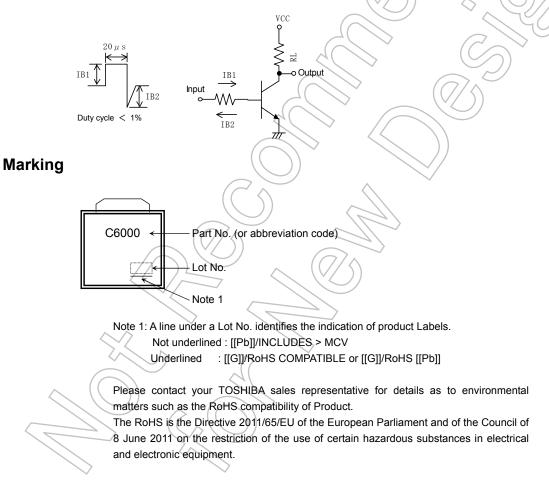
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



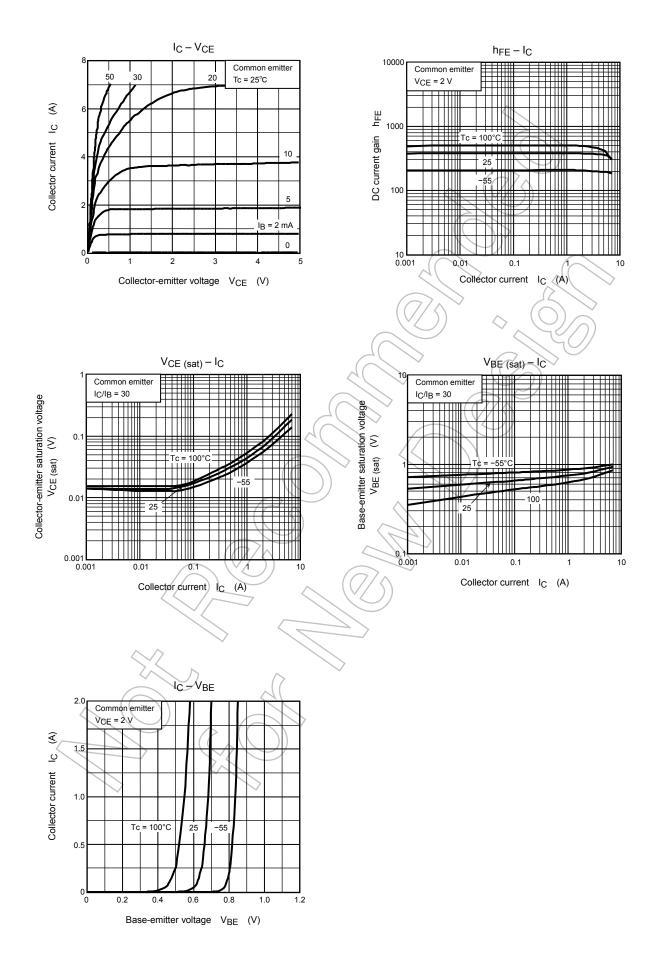
Electrical Characteristics (Ta = 25°C)

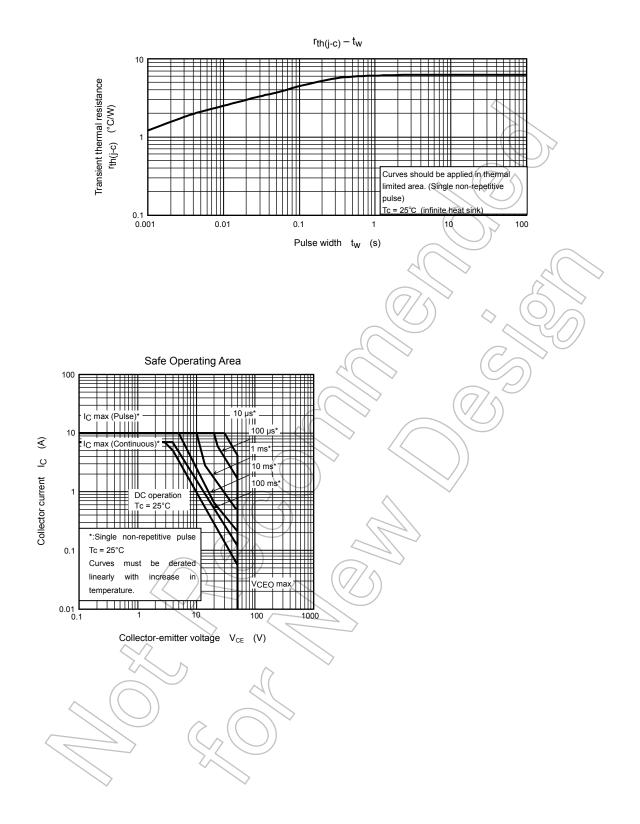
Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off cu	urrent	I _{CBO}	V _{CB} = 120 V, I _E = 0	_	_	100	nA
Emitter cut-off cur	rent	I _{EBO}	V _{EB} = 6 V, I _C = 0	—	_	100	nA
Collector-emitter b	oreakdown voltage	V (BR) CEO	I _C = 10 mA, I _B = 0	50	_	_	V
DC current gain		h _{FE (1)}	V _{CE} = 2 V, I _C = 1 mA	160	1	_	
		h _{FE (2)}	V _{CE} = 2 V, I _C = 2.5 A	250)/	400	
Collector emitter s	aturation voltage	V _{CE (sat)}	I _C = 2.5 A, I _B = 83 mA	77	_	0.18	V
Base-emitter satur	ration voltage	V _{BE (sat)}	I _C = 2.5 A, I _B = 83 mA	\mathcal{O}	_	1.10	V
Switching time	Rise time	t _r	See Figure 1 circuit diagram		45	_	
	Storage time	t _{stg}	$V_{CC} \simeq 20 \text{ V}, \text{ R}_{L} = 8.0 \Omega$	_	450	_	ns
	Fall time	t _f	$I_{B1} = 83 \text{ mA}, I_{B2} = -166 \text{ mA}$	_	13	/	

Figure 1 Switching Time Test Circuit & Timing Chart



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