#### TOSHIBA Diode Silicon Epitaxial Schottky Barrier Type

# **1SS401**

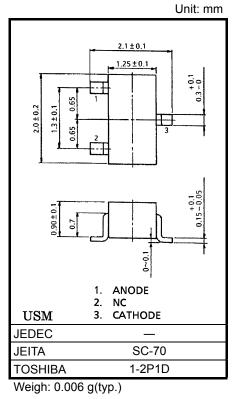
#### High Speed Switching Applications

•	Low forward voltage	: V <sub>F (3)</sub> = 0.38 V (typ.)
-	Lon formara fontago	

- Low reverse current :  $I_R = 50\mu A (max)$
- Small total capacitance : C<sub>T</sub> = 46 pF (typ.)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Maximum (peak) reverse voltage	V <sub>RM</sub>	25	V	
Reverse voltage	V <sub>R</sub>	20	V	
Maximum (peak) forward current	I <sub>FM</sub>	700	mA	
Average forward current	Ι <sub>Ο</sub>	300	mA	
Power dissipation	Р	100	mW	
Junction temperature	Tj	125	°C	
Storage temperature range	T <sub>stg</sub>	-55 to 125	°C	
Operating temperature range	T <sub>opr</sub>	-40 to 100	°C	



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
	V <sub>F (1)</sub>	_	I <sub>F</sub> = 1mA	—	0.16	—	
Forward voltage	V <sub>F (2)</sub>	-	I <sub>F</sub> = 10mA	_	0.22	_	V
	V <sub>F (3)</sub>	-	I <sub>F</sub> = 300mA	_	0.38	0.45	
Reverse current	Ι <sub>R</sub>	-	V <sub>R</sub> = 20V	_	_	50	μA
Total capacitance	CT	—	V <sub>R</sub> = 0, f = 1MHz	-	46	-	pF

#### Pin Assignment (Top View)

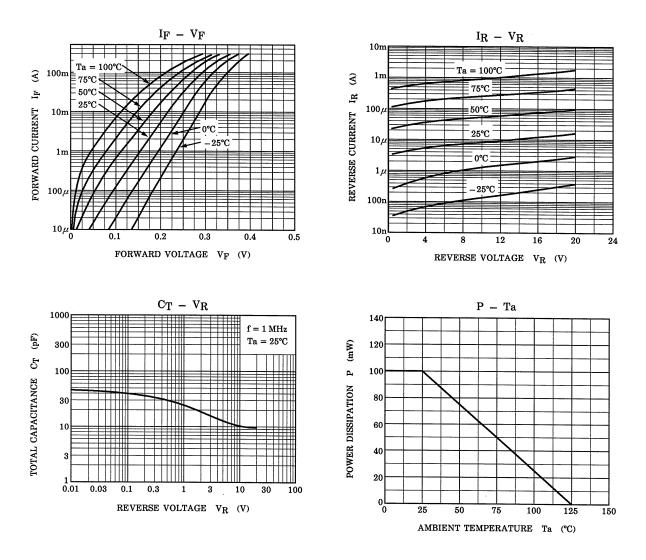


#### Marking



Start of commercial production 1999-03

## **TOSHIBA**



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