## TOSHIBA Diode Silicon Epitaxial Planar Type

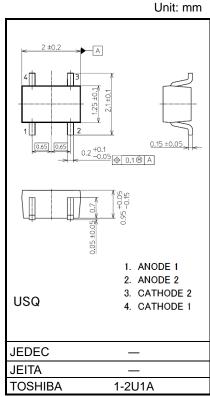
# 1SS382

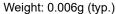
#### Ultra High Speed Switching Application

- Small package
- Composed of 2 independent diodes.
- Low forward voltage  $: V_{F(3)} = 0.92 V (typ.)$
- Fast reverse recovery time: t<sub>rr</sub> = 1.6 ns (typ.)

## Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Maximum (peak) reverse voltage	V <sub>RM</sub>	85	V	
Reverse voltage	VR	80	V	
Maximum (peak) forward current	IFM	300 *	mA	
Average forward current	lo	100 *	mA	
Surge current (10ms)	IFSM	2 *	А	
Power dissipation	P <sub>D</sub> (Note 1, 3)	125	mW	
	P <sub>D</sub> (Note 2, 3)	100		
Junction temperature	T <sub>j</sub> (Note 1)	150	°C	
	T <sub>j</sub> (Note 2)	125		
Storage temperature	T <sub>stg</sub> (Note 1)	-55 to 150	°C	
	T <sub>stg</sub> (Note 2)	-55 to 125	-U	





Note: Using continuously under heavy loads (e.g. the application of high

temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

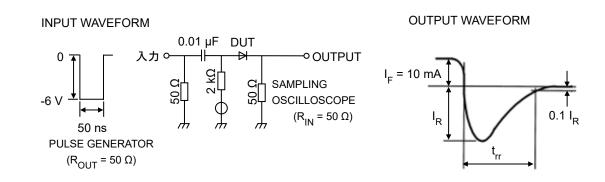
Note 1: For devices with the ordering part number ending in (TE85L,F).

Note 2: For devices with the ordering part number in other than (TE85L,F).

- Note 3: Total rating.
- \*: Unit rating. Total rating = Unit rating × 1.5.

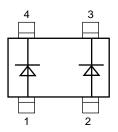
# Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Forward voltage	VF (1)	I <sub>F</sub> = 1 mA	_	0.61	_	V
	VF (2)	IF = 10 mA	_	0.74	_	
	VF (3)	I <sub>F</sub> = 100 mA	_	0.92	1.20	
Reverse current	I <sub>R (1)</sub>	V <sub>R</sub> = 30 V	-	_	0.1	μA
	IR (2)	VR = 80 V		—	0.5	
Total capacitance	CT	V <sub>R</sub> = 0 V, f = 1 MHz	_	0.9	2.0	pF
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 10 mA, Fig.1		1.6	4.0	ns

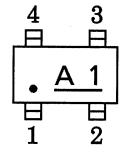


# Fig.1 Reverse Recovery Time (trr) Test Circuit

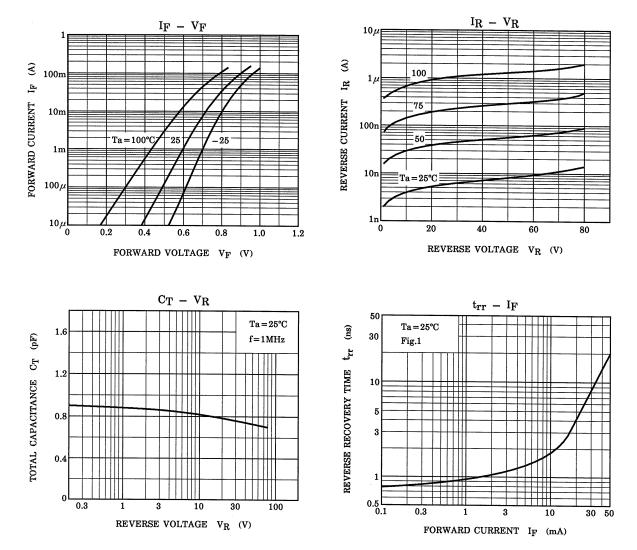
#### Pin Assignment (Top View)



Marking



## **Characteristics Curves**



The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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