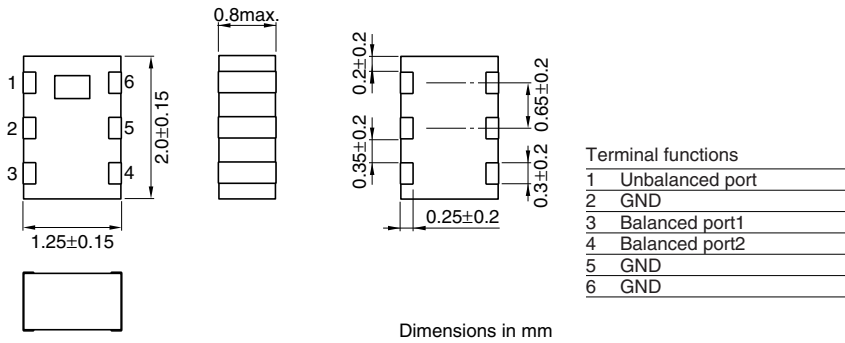


# Multilayer Chip Band Pass Filters(Balance Output Type) Conformity to RoHS Directive

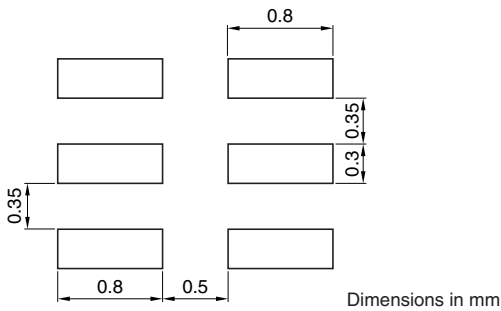
## For ZigBee

DEA Series DEA202450BT-7210A1

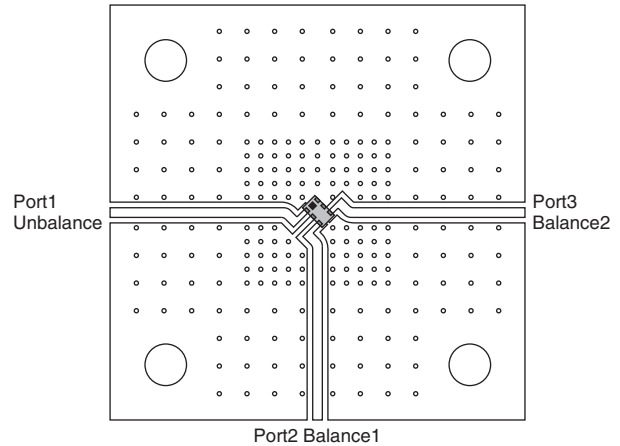
### SHAPES AND DIMENSIONS



### RECOMMENDED PC BOARD PATTERN



### EVALUATION BOARD



Line width is designed to match 50Ω characteristic impedance depending on PCB material and thickness.

Port extension value is 139.56ps for all port.

- Conformity to RoHS Directive: This means that, in conformity with EU Directive 2002/95/EC, lead, cadmium, mercury, hexavalent chromium, and specific bromine-based flame retardants, PBB and PBDE, have not been used, except for exempted applications.

- All specifications are subject to change without notice.

## ELECTRICAL CHARACTERISTICS

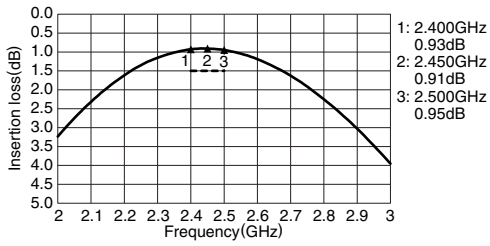
Item		Minimum value	Typical value	Maximum value
Unbalanced port characteristics impedance	( $\Omega$ )	50[Nominal]		
Balanced port characteristics impedance	( $\Omega$ )	Matched to TI CC253x series		
Insertion loss	[2400 to 2500MHz]	(dB)	—	0.95
	[4800 to 5000MHz]	(dB)	15	28
Differential mode attenuation	[7200 to 7500MHz]	(dB)	20	33
		(dB)	10	22
In/out return loss	(deg.)	180 $\pm$ 15	183	—
Phase difference at balanced port	(dB)	0 $\pm$ 2	-0.67	—
Amplitude imbalance at balanced port	Operating	( $^{\circ}$ C)	-40	—
Temperature range		Storage	( $^{\circ}$ C)	-40
				+85
				+85

• Ta:+25 $^{\circ}$ C

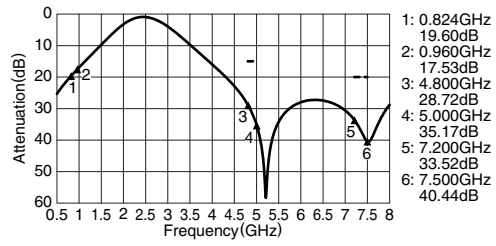
## FREQUENCY CHARACTERISTICS

### Unbalance 50Ω/Balance Matched to TI CC253x series

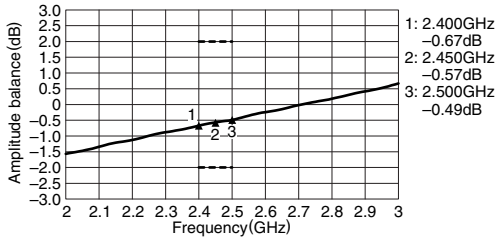
#### SDS21 INSERTION LOSS



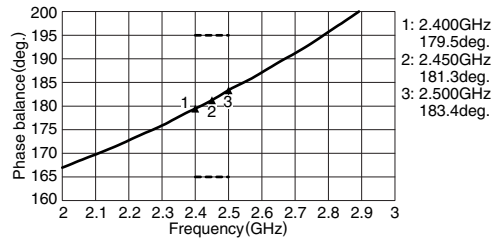
#### SDS21 ATTENUATION



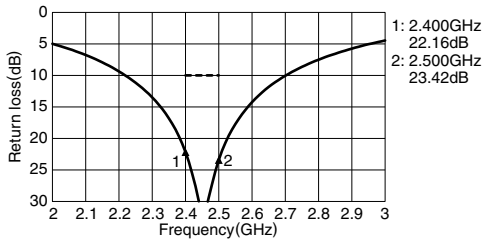
#### AMPLITUDE BALANCE



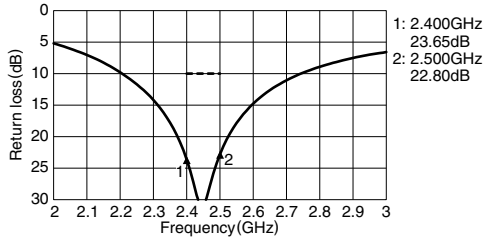
#### PHASE BALANCE



#### SSS11 UNBALANCE RETURN LOSS



#### SDD22 BALANCE RETURN LOSS



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Authorized Distributor

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