

Wireless LAN **Module Evaluation Board**

WBSACVLAY-XZ



To operate this evaluation board, need to sign a Software License Agreement with NXP Semiconductors and obtain SDK (Software Development Kit).

This evaluation board is an object for experiment of operation, and does not guarantee quality. Moreover, the conditions of a module of operation are not recommended in the schematic, the parts, the software, etc. currently used for evaluation board.

ATTENTION: Software related to this module may be under Japan export control. Depending on the customer's country and application (e.g. weapons), Taiyo Yuden may not be able to provide the software to all customers. Please contact your local Taiyo Yuden sales office for additional information.

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Rev. Records

17-Mar. 2020> Ver. 1.0 Released

WBSACVLAY-XZ

TAIYO YUDEN CO., LTD.

1. Introduction

The evaluation board is designed for customers to easily work with “WYSACVLAY-XZ”, **MCU embedded Wireless LAN** module, for functional evaluation purpose. This document describes the hardware configuration and the usage.

2. Target Module

WYSACVLAY-XZ

3. Accessories

1	Evaluation Board (WBSACVLAY-XZ)	1 piece
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4. Recommended Operating Environment

HOST PC:

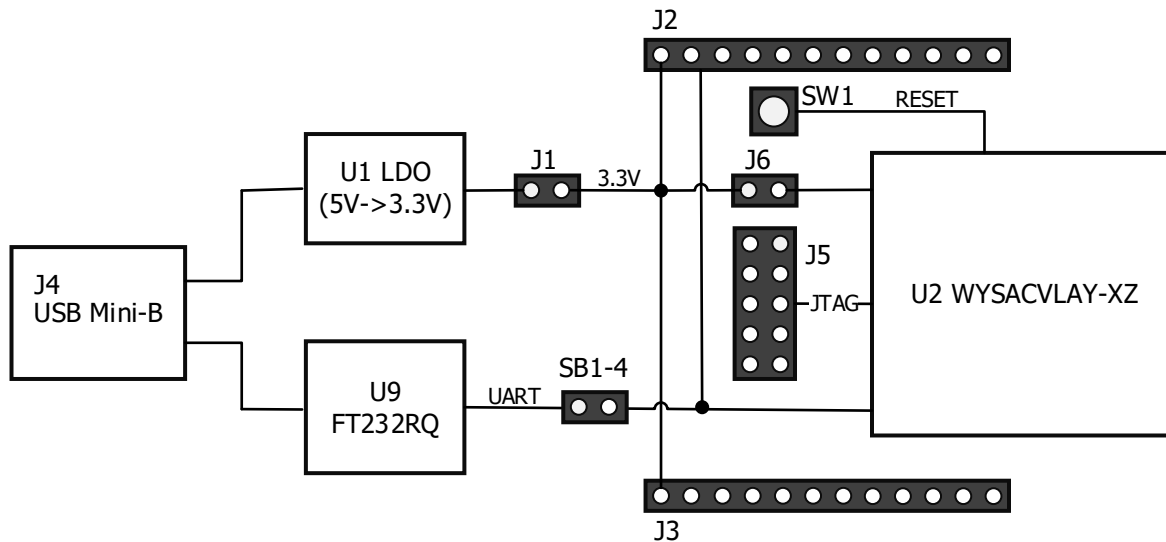
CPU: 1000MHz or higher

Interface: USB

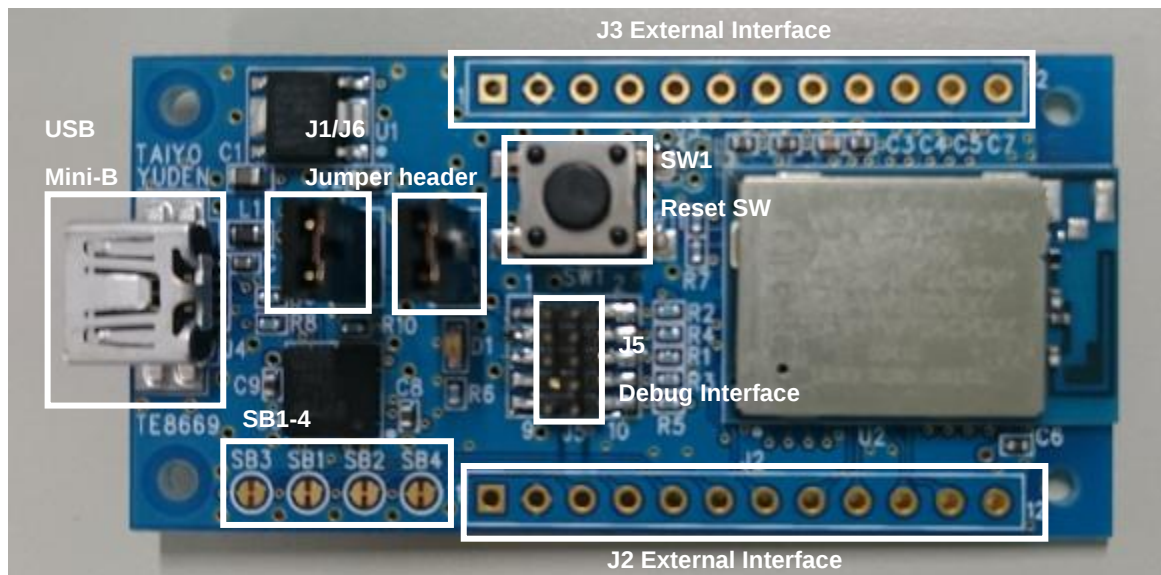
DC Power Supply: External power supply is not required since 5V power is supplied via USB connection.

5. Block Diagram

WBSACVLAY-XZ has several connectors. J4 (USB mini B connector) is used for USB signal that is converted from UART by FT232RQ. The embedded application running on WYSACVLAY uses UART as a host interface usually. Solder bridge port (SB1-4) are used to switch UART interfaces (U9 or J2). J1 and J6 are jumper connector to switch power sources (from LDO or from J2/J3).



6. Evaluation Board Layout



7. Pin Description of Evaluation Board

J2 External Interface

Pin#	Name	Direction	Description
1	EX3V3_PWR	-	Power Supply 3.3V
2	GND	-	Ground
3	UART1_TXD	Output	UART1 TXD signal
4	UART1_RTSN	Output	UART1 RTS signal
5	UART1_RXD	Input	UART1 RXD signal
6	UART1_CTSN	Input	UART1 CTS signal
7	GPIO41	Input/Output	
8	SPI_CLK	Input/Output	
9	SPI_SEL	Input/Output	
10	SPI_TXD	Output	
11	SPI_RXD	Input	
12	SPI_RDY	Input/Output	

J3 External Interface

Pin#	Name	Direction	Description
1	EX3V3_PWR	-	Power Supply 3.3V
2	GND	-	Ground
3	UART0_TXD	Output	UART0 TXD signal
4	UART0_RXD	Input	UART0 RXD signal
5	I2C_SDA	Input/Output	I2C0_SDA
6	I2C_SCL	Input/Output	I2C0_SCL
7	WAKEUP0	Input	
8	WAKEUP1	Input	
9	GPIO41	Input/Output	
10	NC_CON5	-	
11	NC_CON4	-	
12	RESET	Input	Reset signal (Active Low)

WBSACVLAY-XZ

TAIYO YUDEN CO., LTD.

J5 Debug Interface (not used by TAIYO YUDEN CO., LTD. standard application)

Pin#	Name	Direction	Description
1	EX3V3_PWR	-	Power (to monitor power supply voltage)
2	TMS	Input	JTAG TMS signal
3	GND	-	Ground
4	TCK	Input	JTAG TCK signal
5	GND	-	Ground
6	TDO	Output	JTAG TDO signal
7	NC	-	
8	TDI	Input	JTAG TDI signal
9	NC	-	
10	TRSTN	Input	JTAG target reset signal

Note: Please revise the board file to the following contents to operate the evaluation board.

1.

```
int board_32k_osc()
{
    return false;
}
```

2.

```
int board_rc32k_calib()
{
    return true;
}
```

3.

```
void board_uart_pin_config(int id)
{
    switch (id) {
        case UART0_ID:
            GPIO_PinMuxFun(GPIO_2, GPIO2_UART0_TXD);
            GPIO_PinMuxFun(GPIO_3, GPIO3_UART0_RXD);
            break;
        case UART1_ID:
            GPIO_PinMuxFun(GPIO_42, GPIO42_UART1_CTSn); // Not required without Flow control.
            GPIO_PinMuxFun(GPIO_43, GPIO43_UART1_RTSn); // Not required without Flow control.
    }
}
```

```
GPIO_PinMuxFun(GPIO_44, GPIO44_UART1_TXD);
GPIO_PinMuxFun(GPIO_45, GPIO45_UART1_RXD);
break;
case UART2_ID:
    /* Not implemented yet */
    break;
}
}
```

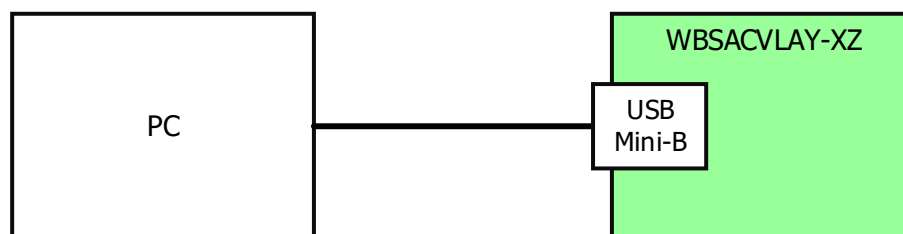
8. Software

- Device Drivers for FT232RQ:
FT232RQ USB-UART converter is built in this board. FT232RQ is made by Future Technology Devices International Limited. Download USB driver from following URL.
<http://www.ftdichip.com/>

9. Example of Connection for Evaluation

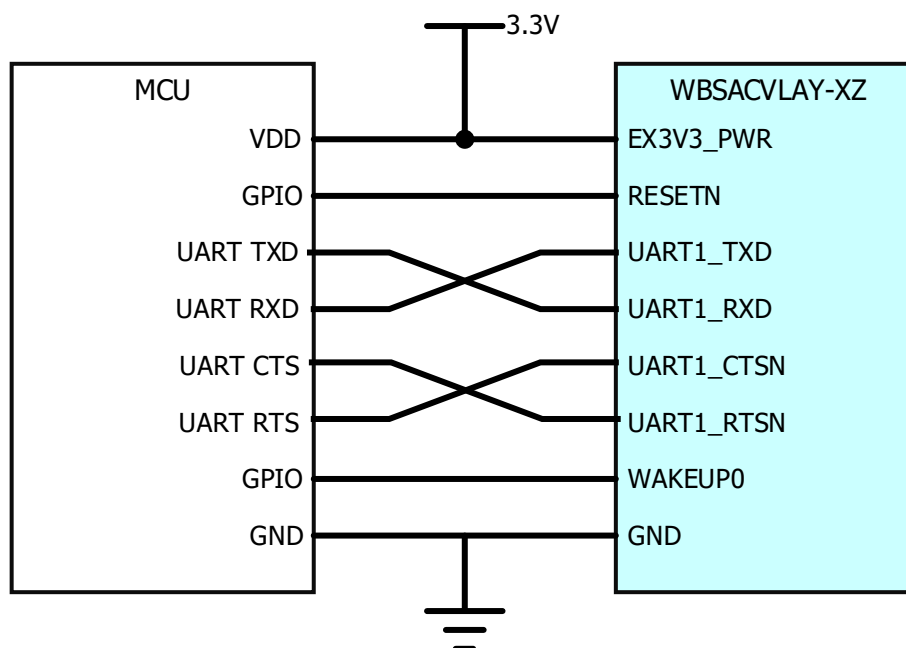
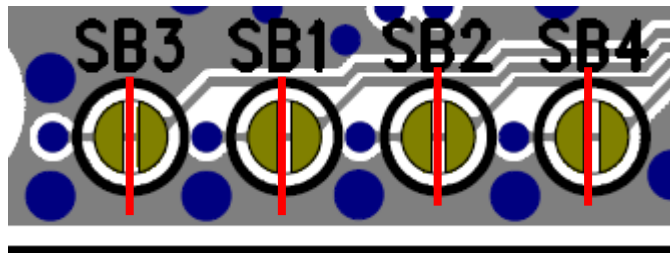
Case1. With PC

- Put jumper on J1 and J6 and connect USB cable
- Connect USB cable between PC and WBSACLVXY-1
- Run serial terminal software. Default baud rate of the module is 115.2kbps. Please configure baud rate of serial terminal software.



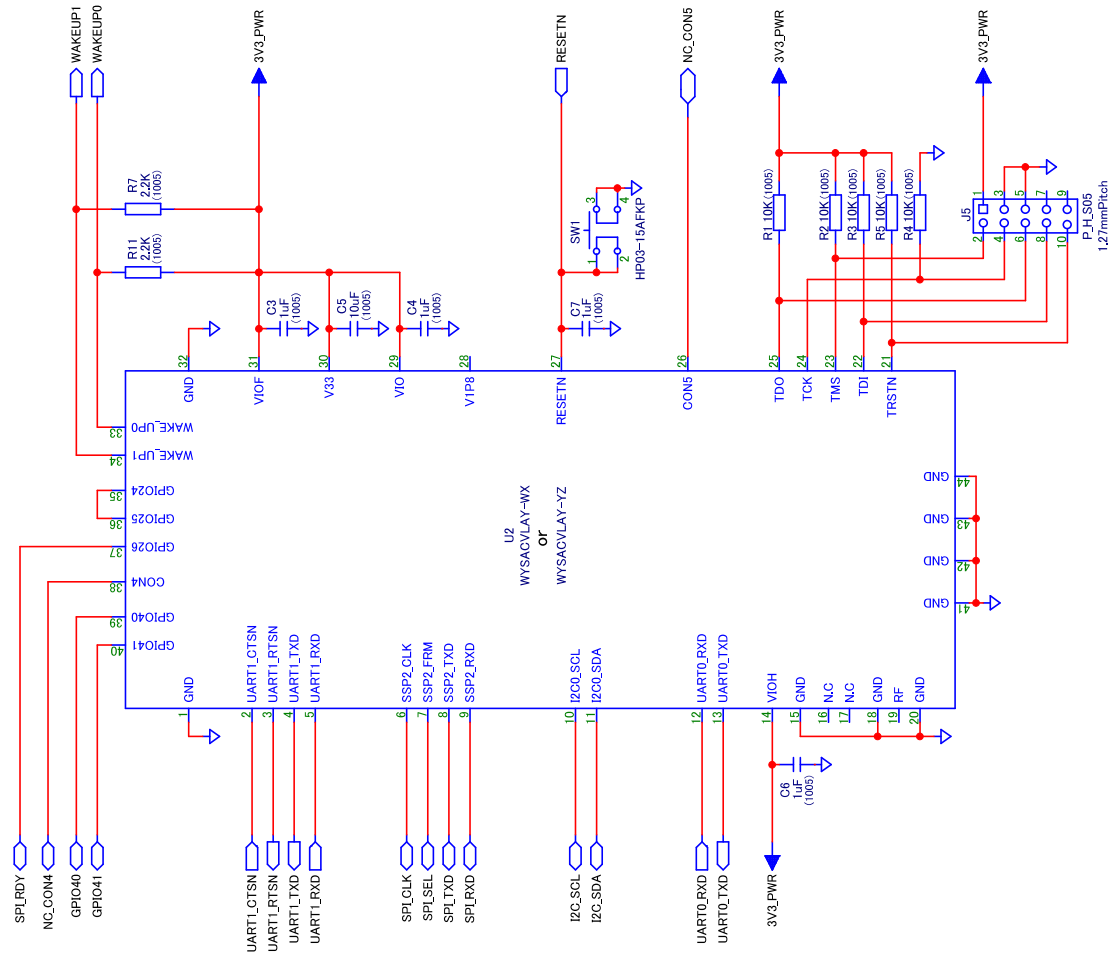
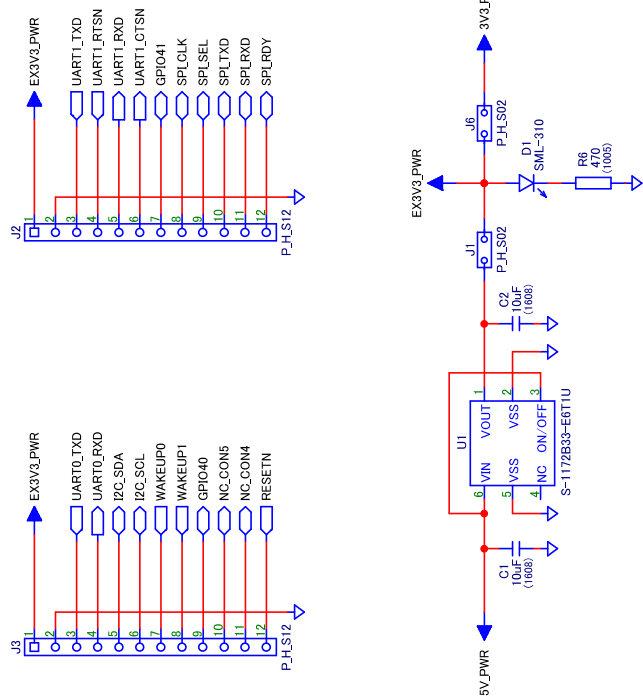
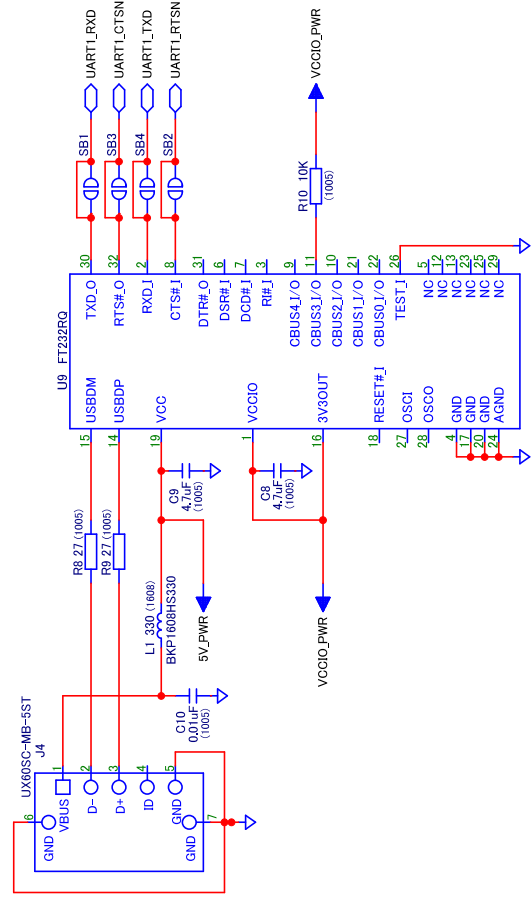
Case2. Using external interface (with other MCU)

- Cut the line between the land patterns of Solder Bridge SB1 – SB4 in order to disconnect the UART line of the module from FT232RQ.
- Put jumper on J6 only and supply power source (3.3V) to J2/J3 pin#1.
- **DO NOT** put jumper on J1 in this case. If do that, MCU and WBSACVLXY-1 may be broken.



10. Schematic of evaluation board

REV	ECO	APPROVED	DESCRIPTION	DATE



APPROVED	Checked	Drawn	Designed	Title	Document No.
				Circuit schematic	HD-MC-BTE8669
					(1/1)

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