

2.495V Programmable Shunt Voltage Reference

GENERAL DESCRIPTION

TS431 series integrated circuits are three-terminal programmable shunt regulator diodes. These monolithic IC voltage references operate as a low temperature coefficient zener which is programmable from V_{REF} to 36 volts with two external resistors. These devices exhibit a wide operating current range of 0.3 to 100mA with a typical dynamic impedance of 0.22 Ω .

The characteristics of these references make them excellent replacements for zener diodes in many applications such as digital voltmeters, power supplies, and op amp circuitry. The 2.495V reference makes it convenient to obtain a stable reference from 5.0V logic supplies, and since The TS431 series operates as a shunt regulator, it can be used as either a positive or negative stage reference.

FEATURES

- Precision Reference Voltage TS431A – 2.495V ±1% TS431B – 2.495V ±0.5%
- Equivalent Full Range Temp. Coefficient: 50ppm/°C
- Programmable Output Voltage up to 36V
- Fast Turn-On Response
- Sink Current Capability of 1~100mA
- Low Dynamic Output Impedance: 0.2Ω
- Low Output Noise
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

APPLICATION

- SMPS
- Lighting
- Telecommunication
- Home appliance



SOT-23



Pin Definition:

- 1. Reference
- 2. Cathode
- 3. Anode

SOP-8

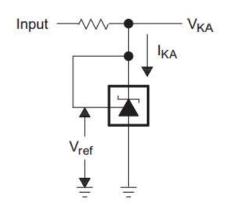


Pin Definition

- 1. Cathode 5. N/C
- 2. Anode 6. Anode
- 3. Anode 7. Anode
- 4. N/C 8. Reference

Notes: MSL 3 (Moisture Sensitivity Level) per J-STD-020

TYPICAL APPLICATIN CIRCUIT





ABSOLUTE MAXIMUM RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Cathode Voltage (Note)		VKA	37	V
Continuous Cathode Current Range		lκ	-100 ~ +150	mA
Reference Input Current Range		I _{REF}	-0.05 ~ +10	mA
B	SOT-23	P _D	0.30	347
Power Dissipation	SOP-8		0.50	W
Junction Temperature		TJ	+150	°C
Operating Temperature Range		T _{OPER}	-40 ~ +85	°C
Storage Temperature Range		TstG	-65 ~ +150	°C

RECOMMEND OPERATING CONDITION			
PARAMETER	SYMBOL	LIMIT	UNIT
Cathode Voltage	VKA	V _{REF} ~ 36	V
Continuous Cathode Current Range	lκ	1 ~ 100	mA

ELECTRICAL CHARACTERISTICS						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Reference voltage	TS431A		2.470	2.495	2.520	V
	TS431B	V _{REF}	2.483		2.507	
Deviation of reference input voltage	$V_{KA} = V_{REF}$, $I_{K} = 10$ mA $T_{A} = $ full range	ΔV_{REF}		8	17	mV
Radio of change in Vref to	of change in Vref to I_{KA} =10mA, ΔV_{KA} = 10V to V_{REF} ΔV_{KA} = 36V to 10V	ΔV_{REF}		-1.4	-2.7	
change in cathode Voltage		/ΔVκα		-1	-2	mV/V
Reference Input current	R1=10kΩ, R2=∞,	I _{REF}		0.7	4	μA
Deviation of reference input current, over temp.	R1=10kΩ, R2= ∞ , I _{KA} =10mA T _A = full range	Δlref		0.4	1.2	μА
Off-state Cathode Current	V _{REF} =0V	IKA (off)			1	μA
Minimum operating cathode current	V _{KA} = V _{REF}	I _{KA(min)}		0.4	0.6	mA
Dynamic Output Impedance	f<1kHz, V _{KA} = V _{REF} I _{KA} =1mA to 100mA	ZKA		0.22	0.5	Ω

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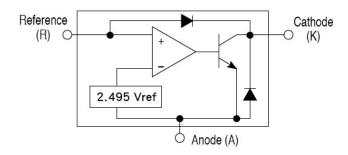
Note: Voltage values are with respect to the anode terminal unless otherwise noted.



ORDERING INFORMATION

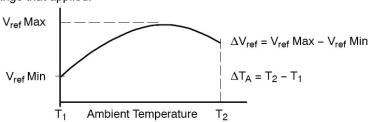
PART NO.	PACKAGE	PACKING
TS431ACX RFG	SOT-23	3,000pcs / 7" Reel
TS431BCX RFG	SOT-23	3,000pcs / 7" Reel
TS431ACS RLG	SOP-8	2,500pcs / 13" Reel
TS431BCS RLG	SOP-8	2,500pcs / 13" Reel

BLOCK DIAGRAM



- * The deviation parameters ΔV_{REF} and ΔI_{REF} are defined as difference between the maximum value and minimum value obtained over the full operating ambient temperature range that applied.
- * The average temperature coefficient of the reference input voltage, αV_{REF} is defined as:

$$\alpha V_{ref} \; \left(\frac{ppm}{^{\circ}C} \right) = \frac{\left(\frac{(\Delta V_{ref})}{V_{ref} \; (T_{A} = 25^{\circ}C)} \times 10^{6} \right)}{\Delta T_{A}}$$



Where: **T2-T1** = full temperature change.

 αV_{REF} can be positive or negative depending on whether the slope is positive or negative.

Example: Maximum V_{REF}=2.496V at 30°C, minimum V_{REF} =2.492V at 0°C, V_{REF} =2.495V at 25°C, ΔT=70°C

$$\alpha V_{REF}$$
 | = [4mV / 2495mV] * 10⁶ / 70°C ≈ 23ppm/°C

Because minimum V_{REF} occurs at the lower temperature, the coefficient is positive.

* The dynamic impedance ZKA is defined as:

$$|Z_{KA}| = \Delta V_{KA} / \Delta I_{KA}$$

* When the device operating with two external resistors, R1 and R2, (refer to Figure 2) the total dynamic impedance of the circuit is given by:

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$$|Z_{KA}| = \Delta v / \Delta i | \approx Z_{KA} | * (1 + R1 / R2)$$



ADDITIONAL INFORMATION – STABILITY

 $When \ TS431A/431B \ is \ used \ as \ a \ shunt \ regulator, \ there \ are \ two \ options \ for \ selection \ of \ C_L, \ are \ recommended \ for \ optional \ stability:$

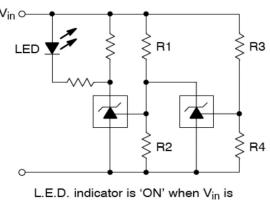
- A) No load capacitance across the device, decouple at the load.
- B) Large capacitance across the device, optional decoupling at the load.

The reason for this is that TS431A/431B exhibits instability with capacitances in the range of 10nF to 1 μ F (approx.) at light cathode current up to 3mA (typ). The device is less stable the lower the cathode voltage has been set for. Therefore while the device will be perfectly stable operating at a cathode current of 10mA (approx.) with a 0.1 μ F capacitor across it, it will oscillate transiently during start up as the cathode current passes through the instability region. Select a very low capacitance, or alternatively a high capacitance (10 μ F) will avoid this issue altogether. Since the user will probably wish to have local decoupling at the load anyway, the most cost-effective method is to use no capacitance at all directly across the device. PCB trace/via resistance and inductance prevent the local load decoupling from causing the oscillation during the transient startup phase.

Note: if the TS431A/431B is located right at the load, so the load decoupling capacitor is directly across it, then this capacitor will have to be ≤ 1 nF or ≥ 10 µF.

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APPLICATIONS EXAMPLES



L.E.D. indicator is 'ON' when V_{in} is between the upper and lower limits,

Lower limit =
$$\left(1 + \frac{R1}{R2}\right) V_{ref}$$

Upper limit = $\left(1 + \frac{R3}{R4}\right) V_{ref}$

Figure 1. Voltage Monitor

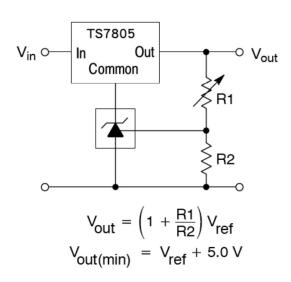


Figure 2. Output Control for Three Terminal Fixed Regulator



APPLICATIONS EXAMPLES (CONTINUE)

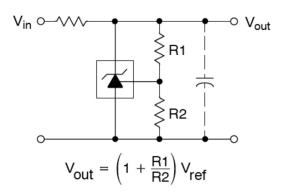


Figure 3. Shunt Regulator

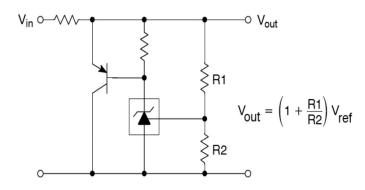


Figure 4. High Current Shunt Regulator

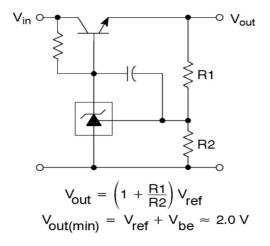


Figure 5. Series Pass Regulator

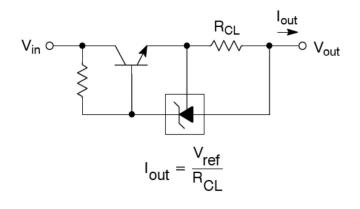


Figure 6. Constant Current Source

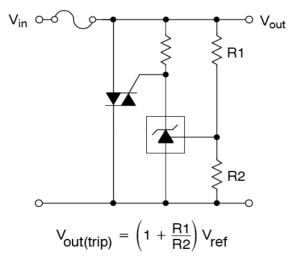


Figure 7. TRIAC Crowbar

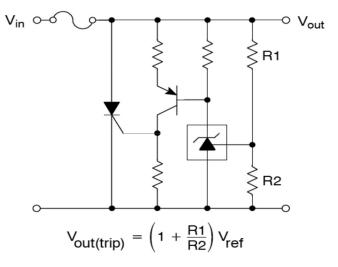
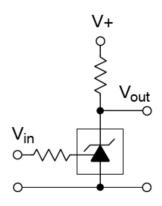


Figure 8. SCR Crowbar



APPLICATIONS EXAMPLES (CONTINUE)



Vin	Vout
<vref< td=""><td>V+</td></vref<>	V+
>Vref	≈0.74V

 V_{in} I_{sink} I_{sink} I_{sink} I_{sink}

Figure 9. Single-Supply Comparator with Temperature-Compensated Threshold

12 V R $2 \text{ k}\Omega$ CDelay = $R \times C \times I_n \left(\frac{12 \text{ V}}{12 \text{ V} - \text{V}_{ref}}\right)$

Figure 11. Delay Timer

Figure 10. Constant Current Sink

Version: I2104

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TYPICAL PERFORMANCE CHARACTERISTICS

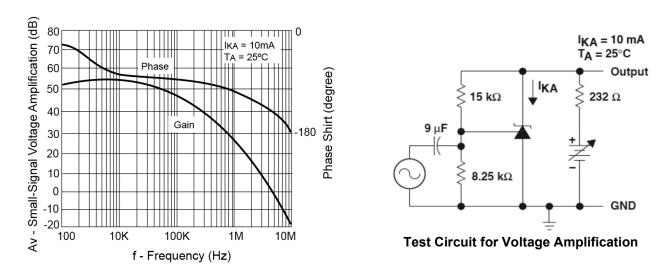


Figure 12. Small-Signal Voltage Gain and Phase Shirt vs. Frequency

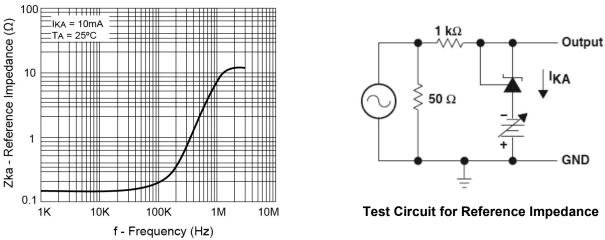
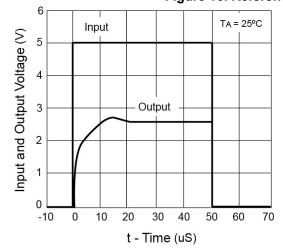
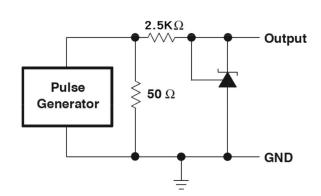


Figure 13. Reference Impedance vs. Frequency



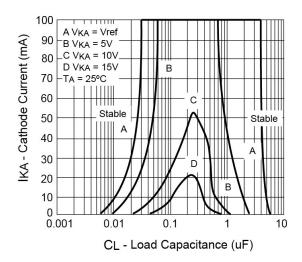


Test Circuit for Pulse Response, lk=1mA

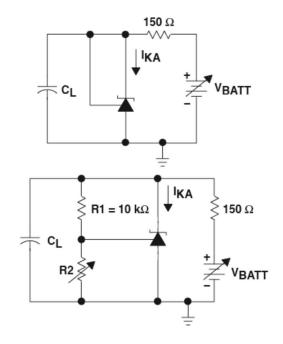
Figure 14. Pulse Response



TYPICAL PERFORMANCE CHARACTERISTICS (CONTINUE)



The areas under the curves represent conditions that may cause the device to oscillate. For curves B, C, and D, R2 and V+ were adjusted to establish the initial VKA and IKA conditions with CL=0. VBATT and CL then were adjusted to determine the ranges of stability.



Test Circuit for Curve B, C and D

Figure 15. Reference Impedance vs. Frequency

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CHARACTERISTICS CURVES

 $(T_C = 25^{\circ}C \text{ unless otherwise noted})$

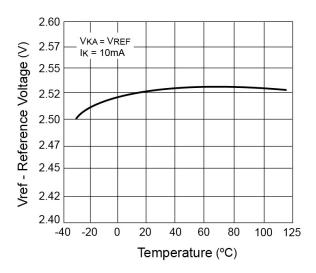


Figure 16. Reference Voltage vs. Temperature

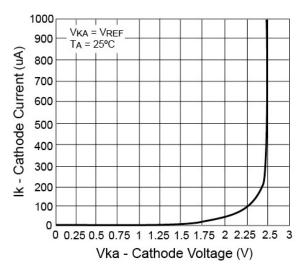


Figure 18. Cathode Current vs. Cathode Voltage

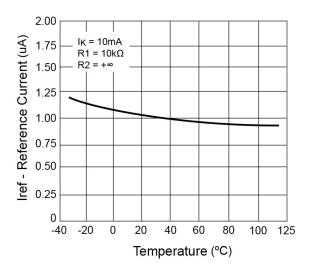


Figure 17. Reference Current vs. Temperature

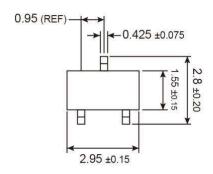
Version: I2104

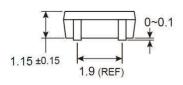
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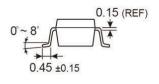


PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)

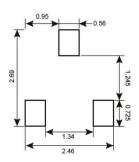
SOT-23







SUGGESTED PAD LAYOUT (Unit: Millimeters)



MARKING DIAGRAM



1 = Device Code

X = Tolerance Code

 $(A = \pm 1\%, B = \pm 0.5\%)$

Y = Year Code

M = Month Code for Halogen Free Product

O =Jan P =Feb Q =Mar R =Apr

S =May T =Jun U =Jul V =Aug

W = Sep X = Oct Y = Nov Z = Dec

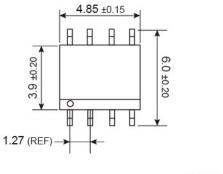
10

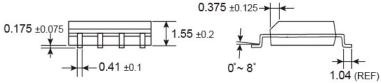
L = Lot Code



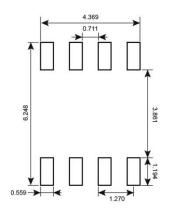
PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)

SOP-8





SUGGESTED PAD LAYOUT (Unit: Millimeters)



MARKING DIAGRAM



Y = Year Code

M = Month Code for Halogen Free Product

O =Jan P =Feb Q =Mar R =Apr S =May T =Jun U =Jul V =Aug

 \mathbf{W} =Sep \mathbf{X} =Oct \mathbf{Y} =Nov \mathbf{Z} =Dec

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L = Lot Code



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