

Surface Mount Rectifiers

FEATURES

- Glass passivated chip junction
- Ideal for automated placement
- Low forward voltage drop
- High surge current capability
- Moisture sensitivity level: level 1, per J-STD-020
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

Molding compound, UL flammability classification rating 94V-0

Base P/N with prefix "H" on packing code - AEC-Q101 qualified

Base P/N with suffix "G" on packing code - green compound (halogen-free)

MECHANICAL DATA

Case: DO-214AA (SMB)





1.5

40

10

- 55 to +150

- 55 to +150

S3

MB

1000

700

1000

Unit

V

V

V А

А

V

μΑ

μs

pF

^DC/W

°C

°C

DO-214AA (SMB)

Terminal: Matte tin plated leads, solderable per JESD22-B10 Meet JESD 201 class 1A whisker test with prefix "H" on packing code meet JESD 201 class 2 whisk Polarity: Indicated by cathode band Weight: 0.09 g (approximately)	er test	00 /T						
MAXIMUM RATINGS AND ELECTRICAL CHAR	MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS (T _A =25°C unless otherwise noted)S3S3S3S3S3S3S3							
PARAMETER	SYMBOL	AB	BB	DB	GB	JB	КВ	
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	200	400	600	800	
Maximum RMS voltage	V _{RMS}	35	70	140	280	420	560	
Maximum DC blocking voltage	V _{DC}	50	100	200	400	600	800	
Maximum average forward rectified current	I _{F(AV)}	3						
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	80						
Maximum instantaneous forward voltage (Note 1) @ 3 A	V _F	1.15						
Maximum reverse current @ rated VR T _J =25 $^\circ\!\!\mathbb{C}$	1	10						
T _J =125 ℃	I _R				250			

Trr

Cj

 $R_{\theta jL}$

 T_{J}

 $\mathsf{T}_{\mathsf{STG}}$

Storage temperature range

Typical reverse recovery time (Note 2)

Typical junction capacitance (Note 3)

Operating junction temperature range

Typical thermal resistance

Note 1: Pulse test with PW=300µs, 1% duty cycle Note 2: Reverse Recovery Test Conditions: I_F=0.5A, I_R=1.0A, I_{RR}=0.25A

Note 3: Measured at 1 MHz and Applied Reverse Voltage of 4.0V D.C.



S3AB thru S3MB

Taiwan Semiconductor

ORDERING INFORMATION					
PART NO.	AEC-Q101	PACKING CODE	GREEN COMPOUND	PACKAGE	PACKING
	QUALIFIED		CODE		
00D		R5		SMB	850 / 7" Plastic reel
S3xB (Note 1)	Prefix "H"	R4	Suffix "G"	SMB	3,000 / 13" Paper reel
		M4		SMB	3,000 / 13" Plastic reel

Note 1: "x" defines voltage from 50V (S3AB) to 1000V (S3MB)

EXAMPLE						
PREFERRED P/N	PART NO.	AEC-Q101 QUALIFIED	PACKING CODE	GREEN COMPOUND CODE	DESCRIPTION	
S3MB R5	S3MB		R5			
S3MB R5G	S3MB		R5	G	Green compound	
S3MBHR5	S3MB	Н	R5		AEC-Q101 qualified	

RATINGS AND CHARACTERISTICS CURVES

(TA=25°C unless otherwise noted)

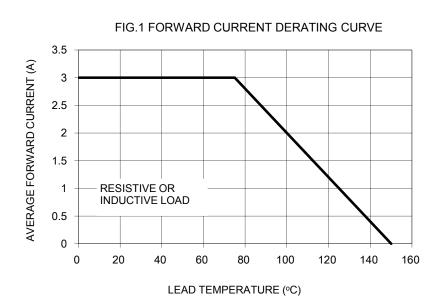
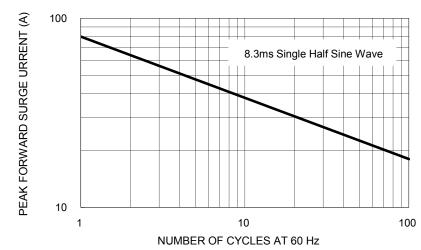


FIG. 3 MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT



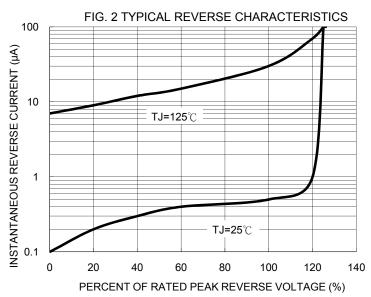
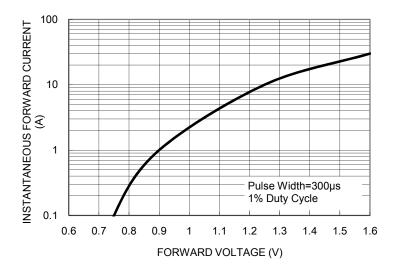


FIG. 4 TYPICAL FORWARD CHARACTERISTICS





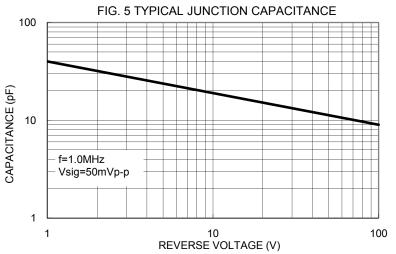
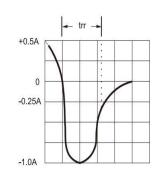
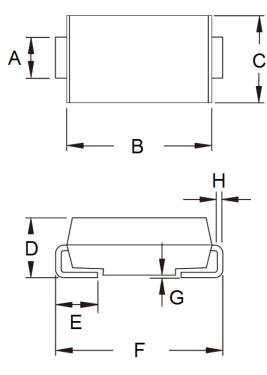


FIG.6- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

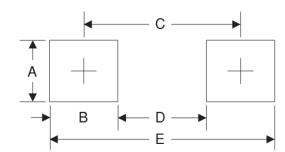


PACKAGE OUTLINE DIMENSIONS



DIM.	Unit (mm)		Unit (inch)		
Dilvi.	Min	Max	Min	Max	
А	1.95	2.10	0.077	0.083	
В	4.25	4.75	0.167	0.187	
С	3.48	3.73	0.137	0.147	
D	1.99	2.61	0.078	0.103	
E	0.90	1.41	0.035	0.056	
F	5.10	5.30	0.201	0.209	
G	0.10	0.20	0.004	0.008	
Н	0.15	0.31	0.006	0.012	

SUGGESTED PAD LAYOUT



P/N

G

F

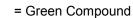
YW

Symbol	Unit (mm)	Unit (inch)
A	2.3	0.091
В	2.5	0.098
С	4.3	0.169
D	1.8	0.071
E	6.8	0.268

MARKING DIAGRAM



= Specific Device Code



- = Date Code
- = Factory Code

Document Number: DS_D1405060



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S3DB R5GS3BBHR4S3DBHR4S3GB R5GS3GBHR4S3GBHR4S3KBHR4S3MB R5GS3GBHR5S3ABHR4S3MBHR5S3BBHR5S3GB R4GS3JB R5GS3AB R5GS3DB R4GS3AB R4GS3DBHR5S3KB R4GS3BBR5GS3JBHR4S3JBHR5S3KBHR5S3ABHR5S3ABHR5S3BB R4GS3KB R5GS3JB R4GS3MB R4G