



Glass Passivated Bridge Rectifiers

FEATURES

- Ideal for printed circuit board
- Reliable low cost construction utilizing molded plastic technique
- High surge current capability
- UL Recognized File # E-326854
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition







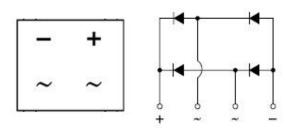
MECHANICAL DATA

Case: Molded plastic body

Molding compound, UL flammability classification rating 94V-0 Base P/N with suffix "G" on packing code - halogen-free **Terminal:** Matte tin plated leads, solderable per JESD22-B102

Meet JESD 201 class 1A whisker test **Polarity:** Polarity as marked on the body

Weight: 0.36 g (approximately)



DBL

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS (T _A =25°ℂ unless otherwise noted)									
PARAMETER	SYMBOL	DBL	DBL	DBL	DBL	DBL	DBL	DBL	UNIT
FARABILTER	STIVIBOL	101G	102G	103G	104G	105G	106G	107G	
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum average forward rectified current	I _{F(AV)}	1						Α	
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	I _{FSM} 40			30		Α		
Rating for fusing (t<8.3ms)	l ² t	6.64 3.73					73	A^2s	
Maximum instantaneous forward voltage (Note 1) $I_F = 1 \text{ A}$	V _F	1.1						V	
$\begin{array}{ll} \text{Maximum DC reverse current} & \text{T_J=25 °C} \\ \text{at rated DC blocking voltage} & \text{T_J=125°C} \end{array}$	I _R	2 500				μA			
Typical junction capacitance Per Leg (Note 2)	Cj	25						pF	
Typical thermal resistance	R _{θjL} R _{θjA}	15 40						°C/W	
Operating junction temperature range	T _J	- 55 to +150						οС	
Storage temperature range	T _{STG}	- 55 to +150						οС	

Note 1: Pulse Test with PW=300µs,1% Duty Cycle

Note 2: Measure at 1.0MHz and Applied Reverse Voltage of 4.0 Volts D.C.



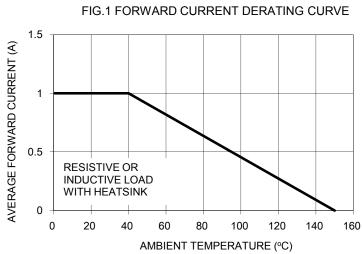
ORDERING INFORMATION							
PART NO.	PACKING CODE	GREEN COMPOUND	PACKAGE	PACKING			
		CODE					
DBL10xG (Note 1)	C1	Suffix "G"	DBL	50 / TUBE			

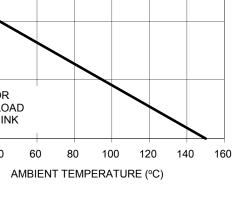
Note 1: "x" defines voltage from 50V (DBL101G) to 1000V (DBL107G)

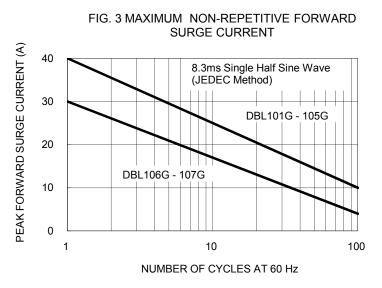
EXAMPLE							
PREFERRED P/N PART NO.		PACKING CODE	GREEN COMPOUND CODE	DESCRIPTION			
DBL107G C1	DBL107G	C1					
DBL107G C1G	DBL107G	C1	G	Green compound			

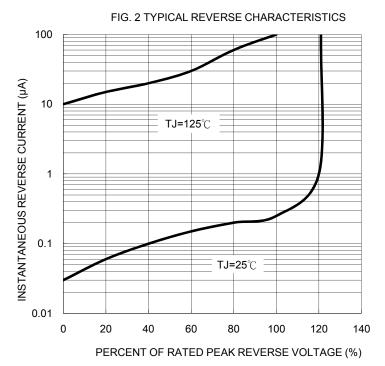
RATINGS AND CHARACTERISTICS CURVES

(TA=25 $^{\circ}$ C unless otherwise noted)









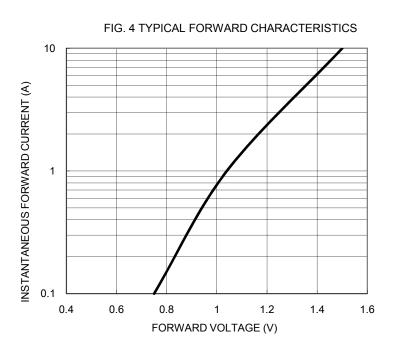
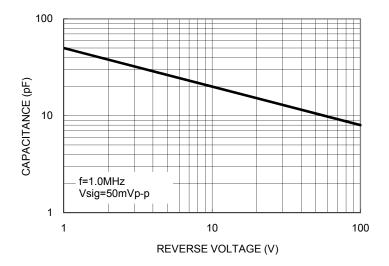
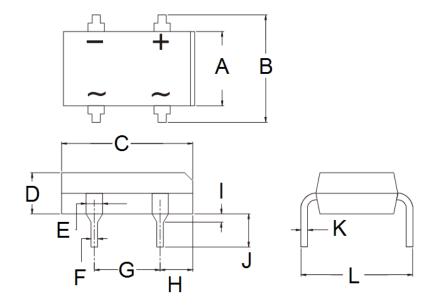




FIG. 5 TYPICAL JUNCTION CAPACITANCE



PACKAGE OUTLINE DIMENSIONS



DIM.	Unit	(mm)	Unit (inch)			
DIIVI.	Min	Max	Min	Max		
Α	6.20	6.50	0.244	0.256		
В	7.24	8.00	0.285	0.315		
С	8.12	8.51	0.320	0.335		
D	2.40	2.60	0.094	0.102		
E	0.89	1.14	0.035	0.045		
F	0.46	0.58	0.018	0.023		
G	5.00	5.20	0.197	0.205		
Н	1.39	1.90	0.055	0.075		
I	1.27	2.03	0.050	0.080		
J	3.81	4.69	0.150	0.185		
K	0.22	0.33	0.009	0.013		
L	7.60	8.90	0.299	0.350		

MARKING DIAGRAM



P/N = Specific Device Code

G = Green Compound

YW = Date Code

F = Factory Code

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Taiwan Semiconductor:

<u>DBL107G C1G</u> <u>DBL103G C1G</u> <u>DBL106G C1G</u> <u>DBL104G C1G</u> <u>DBL104G C1</u> <u>DBL101G C1</u> <u>DBL101G C1</u> <u>DBL103G C1</u> DBL105G C1