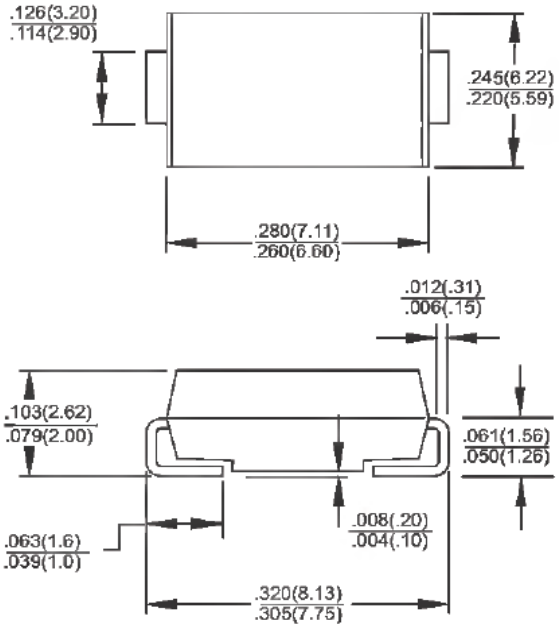
TAIWAN SEMICONDUCTOR

SSL32 - SSL34

3.0AMPS Surface Mount Schottky Barrier Rectifiers

SMC/DO-214AB



Features

- For surface mounted application
- Metal to sillicon rectifier, majority carrier conduction
- Low forward voltage drop
- Easy pick and place
- High surge current capability
- Plastic material used carriers Underwriters Laboratory Classification 94V-0
- Epitaxial construction
- High temperature soldering: 260℃/10 seconds at terminals
- Green compound with suffix "G" on packing code & prefix "G" on datecode

Mechanical Data

- Case: Molded plastic
- Terminals: Matte tin plating
- Polarity: Indicated by cathode band
- Packaging: 16mm tape per EIA STD RS-481
- Weight: 0.21 grams

Dimensions in inches and (millimeters)

Marking Diagram

SL3X = Specific Device Code
G = Green Compound
Y = Year
M = Work Month

Maximum Ratings and Electrical Characteristics

Rating at 25 ℃ ambient temperature unless otherwise specified.
Single phase, half wave, 60 Hz, resistive or inductive load.
For capacitive load, derate current by 20%

Type Number	Symbol	SSL32	SSL33	SSL34	Unit
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	20	30	40	V
Maximum RMS Voltage	V_{RMS}	14	21	28	V
Maximum DC Blocking Voltage	V_{DC}	20	30	40	V
Maximum Average Forward Rectified Current	$I_{F(AV)}$	3			A
Peak Forward Surge Current, 8.3 ms Single Half Sine-wave Superimposed on Rated Load	I_{FSM}	100			A
Maximum Instantaneous Forward Voltage (Note 1) @ 3 A	V_F	0.41			V
Maximum Reverse Current @ Rated VR T_A=25 ℃	I_R	0.2		0.5	mA
T_A=100 ℃		50		100	
Typical Thermal Resistance (Note 2)	$R_{\theta jL}$ $R_{\theta jA}$	17 55			°C/W
Marking Code		SL32	SL33	SL34	
Operating Temperature Range	T_J	- 55 to + 125			°C
Storage Temperature Range	T_{STG}	- 55 to + 150			°C

Note 1: Pluse Test with PW=300 usec, 1% Duty Cycle

Note 2: Measure on P.C.B. Board with 16mm x 16mm Copper Pad Areas

Version:D11

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RATINGS AND CHARACTERISTIC CURVES (SSL32 THRU SSL34)

FIG.1 FORWARD CURRENT DERATING CURVE

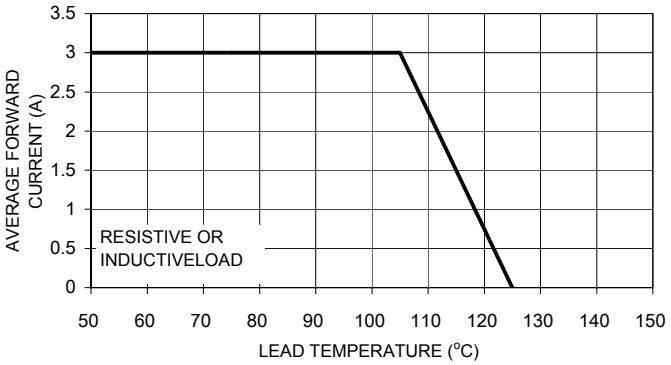


FIG. 2 MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

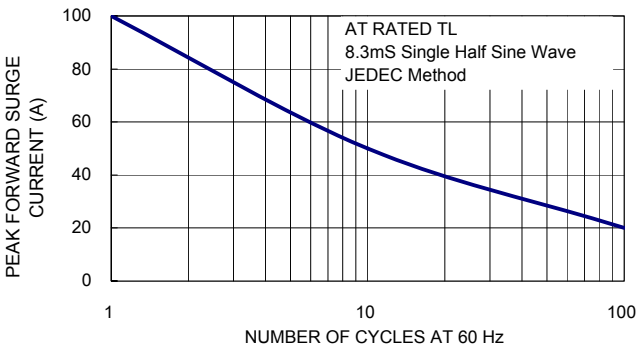


FIG. 3 TYPICAL FORWARD CHARACTERISRICS

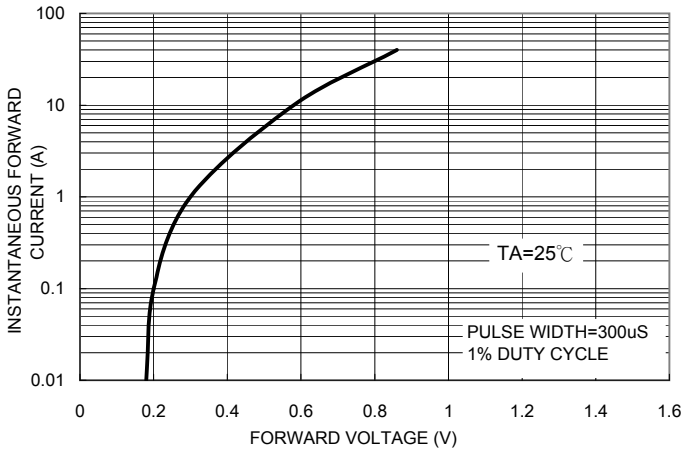


FIG. 4 TYPICAL REVERSE CHARACTERISTICS

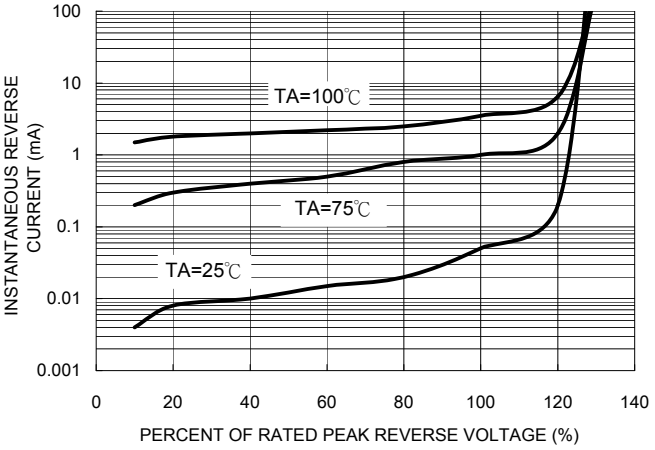
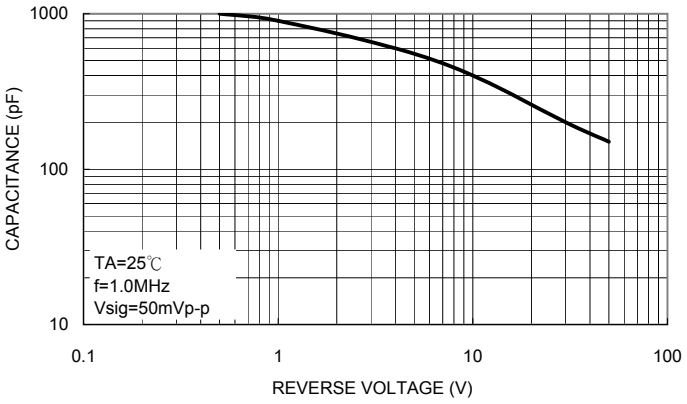


FIG. 5 TYPICAL JUNCTION CAPACITANCE



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