

6A, 200V - 600V Surface Mount Ultrafast Rectifiers

FEATURES

- Very low profile, typical height of 1.1mm
- Excellent high temperature stability
- Glass passivated chip junction
- Controled avalanche characteristics
- Low leakage current
- High forward surge capability
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21

TYPICAL APPLICATIONS

For use in high voltage, high frequency power factor corrections, switching mode power supplies, freewheeling diodes and secondary dc to dc rectifications

MECHANICAL DATA

Case: TO-277A (SMPC) Molding compound, UL flammability classification rating 94V-0 Moisture sensitivity level: level 1, per J-STD-020 Part no. with suffix "H" means AEC-Q101 qualified Packing code suffix "G" means green compound (halogen-free) Terminal: Matte tin plated leads, solderable per JESD22-B102 Meet JESD 201 class 1A whisker test Polarity: Indicated by cathode band Weight: 95 mg (approximately)

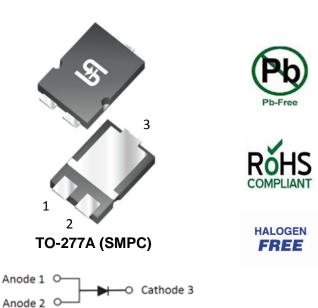
			CS (T _A =25°C	unless of	nerwise no	ted)		
PARAMETER				TPU	H6D	TPU	JH6J	UNIT
Marking code				UH6D UH6J				
Maximum repetitive peak reverse voltage				200 600			V	
Maximum average forward rectified current				6			А	
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load			I _{FSM}	80			A	
	Test	Test condition		TYP	MAX	TYP	MAX	
	I _F =3A	T _J =25°C	V _F	0.80	-	1.98	-	V
1)		T _J =125°C		0.65	-	1.23	-	
	I _F =6A	TJ=25°C	1	0.87	1.05	2.45	3.00	
		T _J =125°C		0.73	0.90	1.59	1.80	
Maximum reverse current @ rated V _B				10				
		T _J =125°C	- I _R	200			μA	
Maximum reverse $I_F=0.5A$, $I_R=1A$, $I_{RR}=0.25A$ ecovery time $I_F=1A$, di/dt=-50A/µs, $V_R=30V$			+	25			ns	
			- L _{rr}	45				
Typical thermal resistance			R _{0JM} ⁽²⁾	12			°C/W	
			$R_{\theta JA}^{(3)}$	80				
Typical junction capacitance ⁽⁴⁾			CJ	50			pF	
Operating junction temperature range				- 55 to +175			°C	
Storage temperature range			T _{STG}	- 55 to +175			°C	
1	half sine	half sine-wave Test $I_F=3A$ $I_F=6A$ $I_F=6A$	half sine-wave Test condition $I_F=3A$ $T_J=25^{\circ}C$ $T_J=125^{\circ}C$ $T_J=125^{\circ}C$ $T_J=125^{\circ}C$ $T_J=125^{\circ}C$ $T_J=125^{\circ}C$ $T_J=125^{\circ}C$ $T_J=125^{\circ}C$ $T_J=125^{\circ}C$ $T_J=125^{\circ}C$	half sine-wave I_{FSM} I_{FSM} $I_{F}=3A \qquad T_{J}=25^{\circ}C \qquad T_{J}=125^{\circ}C \qquad V_{F}$ $I_{F}=6A \qquad T_{J}=25^{\circ}C \qquad V_{F}$ $I_{F}=6A \qquad T_{J}=25^{\circ}C \qquad T_{J}=125^{\circ}C \qquad I_{R}$ $T_{J}=125^{\circ}C \qquad I_{R}$ $F=0.5A, I_{R}=1A, I_{RR}=0.25A \qquad I_{R}$ $F=1A, di/dt=-50A/\mu s, V_{R}=30V \qquad I_{rr}$ $R_{\theta JM}^{(2)}$ $R_{\theta JA}^{(3)}$ C_{J} T_{J}	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $

Note 1: Pulse test with PW=300µs, 1% duty cycle

Note 2: Mounted on FR4 PCB with 16mm x 16mm Cu pad area

Note 3: Free air, mounted on recommned pad

Note 4: Measured at 1 MHz and Applied V_R =4.0 V





Taiwan Semiconductor

ORDERING INFORMATION						
PART NO.	PART NO.	PACKING	PACKING CODE	PACKAGE	PACKING	
FART NO.	SUFFIX	CODE	SUFFIX	FACKAGE	FACKING	
TPUH6x	Н	S1	G	SMPC	1,500/ 7" Plastic reel	
(Note 1, 2)	11	S2	9	SMPC	6,000/ 13" Plastic reel	

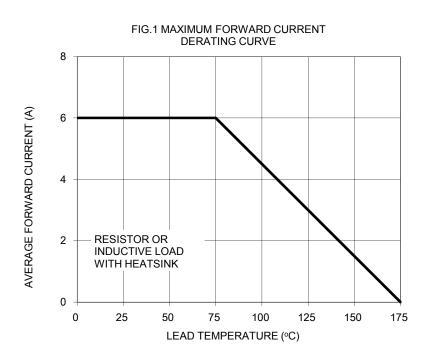
Note 1: "x" defines voltage from 200V (TPUH6D) to 600V (TPUH6J)

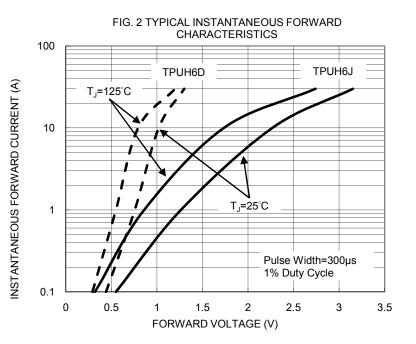
Note 2: Whole series with green compound

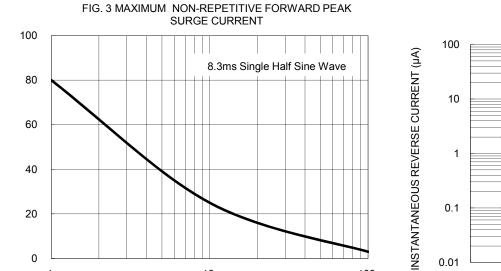
EXAMPLE					
PREFERRED PART NO.	PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION
TPUH6JHS1G	TPUH6J	Н	S1	G	AEC-Q101 qualified Green compound

RATINGS AND CHARACTERISTICS CURVES

(T_A=25°C unless otherwise noted)





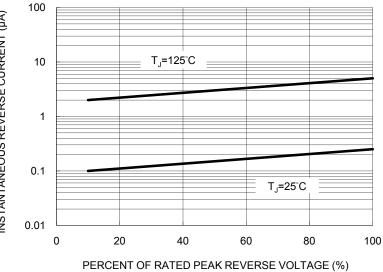


100

10

NUMBER OF CYCLES AT 60 Hz





Document Number: DS_D1411082

1

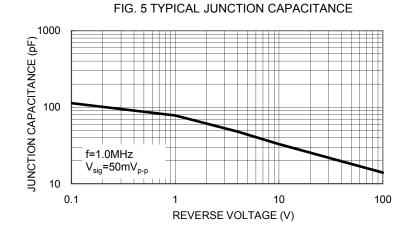
PEAK FORWARD SURGE CURRENT (A)



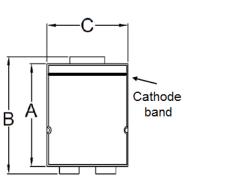
TPUH6D - TPUH6J

Taiwan Semiconductor

FIG. 5 TYPICAL JUNCTION CAPACITANCE

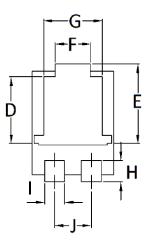


PACKAGE OUTLINE DIMENSIONS TO-277A (SMPC)

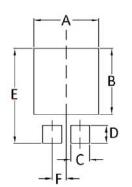




DIM.	Unit	(mm)	Unit (inch)		
Diwi.	Min Max		Min	Max	
А	5.650	5.750	0.222	0.226	
В	6.350	6.650	0.250	0.262	
С	4.550	4.650	0.179	0.183	
D	3.540	3.840	0.139	0.151	
E	4.235	4.535	0.167	0.179	
F	1.850	2.150	0.073	0.085	
G	3.170	3.470	0.125	0.137	
Н	1.043	1.343	0.041	0.053	
I	1.000	1.300	0.039	0.051	
J	1.930	2.230	0.076	0.088	
К	0.175	0.325	0.007	0.013	
L	1.000	1.200	0.039	0.047	



SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)		
Α	4.80	0.189		
В	4.72	0.186		
С	1.40	0.055		
D	1.27	0.050		
E	6.80	0.268		
F	1.04	0.041		

MARKING DIAGRAM

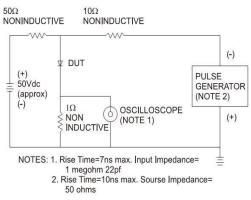


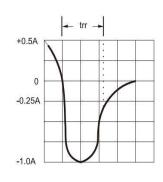
- = Marking Code
- = Date Code
 - = Factory Code

Document Number: DS_D1411082

FIG.6 REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

10Ω NONINDUCTIVE







Notice

Specifications of the products displayed herein are subject to change without notice. TSC or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.

Information contained herein is intended to provide a product description only. No license, express or implied, to any intellectual property rights is granted by this document. Except as provided in TSC's terms and conditions of sale for such products, TSC assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of TSC products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify TSC for any damages resulting from such improper use or sale.

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Taiwan Semiconductor:TPUH6D S2GTPUH6DTPUH6JTPUH6D S2GTPUH6DTPUH6JH