

# 20A, 50V - 600V Glass Passivated Super Fast Rectifiers

### **FEATURES**

- High efficiency, low VF
- High current capability
- High surge current capability
- Low power loss
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21







### **MECHANICAL DATA**

Case: TO-220AB

Molding compound: UL flammability classification rating 94V-0

Part no. with suffix "H" means AEC-Q101 qualified

Packing code with suffix "G" means green compound (halogen-free) **Terminal:** Matte tin plated leads, solderable per JESD22-B102

Meet JESD 201 class 2 whisker test

Polarity: As marked

**Mounting torque:** 0.56 Nm max. **Weight:** 1.82 g (approximately)

PIN 1 O-	PIN 2
PIN 3 O	CASE

**TO-220AB** 

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS (T <sub>A</sub> =25°C unless otherwise noted)										
		SF	SF	SF	SF	SF	SF	SF	SF	
PARAMETER	SYMBOL	2001	2002	2003	2004	2005	2006	2007	2008	UNIT
		G	G	G	G	G	G	G	G	
Maximum repetitive peak reverse voltage	$V_{RRM}$	50	100	150	200	300	400	500	600	V
Maximum RMS voltage	$V_{RMS}$	35	70	105	140	210	280	350	480	V
Maximum DC blocking voltage	$V_{DC}$	50	100	150	200	300	400	500	600	V
Maximum average forward rectified current	I <sub>F(AV)</sub>	20						Α		
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	150				А				
Maximum instantaneous forward voltage (Note 1) @ 10 A	V <sub>F</sub>	0.975 1.3 1.7			.7	٧				
Maximum reverse current @ rated $V_R$ $T_J$ =25°C $T_J$ =125°C	I <sub>R</sub>	5 400				μA				
Maximum reverse recovery time (Note 2)	t <sub>rr</sub>	t <sub>rr</sub> 35			ns					
Typical junction capacitance (Note 3)	CJ	80						pF		
Typical thermal resistance	$R_{\theta JC}$	2.5						°C/W		
Operating junction temperature range	T <sub>J</sub>	- 55 to +150					°C			
Storage temperature range	T <sub>STG</sub>	- 55 to +150					°C			

Note 1: Pulse test with PW=300µs, 1% duty cycle

Note 2: Test conditions:  $I_F$ =0.5A,  $I_R$ =1.0A,  $I_{RR}$ =0.25A

Note 3: Measured at 1 MHz and applied reverse voltage of 4.0 V DC.



ORDERING INFORMATION							
PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX <sup>(*)</sup>	PACKAGE	PACKING		
SF200xG (Note 1)	Н	C0	G	TO-220AB	50 / Tube		

Note 1: "x" defines voltage from 50V (SF2001G) to 600V (SF2008G)

<sup>\*:</sup> Optional available

EXAMPLE					
EXAMPLE PART NO.	PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION
SF2008GHC0G	SF2008G	Н	C0	G	AEC-Q101 qualified Green compound

### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub>=25°C unless otherwise noted)

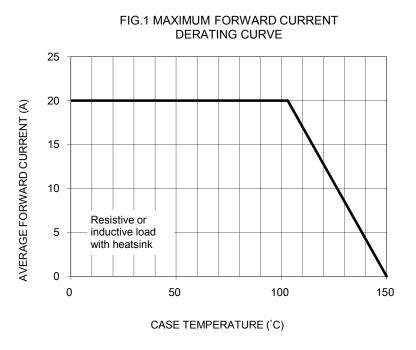
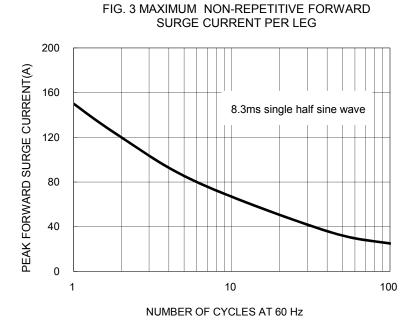


FIG. 2 TYPICAL REVERSE CHARACTERISTICS PER LEG 100 INSTANTANEOUS REVERSE CURRENT.(µA) 10 T<sub>J</sub>=100°C 1  $T_J = 75^{\circ}C$ T<sub>J</sub>=25°C 0.1 0 20 40 60 80 100 120 140



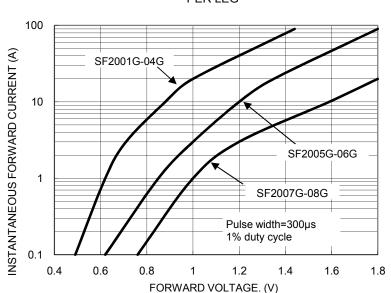


FIG. 4 TYPICAL FORWARD CHARACTERISTICS PER LEG

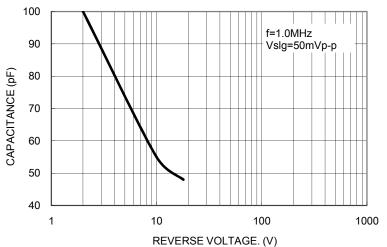
PERCENT OF RATED PEAK REVERSE VOLTAGE (%)

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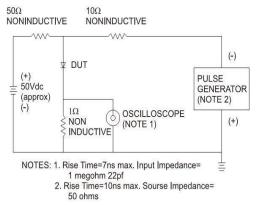


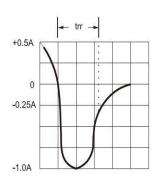


### FIG. 5 TYPICAL JUNCTION CAPACITANCE PER LEG

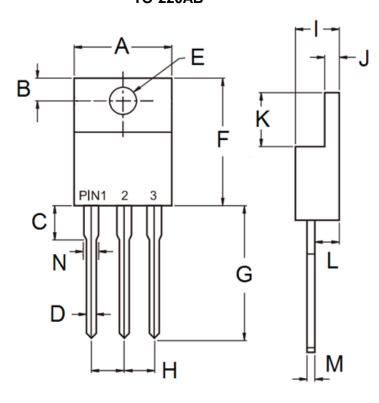


#### FIG.6 REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM





# PACKAGE OUTLINE DIMENSIONS TO-220AB



DIM.	Unit	(mm)	Unit (inch)		
DIIVI.	Min	Max	Min	Max	
Α	-	10.50	-	0.413	
В	2.62	3.44	0.103	0.135	
С	2.80	4.20	0.110	0.165	
D	0.68	0.94	0.027	0.037	
E	3.54	4.00	0.139	0.157	
F	14.60	16.00	0.575	0.630	
G	13.19	14.79	0.519	0.582	
Н	2.41	2.67	0.095	0.105	
I	4.42	4.76	0.174	0.187	
J	1.14	1.40	0.045	0.055	
K	5.84	6.86	0.230	0.270	
L	2.20	2.80	0.087	0.110	
М	0.35	0.64	0.014	0.025	
N	1.14	1.77	0.045	0.070	

## **MARKING DIAGRAM**



P/N = Specific Device Code
G = Green Compound
YWW = Date Code
F = Factory Code

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### Taiwan Semiconductor:

<u>SF2001G SF2002G SF2003G SF2004G SF2005G SF2006G SF2007G SF2008G SF2002G C0 SF2007G C0 SF2005G C0 SF2001G C0 SF2003G C0 SF2008G C0 SF2004G C0 SF2006G C0 SF2004G C0 SF2006G C0 S</u>