## 1.2A, 200V - 1000V Standard Surface Mount Rectifier

#### **FEATURES**

- Ideal for automated placement
- Compact package size
- High surge current capability
- Low power loss, high efficiency
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

#### **APPLICATIONS**

- DC to DC converter
- Switching mode converters and inverters
- General purpose

#### **MECHANICAL DATA**

- Case: SOD-123HE
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.022g (approximately)

KEY PARAMETERS			
PARAMETER	VALUE	UNIT	
I <sub>F</sub>	1.2	А	
V <sub>RRM</sub>	200 - 1000	V	
I <sub>FSM</sub>	50 A		
T <sub>J MAX</sub>	175 °C		
Package	SOD-123HE		
Configuration	Single die		









ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)							
PARAMETER	SYMBOL	S1DLS	S1GLS	S1JLS	S1KLS	S1MLS	UNIT
Marking code on the device		1DLS	1GLS	1JLS	1KLS	1MLS	
Repetitive peak reverse voltage	$V_{RRM}$	200	400	600	800	1000	V
Reverse voltage, total rms value	V <sub>R(RMS)</sub>	140	280	420	560	700	V
Forward current	I <sub>F</sub>	1.2		А			
Peak forward surge current, 8.3ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	50			А		
Junction temperature	TJ	- 55 to +175		°C			
Storage temperature	T <sub>STG</sub>	- 55 to +175		°C			



THERMAL PERFORMANCE				
PARAMETER	SYMBOL	ТҮР	UNIT	
Junction-to-lead thermal resistance	R <sub>ƏJL</sub>	46	°C/W	
Junction-to-ambient thermal resistance	R <sub>θJA</sub>	86	°C/W	
Junction-to-case thermal resistance	R <sub>θJC</sub>	50	°C/W	

Thermal Performance Note: Units mounted on PCB (5mm x 5mm Cu pad test board)

ELECTRICAL SPECIFICATIONS ( $T_A = 25^{\circ}C$ unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
Forward voltage <sup>(1)</sup>	I <sub>F</sub> = 1.2A, T <sub>J</sub> = 25°C	V <sub>F</sub>	-	1.3	V
Reverse current @ rated $V_R^{(2)}$	$T_J = 25^{\circ}C$	I <sub>R</sub>	-	5	μA
	T <sub>J</sub> = 125°C		-	150	μA

#### Notes:

1. Pulse test with PW = 0.3ms

2. Pulse test with PW = 30ms

### ORDERING INFORMATION

ORDERING CODE <sup>(1)</sup>	PACKAGE	PACKING
S1xLS	SOD-123HE	10,000 / Tape & Reel

Notes:

1. "x" defines voltage from 200V(S1DLS) to 1000V(S1MLS)



#### **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

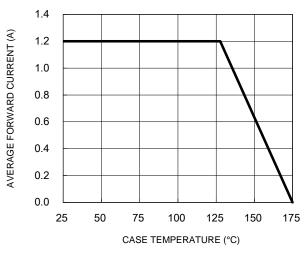
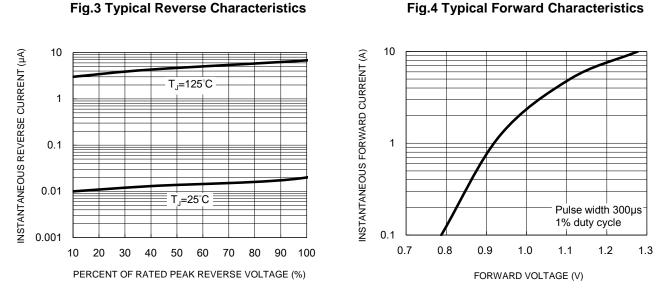


Fig.1 Forward Current Derating Curve

#### Fig.3 Typical Reverse Characteristics



100

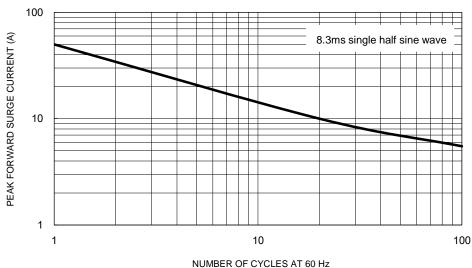
10

1

0.1

CAPACITANCE (pF)

#### Fig.5 Maximum Non-Repetitive Forward Surge Current



**Fig.2 Typical Junction Capacitance** 

10

REVERSE VOLTAGE (V)

100

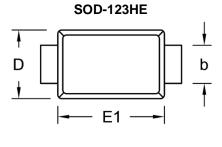
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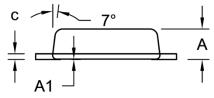
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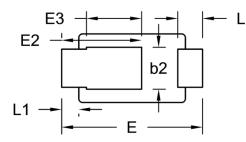
f=1.0MHz Vsig=50mVp-p

## PACKAGE OUTLINE DIMENSIONS

**5** TAIWAN SEMICONDUCTOR

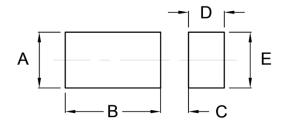






DIM.	Unit (mm)		Unit	(inch)	
	Min.	Max.	Min.	Max.	
A	0.75	0.85	0.030	0.033	
A1	0.00	0.02	0.000	0.001	
b	0.85	1.15	0.033	0.045	
b2	0.95	1.25	0.037	0.049	
с	0.10	0.20	0.004	0.008	
D	1.65	1.95	0.065	0.077	
E	3.50	3.90	0.138	0.154	
E1	2.60	3.00	0.102	0.118	
E2	1.90	2.30	0.075	0.091	
E3	1.35	1.55	0.053	0.061	
L	0.55	0.75	0.022	0.030	
L1	0.35	0.55	0.014	0.022	

#### SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	1.40	0.055
В	2.40	0.094
С	0.70	0.028
D	0.90	0.035
E	1.40	0.055

#### **MARKING DIAGRAM**



YW = Date Code

F = Factory Code



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