

# 1A, 50V - 1000V Standard Surface Mount Rectifier

#### **FEATURES**

- Glass passivated chip junction
- Ideal for automated placement
- Low profile package
- Low power loss, high efficiency
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

#### **APPLICATIONS**

- DC to DC converter
- Switching mode converters and inverters
- General purpose

#### **MECHANICAL DATA**

- Case: Sub SMA
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.019g (approximately)

KEY PARAMETERS				
PARAMETER	VALUE	UNIT		
I <sub>F</sub>	1	Α		
$V_{RRM}$	50 - 1000	V		
I <sub>FSM</sub>	30	Α		
T <sub>J MAX</sub>	175	°C		
Package	Sub SMA			
Configuration	Single die			









Sub SMA



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)									
PARAMETER	SYMBOL	S1AL	S1BL	S1DL	S1GL	S1JL	S1KL	S1ML	UNIT
Marking code on the device		1AL	1BL	1DL	1GL	1JL	1KL	1ML	
Repetitive peak reverse voltage	$V_{RRM}$	50	100	200	400	600	800	1000	V
Reverse voltage, total rms value	$V_{R(RMS)}$	35	70	140	280	420	560	700	V
Forward current	I <sub>F</sub>	1		Α					
Peak forward surge current, 8.3ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	FSM 30			А				
Junction temperature	$T_J$	T <sub>J</sub> - 55 to +175			°C				
Storage temperature	T <sub>STG</sub>	- 55 to +175			°C				



# S1AL – S1ML Taiwan Semiconductor

THERMAL PERFORMANCE				
PARAMETER	SYMBOL	TYP	UNIT	
Junction-to-lead thermal resistance <sup>(1)</sup>	R <sub>OJL</sub>	25	°C/W	
Junction-to-lead thermal resistance <sup>(2)</sup>	R <sub>OJL</sub>	30	°C/W	
Junction-to-ambient thermal resistance	R <sub>OJA</sub>	85	°C/W	

#### Notes:

1. Part number: S1AL - S1JL 2. Part number: S1KL - S1ML

ELECTRICAL SPECIFICATIONS (T <sub>A</sub> = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	TYP	MAX	UNIT
Forward voltage <sup>(1)</sup>	I <sub>F</sub> = 1A, T <sub>J</sub> = 25°C	$V_{F}$	-	1.1	V
Reverse current @ rated V <sub>R</sub> <sup>(2)</sup>	T <sub>J</sub> = 25°C		-	5	μA
	T <sub>J</sub> = 125°C	- I <sub>R</sub>	-	50	μA
Junction capacitance	1MHz, V <sub>R</sub> = 4.0V	CJ	9	-	pF
Reverse recovery time	$I_{rr} = 0.5A, I_{R} = 1.0A,$ $I_{rr} = 0.25A$	t <sub>rr</sub>	1800	-	ns

#### Notes:

1. Pulse test with PW = 0.3ms

2. Pulse test with PW = 30ms

ORDERING INFORMATION			
ORDERING CODE <sup>(1)</sup>	PACKAGE	PACKING	
S1xL	Sub SMA	10,000 / Tape & Reel	

## Notes:

1. "x" defines voltage from 50V(S1AL) to 1000V(S1ML)



#### **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

Fig.1 Forward Current Derating Curve

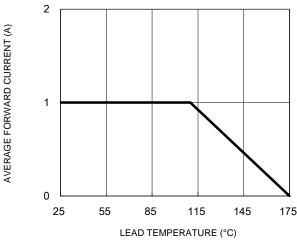


Fig.3 Typical Reverse Characteristics

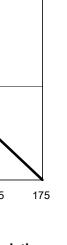


Fig.2 Typical Junction Capacitance

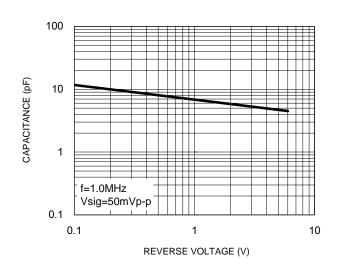
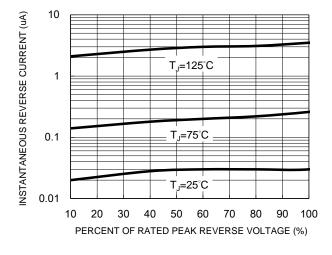


Fig.4 Typical Forward Characteristics



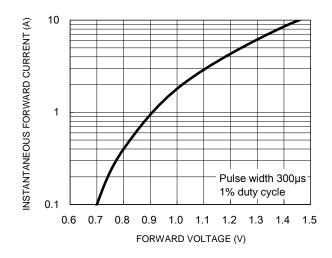
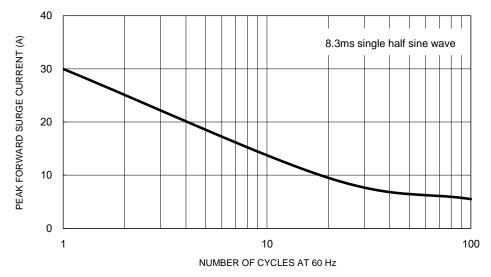


Fig.5 Maximum Non-Repetitive Forward Surge Current

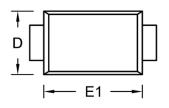


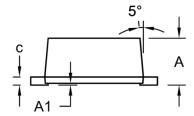


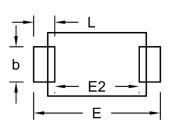


# **PACKAGE OUTLINE DIMENSIONS**

## Sub SMA

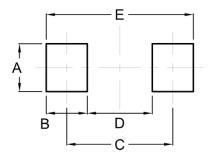






DIM.	Unit (mm)		Unit (	(inch)	
Dilvi.	Min.	Max.	Min.	Max.	
Α	1.23	1.43	0.048	0.056	
A1	0.00	0.10	0.000	0.004	
b	0.80	1.20	0.031	0.047	
С	0.16	0.30	0.006	0.012	
D	1.70	1.90	0.067	0.075	
E	3.40	3.80	0.134	0.150	
E1	2.70	2.90	0.106	0.114	
E2	2.45	2.60	0.096	0.102	
L	0.35	0.85	0.014	0.033	

# **SUGGESTED PAD LAYOUT**



Symbol	Unit (mm)	Unit (inch)
Α	1.40	0.055
В	1.20	0.047
С	3.10	0.122
D	1.90	0.075
E	4.30	0.169

# **MARKING DIAGRAM**



P/N = Marking Code G = Green Compound

ΥW = Date Code F = Factory Code



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