

1A, 50V - 1000V High Efficient Surface Mount Rectifiers

FEATURES

- Glass passivated chip junction
- Ideal for automated placement
- Low forward voltage drop
- Fast switching for high efficiency
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition



DO-214AC (SMA)





MECHANICAL DATA

Case: DO-214AC (SMA)

Molding compound, UL flammability classification rating 94V-0

Moisture sensitivity level: level 1, per J-STD-020 Part no. with suffix "H" means AEC-Q101 qualified

Packing code with suffix "G" means green compound (halogen-free) **Terminal:** Matte tin plated leads, solderable per JESD22-B102

Meet JESD 201 class 2 whisker test **Polarity:** Indicated by cathode band **Weight:** 0.06 g (approximately)

PARAMETER	SYMBOL	HS	HS	HS	HS	HS	HS	HS	HS	UNIT
PARAIVIE I ER	STIVIBUL	1A	1B	1D	1F	1G	1J	1K	1M	ONT
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	200	300	400	600	800	1000	V
Maximum RMS voltage	V_{RMS}	35	70	140	210	280	420	560	700	V
Maximum DC blocking voltage	V_{DC}	50	100	200	300	400	600	800	1000	V
Maximum average forward rectified current	I _{F(AV)}					1				Α
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	30				Α				
Maximum instantaneous forward voltage (Note 1) @ 1 A	V _F		1	.0		1.3		1.7		V
T_J =25°C Maximum reverse current @ rated V_R T_J =100°C T_J =125°C	I _R	5 50 150					μΑ			
Maximum reverse recovery time (Note 2)	t _{rr}	50 75					ns			
Typical junction capacitance (Note 3)	CJ	20 15					pF			
Typical thermal resistance	$R_{\theta JA}$	70					°C/W			
Operating junction temperature range	TJ	- 55 to +150					°C			
Storage temperature range	T _{STG}	- 55 to +150					°C			

Note 1: Pulse test with PW=300µs, 1% duty cycle

Note 2: Reverse Recovery Test Conditions: I_F =0.5A, I_R =1.0A, I_{RR} =0.25A

Note 3: Measured at 1 MHz and Applied Reverse Voltage of 4.0V D.C.



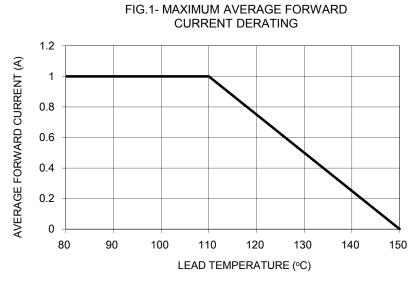
ORDERING INFORMATION						
PART NO.	PART NO.	PACKING CODE	PACKING CODE	PACKAGE	PACKING	
	SUFFIX		SUFFIX			
	Н	R3		SMA	1,800 / 7" Plastic reel	
		R2	G	SMA	7,500 / 13" Paper reel	
HS1x (Note 1)		M2		SMA	7,500 / 13" Plastic reel	
		F3		Folded SMA	1,800 / 7" Plastic reel	
		F2		Folded SMA	7,500 / 13" Paper reel	
		F4		Folded SMA	7,500 / 13" Plastic reel	
		E3		Clip SMA	1,800 / 7" Plastic reel	
		E2		Clip SMA	7,500 / 13" Plastic reel	

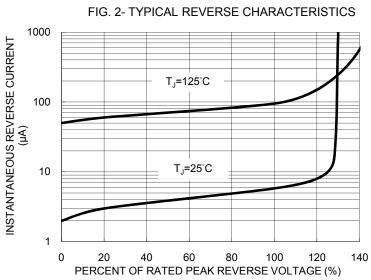
Note 1: "x" defines voltage from 50V (HS1A) to 1000V (HS1M)

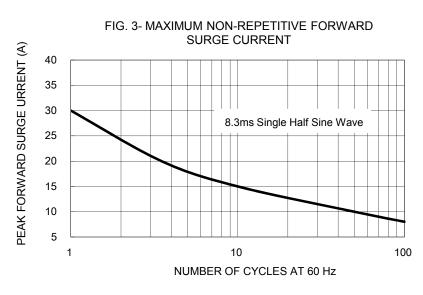
EXAMPLE						
PREFERRED PART NO.	PART NO. PACKING CODE		PACKING CODE SUFFIX	DESCRIPTION		
HS1MHR3G	HS1M	Н	R3	G	AEC-Q101 qualified Green compound	

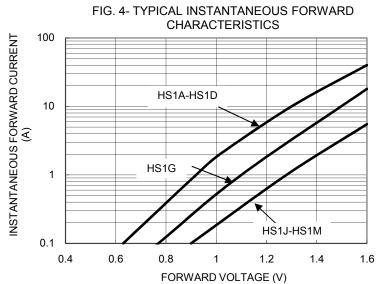
RATINGS AND CHARACTERISTICS CURVES

(T_A=25°C unless otherwise noted)











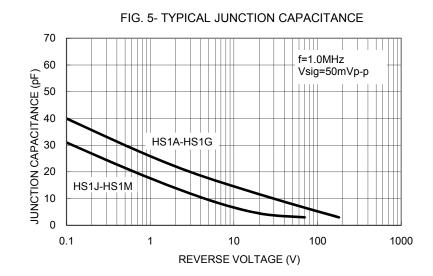
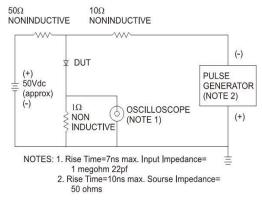
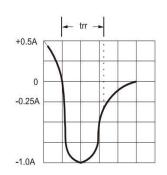
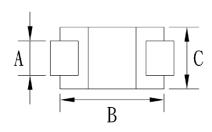


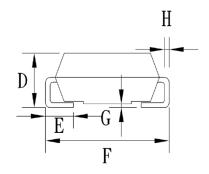
FIG.6- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM





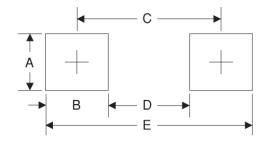
PACKAGE OUTLINE DIMENSIONS DO-214AC (SMA)





DIM.	Unit	(mm)	Unit (inch)		
DIIVI.	Min	Max	Min	Max	
Α	1.27	1.58	0.050	0.062	
В	4.06	4.60	0.160	0.181	
С	2.29	2.83	0.090	0.111	
D	1.99	2.50	0.078	0.098	
Е	0.90	1.41	0.035	0.056	
F	4.95	5.33	0.195	0.210	
G	0.10	0.20	0.004	0.008	
Н	0.15	0.31	0.006	0.012	

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
Α	1.68	0.066
В	1.52	0.060
С	3.93	0.155
D	2.41	0.095
E	5.45	0.215

MARKING DIAGRAM



P/N = Specific Device Code G = Green Compound YW = Date Code

F = Factory Code



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HS1A HS1F HS1A F2 HS1F F2 HS1B F2 HS1K R2 HS1A F2G HS1G F2 HS1B F2G HS1K R2G HS1A R2G HS1D R2G HS1M F2 HS1J F2G HS1F R2G HS1B R2 HS1B R3 HS1G R2 HS1D R3 HS1G R2G HS1D F2G HS1G R3 HS1M R3 HS1M R2 HS1M F2G HS1D R2 HS1J F2 HS1K R3 HS1B R2G HS1J R3 HS1D F2 HS1K F2G HS1J R2G HS1J R2G HS1J R3 HS1D F2 HS1K F2G HS1J R2G HS1J R2G HS1B M2G HS1D M2G HS1F M2G HS1G M2G HS1D H HS1FH HS1GH HS1JH HS1KH HS1MH