

3A, 50V - 1000V Glass Passivated Rectifiers

FEATURES

- Glass passivated chip junction
- High current capability, Low VF
- High reliability
- High surge current capability
- Low power loss, high efficiency
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition



MECHANICAL DATA

Case: DO-201AD

Molding compound, UL flammability classification rating 94V-0

Part No. with suffix "H" means AEC-Q101 qualified

Packing code with suffix "G" means green compound (halogen-free)

Terminal: Pure tin plated leads, solderable per JESD22-B102

Meet JESD 201 class 2 whisker test

Weight: 1.2 g (approximately)

DO-201AD

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS (T _A =25°C unless otherwise noted)									
PARAMETER	SYMBOL	1N 5400G	1N 5401G	1N 5402G	1N 5404G	1N 5406G	1N 5407G	1N 5408G	UNIT
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V _{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V _{DC}	50	100	200	400	600	800	1000	V
Maximum average forward rectified current	I _{F(AV)}	3							A
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	125							A
Maximum instantaneous forward voltage (Note 1) @ 3 A	V _F	1.1		1.0				V	
Maximum reverse current @ rated VR T _J =25 °C T _J =125 °C	I _R	5 100							μA
Typical junction capacitance (Note 2)	C _J	25							pF
Typical thermal resistance	R _{θJC} R _{θJA}	15 45							°C/W
Operating junction temperature range	T _J	- 55 to +150							°C
Storage temperature range	T _{STG}	- 55 to +150							°C

Note1: Pulse Test with PW=300μs, 1% Duty Cycle

Note2: Measured at 1 MHz and Applied Reverse Voltage of 4.0V D.C.

ORDERING INFORMATION					
PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX	PACKAGE	PACKING
1N540xG (Note 1)	H	A0	G	DO-201AD	500 / Ammo box
		R0		DO-201AD	1,250 / 13" Paper reel
		B0		DO-201AD	500 / Bulk packing
		X0		DO-201AD	Forming

Note 1: "x" defines voltage from 50V (1N5400G) to 1000V (1N5408G)

EXAMPLE					
PREFERRED PART NO.	PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION
1N5408GHA0G	1N5408G	H	A0	G	AEC-Q101 qualified Green compound

RATINGS AND CHARACTERISTICS CURVES

($T_A=25^{\circ}\text{C}$ unless otherwise noted)

FIG.1 MAXIMUM FORWARD CURRENT DERATING CURVE

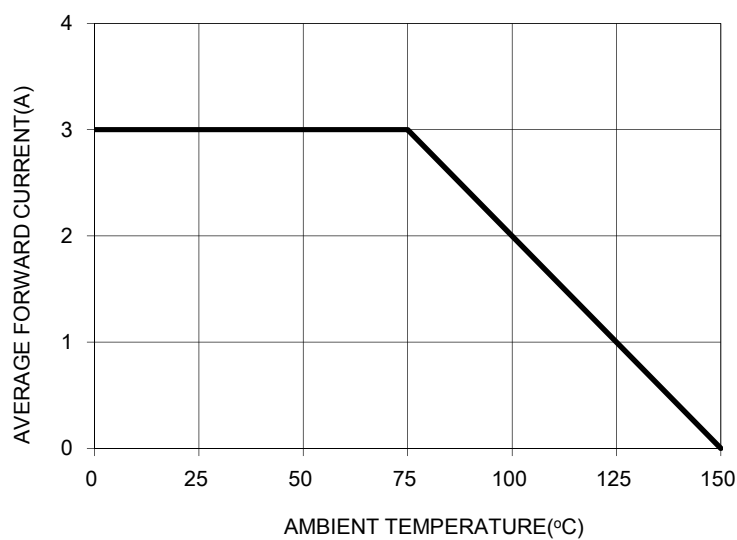


FIG. 2 TYPICAL REVERSE CHARACTERISTICS

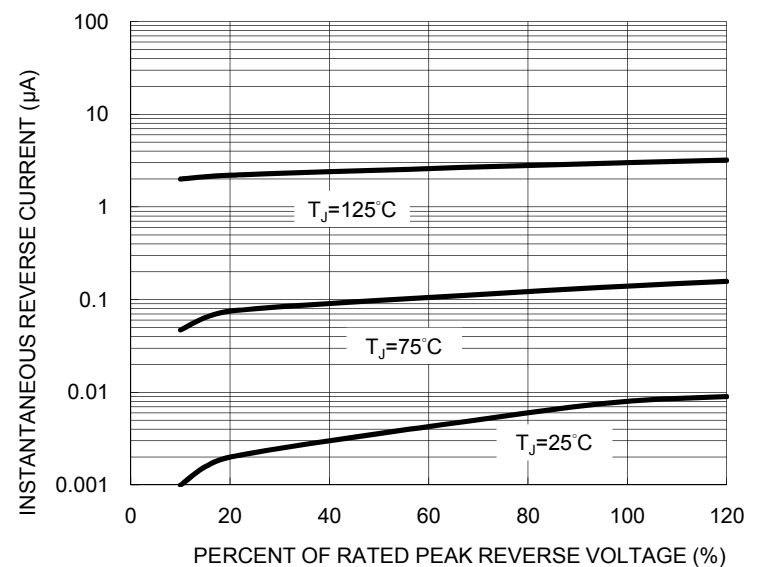


FIG. 3 MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

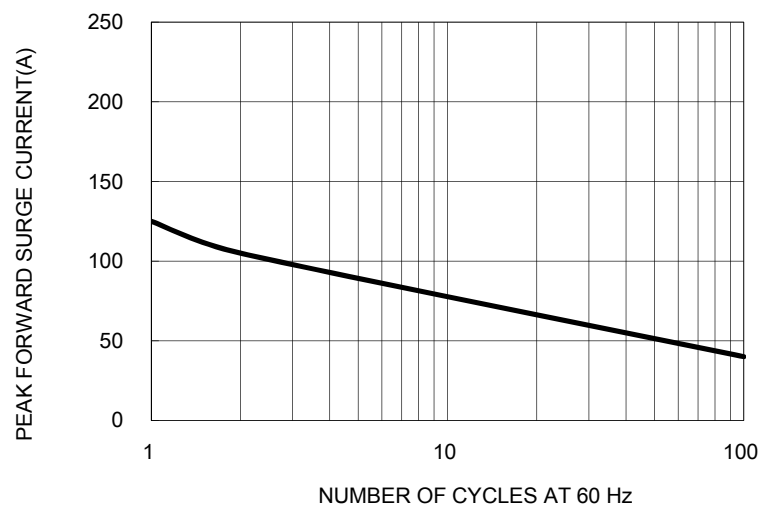


FIG. 4 TYPICAL FORWARD CHARACTERISTICS

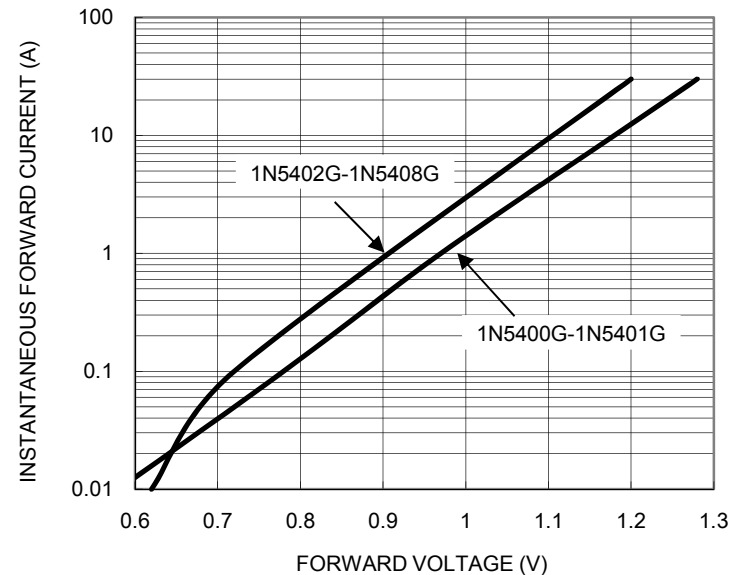
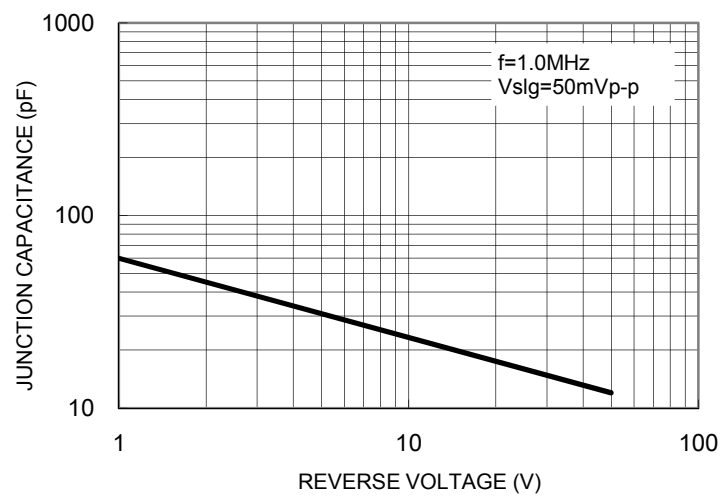
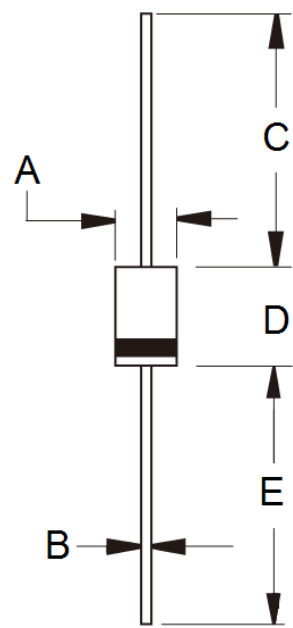


FIG. 5 TYPICAL JUNCTION CAPACITANCE



PACKAGE OUTLINE DIMENSIONS

DO-201AD



DIM.	Unit (mm)		Unit (inch)	
	Min	Max	Min	Max
A	5.00	5.60	0.197	0.220
B	1.20	1.30	0.048	0.052
C	25.40	-	1.000	-
D	8.50	9.50	0.335	0.375
E	25.40	-	1.000	-

MARKING DIAGRAM



P/N = Specific Device Code
G = Green Compound
YWW = Date Code
F = Factory Code

Notice

Specifications of the products displayed herein are subject to change without notice. TSC or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.

Information contained herein is intended to provide a product description only. No license, express or implied, to any intellectual property rights is granted by this document. Except as provided in TSC's terms and conditions of sale for such products, TSC assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of TSC products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify TSC for any damages resulting from such improper use or sale.

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Taiwan Semiconductor:

[1N5400G](#) [1N5401G](#) [1N5402G](#) [1N5404G](#) [1N5406G](#) [1N5407G](#) [1N5408G](#) [1N5408GHR0G](#) [1N5401G R0](#)
[1N5408G R0](#) [1N5404G R0](#) [1N5407G R0](#) [1N5406G R0](#) [1N5400GHR0G](#) [1N5400G R0G](#) [1N5408GHR0](#)
[1N5407GHR0G](#) [1N5406GHR0G](#) [1N5404GHR0](#) [1N5404GHR0G](#) [1N5401GHR0G](#) [1N5402GHR0G](#) [1N5407G R0G](#)
[1N5408G R0G](#) [1N5400G R0](#) [1N5402G R0](#) [1N5402G R0G](#) [1N5404G R0G](#) [1N5406G R0G](#) [1N5401G R0G](#)
[1N5407GHA0G](#) [1N5408GHA0G](#) [1N5406G A0G](#) [1N5406GHA0G](#) [1N5401GHA0G](#) [1N5404G A0G](#) [1N5408G A0G](#)
[1N5407G A0G](#) [1N5404GH](#) [1N5406GH](#) [1N5407GH](#) [1N5408GH](#)