TDA7391PD

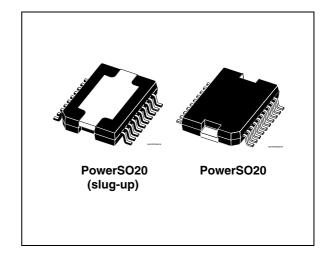
32 W bridge car radio amplifier

Features

- High power capability:
 - 40 W/3.2 Ω EIAJ
 - 32 W/3.2 Ω @ V $_{S}$ = 14.4 V, f = 1 kHz, d = 10 %
 - 26 W/4 Ω @ V_S = 14.4 V, f = 1 kHz, d = 10 %
- Differential inputs (either single ended or differential input signal are accepted)
- Minimum external component count:
 - No bootstrap capacitors
 - No Boucherot cells
 - Internally fixed gain (30 dB)
 - No SVR capacitor
- Standby function (CMOS compatible)
- Programmable turn-on/off delay
- No audible pop during mute and stand-by operations

Protections

- Short circuit (to GND, to V_S, across the load)
- Very inductive loads
- Chip over temperature
- Load dump
- Open GND
- ESD



Description

The TDA7391PD is a bridge class AB audio power amplifier specially intended for car radio high power applications.

The high power capability together with the possibility to operate either in differential input mode or single ended input mode makes it suitable for boosters and high end car radio equipment. The exclusive fully complementary output stage and the internal fixed gain configuration drop the external component count.

The on board clipping detector allows easy implementation of gain compression systems.

Table 1. Device summary

Order code	Package	Packing
E-TDA7391PD	PowerSO20	Tube
E-TDA7391PDTR	PowerSO20	Tape and reel
TDA7391PDU	PowerSO20 (slug-up)	Tube
TDA7391PDUTR	PowerSO20 (slug-up)	Tape and reel

Contents TDA7391PD

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1 Test and application circuit, block diagram

Figure 1. Test and application circuit

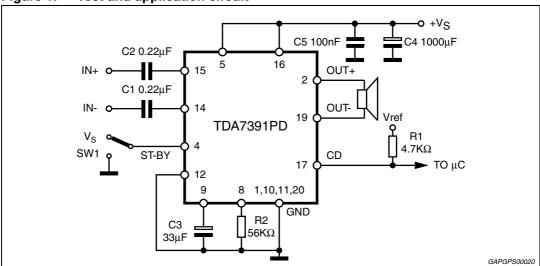
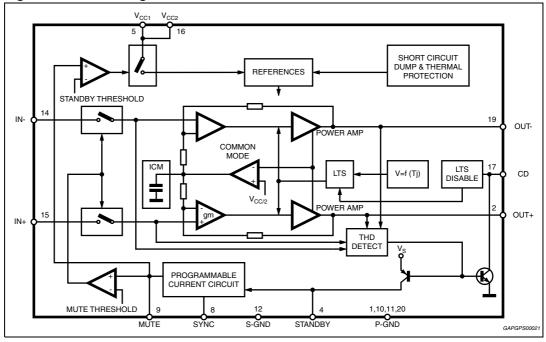


Figure 2. Block diagram



Pins description TDA7391PD

2 Pins description

Figure 3. Pins connection (top view)

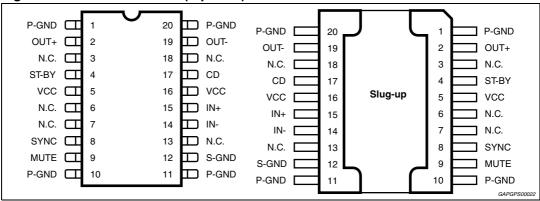


Table 2. Pins function

Pin	Function	Description
14, 15	INPUTS	The input stage is a high impedance type also capable of operation in single ended mode with one input capacitively coupled to the signal GND. The impedance seen by the inverting and non inverting input pins must be matched.
5, 16	⁺ V _S	Supply voltage.
17	CD	The TDA7391PD is equipped with a diagnostic circuitry able to detect the clipping in the Output Signal (distortion = 10%). The CD pin (open collector) gives out low level signal during clipping.
2, 19	OUTPUTS	The output stage is a bridge type able to drive loads as low as 3.2Ω . It consists of two class AB fully complementary PNP/NPN stages fully protected. A rail to rail output voltage swing is achieved without need of bootstrap capacitors. No external compensation is necessary.
1, 10, 11, 20	GND	Power Ground.
12	S-GND	Signal ground.
4	STAND-BY	The device features a ST-BY function which shuts down all the internal bias supplies when the ST-BY pin is low. In ST-BY mode the amplifier sinks a small current (in the range of few μ A). When the ST-BY pin is high the IC becomes fully operational.
8	SYNC	A resistor (R_2) has to be connect between pin 8 and GND in order to program the current that flows in the C_3 capacitor (pin 9). The values of C_3 and R_2 determine the time required to bias the amplifier.
9	MUTE	The pin will have a capacitor (C ₃) tied to GND to set the MUTE/STAND-BY time. An automatic Mute during turn on/off is provided to prevent noisy transients.

3 Electrical specifications

3.1 Absolute maximum ratings

Table 3. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _S	DC supply voltage	28	V
V _{OP}	Operating supply voltage	18	V
V _{PEAK}	Peak supply voltage (t = 50 ms)	50	V
1.	Output peak current repetitive (f > 10 Hz)	4.5	Α
I _O	Output peak current non repetitive	6	Α
P _{tot}	Power dissipation (T _{case} = 85 °C)	32	W
T _{stg} , T _j	Storage and junction temperature	-40 to 150	°C

3.2 Thermal data

Table 4. Thermal data

Symbol	Parameter	Value	Unit
R _{th j-case}	Thermal resistance junction-to-case Max.	2	°C/W

3.3 Electrical characteristics

 $\rm V_{S}\!=14.4~V;~R_{L}\!=4~\Omega,~f$ = 1 kHz, $\rm T_{amb}\!=25~^{\circ}C,~unless~otherwise~specified.$

Table 5. Electrical characteristics

Symbol	Parameter	Test condition	Min.	Тур.	Max.	Unit
V _S	Supply voltage range	-	8	-	18	V
Iq	Total quiescent current	-	-	60	150	mA
V _{OS}	Output offset voltage	-	-	-	120	mV
I _{SB}	Standby current	V _{ST-BY} = 1.5 V	-	-	100	μА
I _{SBin}	Standby input Bias current	V _{ST-BY} = 5 V	-	-	10	μА
V _{SBon}	Standby on threshold voltage	-	-	-	1.5	V
V _{SBoff}	Standby off threshold voltage	-	3.5	-	-	V
ATT _{Standby}	Standby attenuation	-	-	90	-	dB
I _{M in}	Mute input bias current	(V _{MUTE} = 5 V)	-	-	10	μА
A _M	Mute attenuation	-	-	90	-	dB

Table 5. Electrical characteristics (continued)

Symbol	Parameter	Test condition	Min.	Тур.	Max.	Unit
		d = 10 %	20	26	-	W
P _O	Output power	d = 1 %	-	21	-	W
		d = 10 %; R _L = 3.2 Ω	-	32	-	W
P _{O EIAJ}	EIAJ output power (*)	V _S = 13.7 V	-	40	-	W
d	Distortion	-	-	0.06	-	%
u	DISTOLLION	P _O = 0.1 to 15 W	-	0.03	-	%
G _V	Voltage gain	-	29.5	30	30.5	dB
f _H	High frequency rolloff	P _O = 1 W; -3 dB	75	-	-	kHz
В	Input Impedance	Differential	36	60	-	kΩ
R _{IN}		Single Ended	30	55	-	kΩ
E _{IN}	Input noise voltage	$R_g = 0 \Omega$; $f = 22 Hz$ to 22 kHz	-	4	-	mV
CMRR	Input common mode rejection	f = 1 kHz; V _{IN} = 1 V _{rms}	-	65	-	dB
SVR	Supply voltage rejection	$R_g = 0 \Omega$; $V_r = 1 Vrms$	-	60	-	dB
CDL	Clipping detection level	-	-	10	-	%
T _{sd}	Absolute thermal shutdown junction temperature	-	-	160	-	°C

3.4 Electrical characteristics curves

Figure 4. Quiescent current vs. supply voltage

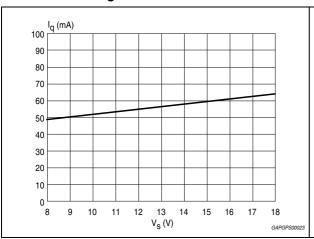


Figure 5. EIAJ power vs. supply voltage

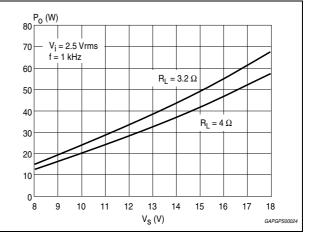
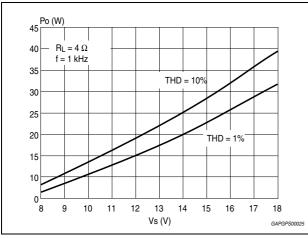


Figure 6. Output power vs. supply voltage $(@R_L = 4\Omega)$

Figure 7. Distortion vs. frequency (@ $R_L = 4\Omega$)



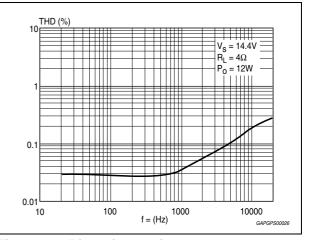
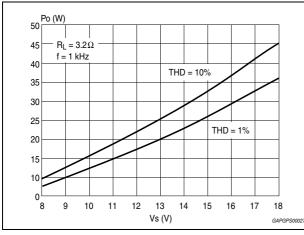


Figure 8. Output power vs. supply voltage $(@R_1 = 3.2\Omega)$

Figure 9. Distortion vs. frequency (@ $R_1 = 3.2\Omega$)



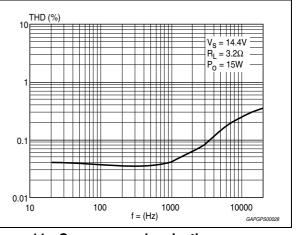
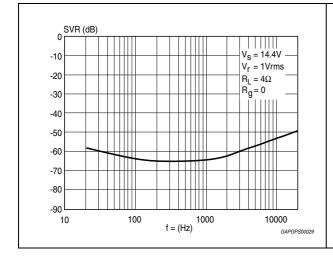


Figure 10. Supply voltage rejection vs. frequency

Figure 11. Common mode rejection vs. frequency



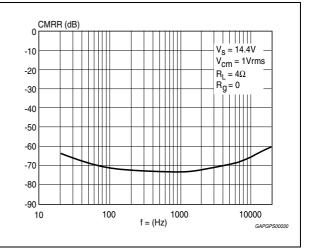
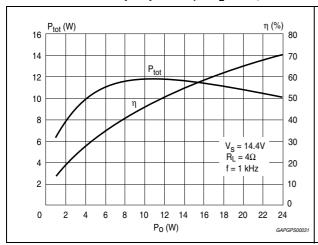
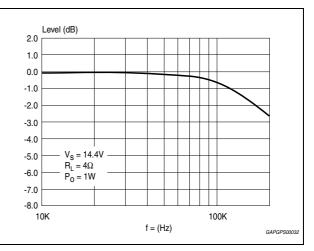


Figure 12. Total power dissipation and effic. vs. output power (@R_ = 4 Ω)

Figure 13. Power bandwidth





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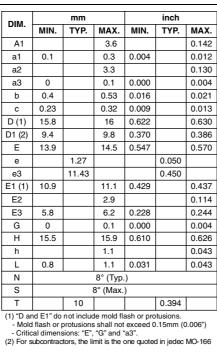
TDA7391PD Package information

4 Package information

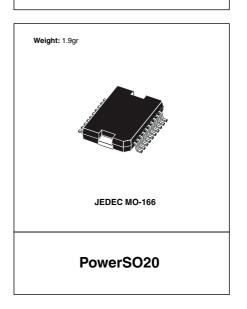
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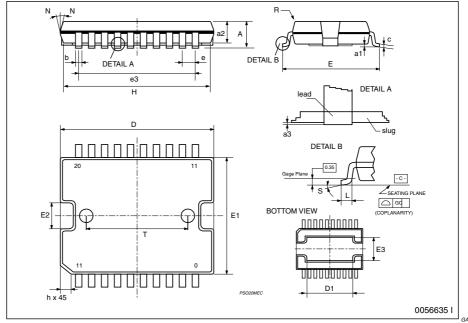
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Figure 14. PowerSO20 mechanical data and package dimensions



OUTLINE AND MECHANICAL DATA





Package information TDA7391PD

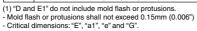
Figure 15. PowerSO20 (slug-up) mechanical data and package dimensions

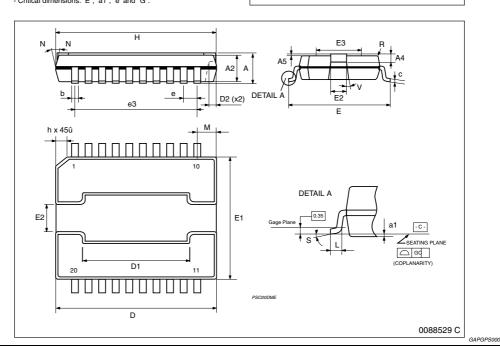
DIM.	mm			inch			
Dilvi.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
Α	3.25		3.5	0.128		0.138	
A2	3	3.15	3.3	0.118	0.124	0.130	
A4	0.8		1	0.031		0.039	
A5	0.15	0.2	0.25	0.006	0.008	0.010	
a1	0.030		-0.040	0.0012		-0.0016	
b	0.4		0.53	0.016		0.021	
С	0.23		0.32	0.009		0.012	
D (1)	15.8		16	0.622		0.630	
D1	9.4		9.8	0.370		0.385	
D2		1			0.039		
Е	13.9		14.5	0.547		0.570	
E1 (1)	10.9		11.1	0.429		0.437	
E2			2.9			0.114	
E3	5.8		6.2	0.228		0.244	
е	1.12	1.27	1.42	0.044	0.050	0.056	
e3		11.43			0.450		
G	0		0.1	0		0.004	
Н	15.5		15.9	0.61		0.625	
h			1.1			0.043	
L	0.8		1.1	0.031		0.043	
Ν	10° (max)						
R	0.6 0.024						
S		0	° (min.)	8° (max	.)		
V	5° (min.) 7° (max.)						

OUTLINE AND MECHANICAL DATA



PowerSO20 (Slug-up)





TDA7391PD Revision history

5 Revision history

Table 6. Document revision history

Date	Revision	Changes	
10-Oct-1998	1	Initial release.	
02-Jul-2008	2	Document reformatted. Document status promoted from product preview to datasheet. Added <i>Table 1: Device summary</i> . Added ECOPACK description in <i>Section 5: Package information</i> .	
19-Apr-2010	3 Updated Table 1: Device summary.		
26-Jan-2011	4	Updated <i>Table 1: Device summary</i> . Added PowerSO20 (slug-up) drawing in <i>Figure 3</i> . Added PowerSO20 (slug-up) in <i>Section 4: Package information</i> .	
16-Sep-2013	5	Updated Disclaimer.	

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