

Features

- Low forward voltage drop meaning very small conduction losses
- Low switching losses allowing high frequency operation
- Avalanche capability specified
- AEC-Q101 qualified

Description

Dual center tap Schottky barrier rectifier designed for high frequency switched mode power supplies and DC to DC converters.

Packaged in D²PAK, this device is intended for use in low voltage, high frequency inverters, free-wheeling and polarity protection for automotive applications.

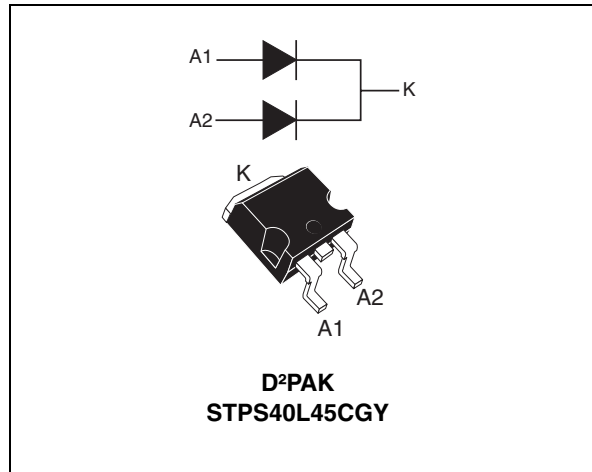


Table 1. Device summary

Symbol	Value
$I_{F(AV)}$	2 x 20 A
V_{RRM}	45 V
$T_j \text{ (max)}$	150 °C
$V_F \text{ (max)}$	0.49 V

1 Characteristics

Table 2. Absolute ratings (limiting values, per diode)

Symbol	Parameter			Value	Unit
V _{RRM}	Repetitive peak reverse voltage			45	V
I _{F(RMS)}	Forward rms current			30	A
I _{F(AV)}	Average forward current	T _C =130 °C δ = 0.5	per diode per device	20 40	A
I _{FSM}	Surge non repetitive forward current	t _p = 10 ms sinusoidal		230	A
I _{RRM}	Repetitive peak reverse current	t _p = 2 μs square F = 1 kHz		2	A
I _{RSM}	Non repetitive peak reverse current	t _p = 100 μs square		3	A
P _{ARM}	Repetitive peak avalanche power	t _p = 1 μs T _j = 25 °C		8100	W
T _{stg}	Storage temperature range			-65 to + 150	°C
T _j	Operating junction temperature ⁽¹⁾			-40 to + 150	°C
dV/dt	Critical rate of rise of reverse voltage			10000	V/μs

1. $\frac{dP_{tot}}{dT_j} < \frac{1}{R_{th(j-a)}}$ condition to avoid thermal runaway for a diode on its own heatsink

Table 3. Thermal resistances

Symbol	Parameter		Value	Unit
$R_{th(j-c)}$	Junction to case	Per diode Total	1.5 0.8	°C/W
$R_{th(c)}$	Coupling		0.1	°C/W

When the diodes 1 and 2 are used simultaneously :

$$\Delta T_j(\text{diode } 1) = P(\text{diode } 1) \times R_{th(j-c)}(\text{Per diode}) + P(\text{diode } 2) \times R_{th(c)}.$$

Table 4. Static electrical characteristics (per diode)

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
$I_R^{(1)}$	Reverse leakage current	$T_j = 25\text{ °C}$	$V_R = V_{RRM}$			0.6	mA
		$T_j = 125\text{ °C}$			140	280	mA
$V_F^{(1)}$	Forward voltage drop	$T_j = 25\text{ °C}$	$I_F = 20\text{ A}$			0.53	V
		$T_j = 125\text{ °C}$	$I_F = 20\text{ A}$		0.42	0.49	
		$T_j = 25\text{ °C}$	$I_F = 40\text{ A}$			0.69	
		$T_j = 125\text{ °C}$	$I_F = 40\text{ A}$		0.6	0.7	

1. Pulse test: $t_p = 380\text{ }\mu\text{s}$, $\delta < 2\%$

To evaluate the conduction losses use the following equation:

$$P = 0.28 \times I_{F(AV)} + 0.0105 I_{F(RMS)}^2$$

Figure 1. Average forward power dissipation versus average forward current (per diode)

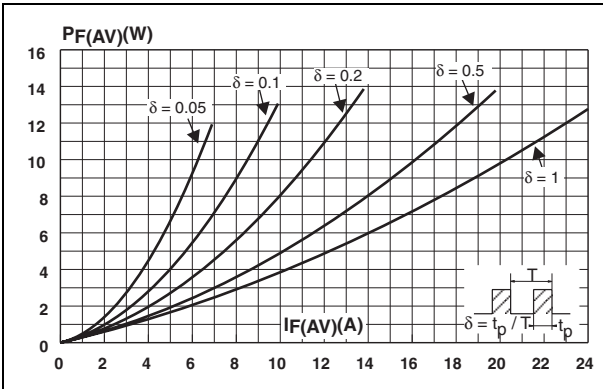


Figure 2. Average forward current versus ambient temperature ($\delta = 0.5$, per diode)

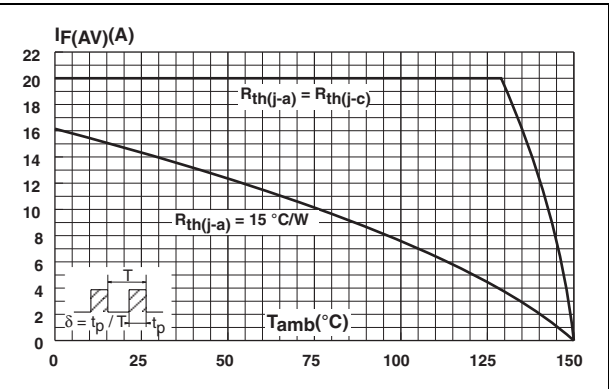


Figure 3. Normalized avalanche power derating versus pulse duration

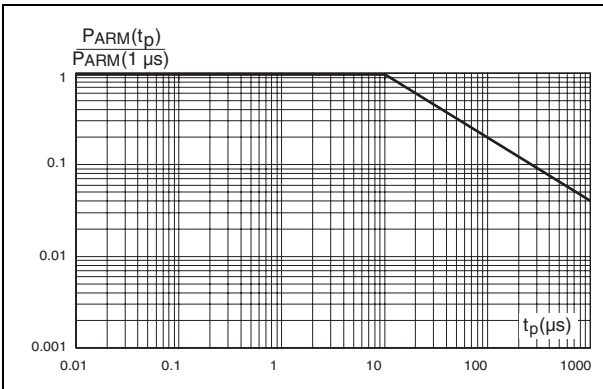


Figure 4. Normalized avalanche power derating versus junction temperature

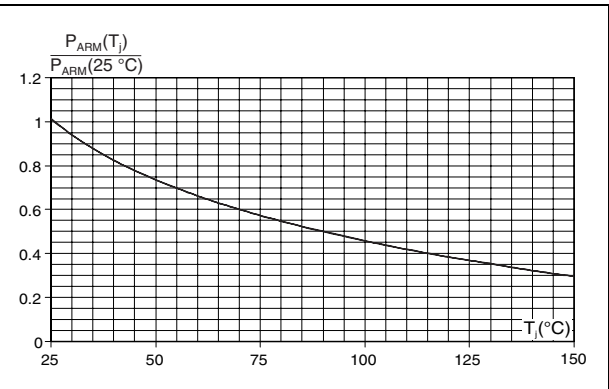


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values, per diode)

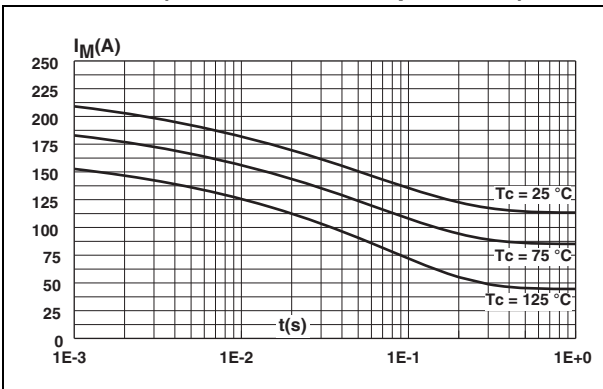


Figure 6. Relative variation of thermal impedance junction to case versus pulse duration

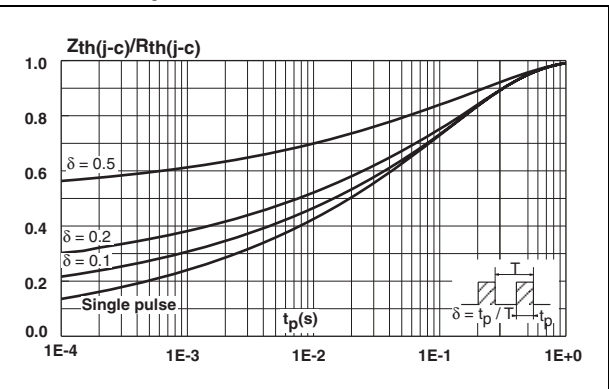


Figure 7. Reverse leakage current versus reverse voltage applied (typical values, per diode)

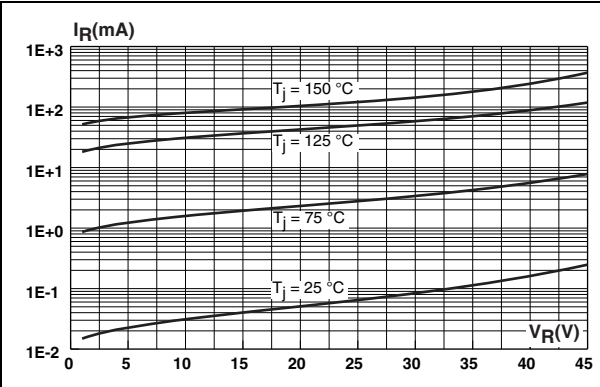


Figure 8. Junction capacitance versus reverse voltage applied (typical values, per diode)

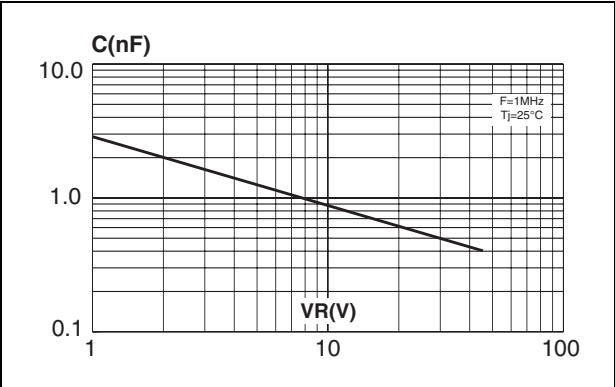


Figure 9. Forward voltage drop versus forward current (maximum values, per diode)

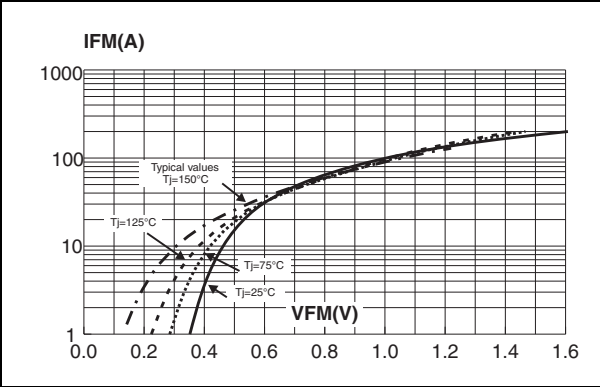
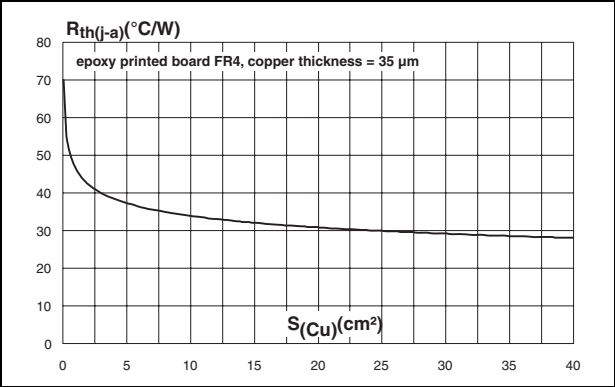


Figure 10. Thermal resistance junction to ambient versus copper surface under tab.



3 Ordering information

Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
STPS40L45CGY-TR	STPS40L45CGY	D ² PAK	1.8 g	500	Tape and Reel

4 Revision history

Table 7. Document revision history

Date	Revision	Changes
25-Jun-2012	1	First issue.

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