

STPS1L30MF

Low drop power Schottky rectifier in flat package

Main product characteristics

I _{F(AV)}	1 A
V_{RRM}	30 V
T _j (max)	150° C
V _F (max)	0.39 V

Features and benefits

- Very low profile package: 0.85 mm
- Backward compatible with standard STmite footprint
- Very small conduction losses
- Negligible switching losses
- Extremely fast switching
- Low forward voltage drop for higher efficiency and extended battery life
- Low thermal resistance
- Avalanche capability specified

Order Code

Part number	Marking		
STPS1L30MF	F1L3		

STmite flat (DO222-AA)

Description

Single Schottky rectifier suited for switch mode power supplies and high frequency DC to DC converters.

Packaged in STmite flat, this device is intended for use in very low voltage, high frequency inverters, free wheeling and polarity protection applications. Due to the very small size of the package this device fits battery powered equipment (cellular, notebook, PDA's, printers) as well as chargers and PCMCIA cards.

Table 1. Absolute ratings (limiting values)

Symbol	Parameter	Value	Unit	
V_{RRM}	Repetitive peak reverse voltage	30	V	
I _{F(RMS)}	RMS forward voltage	RMS forward voltage		
I _{F(AV)}	Average forward current $T_c = 140^{\circ} \text{ C} \delta = 0.5$		1	Α
I _{FSM}	Surge non repetitive forward current	t _p = 10 ms sinusoidal	50	Α
P _{ARM}	Repetitive peak avalanche power $t_p = 1 \mu s T_j = 25^{\circ} C$		1200	W
T _{stg}	Storage temperature range		-65 to + 150	°C
T _j	Maximum operating junction temperature ⁽¹⁾	150	°C	
dV/dt	Critical rate of rise of reverse voltage (rated \	10000	V/µs	

^{1.} $\frac{dPtot}{dT_i} < \frac{1}{Rth(i-a)}$ condition to avoid thermal runaway for a diode on its own heatsink

Characteristics STPS1L30MF

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Table 2. Thermal resistance

Symbol	Parameter		Unit
R _{th(j-c)}	R _{th(j-c)} Junction to case		°C/W
R _{th(j-a)} ⁽¹⁾	Junction to ambient	250	°C/W

^{1.} Mounted with minimum recommended pad size, PC board FR4

Table 3. Static electrical characteristics

Symbol	Parameter	Tests co	onditions	Min.	Тур	Max.	Unit
	Reverse leakage current	T _j = 25° C	$V_R = V_{RRM}$		0.13	0.39	mA
		T _j = 85° C			5.25	16.5	
I _B ⁽¹⁾		T _j = 25° C	V _R = 20 V		0.05	0.24	
'R`		T _j = 85° C			3.5	10.5	
		T _j = 25° C	V _R = 10 V		0.03	0.15	
		T _j = 85° C			2.4	7	
		T _j = 25° C	I _F = 1 A		0.33	0.39	
		T _j = 85° C	IF = I A		0.28	0.34	
		T _j = 25° C	I _F = 2 A		0.39	0.45	
V _F ⁽¹⁾	Forward voltage drop	T _j = 85° C			0.36	0.42	V
VF \		T _j = 25° C	I _F = 3 A		0.45	0.53	V
		T _j = 85° C			0.43	0.51	
		T _j = 25° C	I _F = 4 A		0.50	0.60	
		T _j = 85° C			0.50	0.60	

^{1.} Pulse test: = 380 μ s, δ < 2%

To evaluate the conduction losses use the following equation: $P = 0.26 \text{ x } I_{F(AV)} + 0.08 I_{F^2(RMS)}$

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Figure 1. Conduction losses versus average Figure 2. Average forward current versus ambient temperature (δ = 0.5)

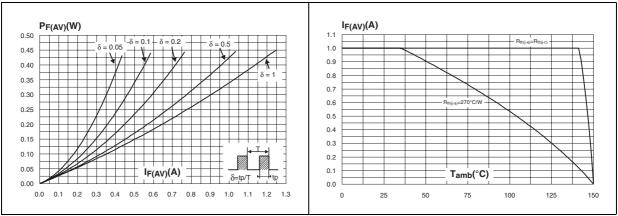


Figure 3. Normalized avalanche power derating versus pulse duration

Figure 4. Normalized avalanche power derating versus junction temperature

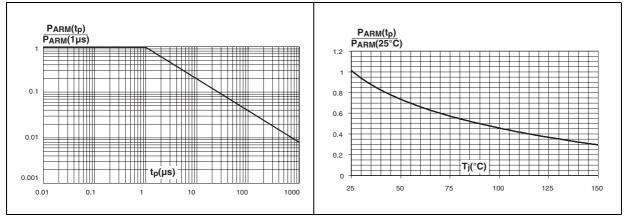
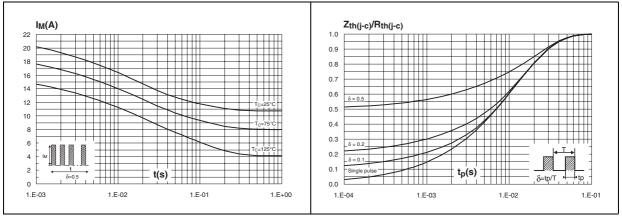


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values)

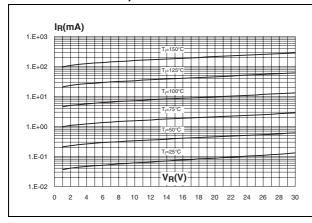
Figure 6. Relative variation of thermal impedance junction to case versus pulse duration



Characteristics STPS1L30MF

Figure 7. Reverse leakage currrent versus reverse voltage applied (typical values)

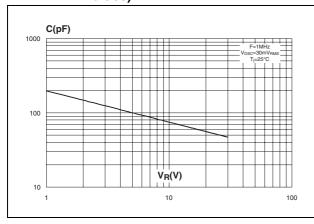
Figure 8. Reverse leakage currrent versus junction temperature (typical values)



1.E+01
1.E+00
1.E-01
1.E-01
0 25 50 75 100 125 150

Figure 9. Junction capacitance versus reverse voltage applied (typical values)

Figure 10. Forward voltage drop versus forward current



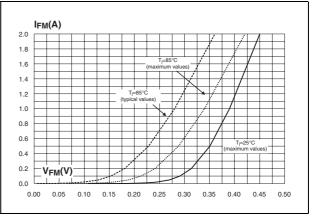
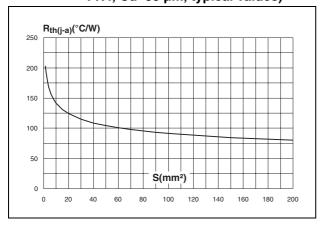


Figure 11. Thermal resistance junction to ambient versus copper surface under tab (epoxy printed board FR4, Cu=35 µm, typical values)



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STPS1L30MF Package information

2 Package information

Table 4. STmite flat dimensions

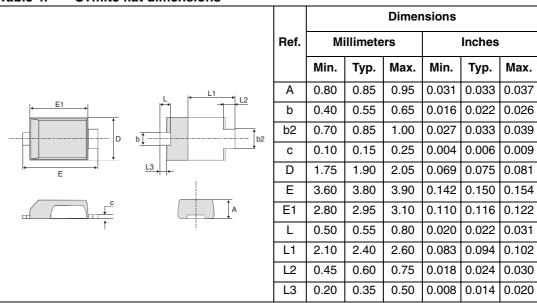
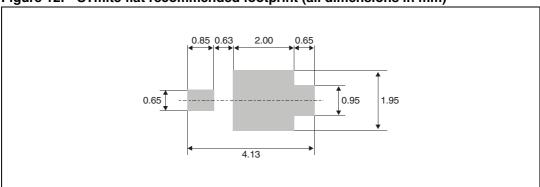


Figure 12. STmite flat recommended footprint (all dimensions in mm)



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

Ordering information STPS1L30MF

3 Ordering information

Part number	Marking	Package	Weight	Base qty	Delivery mode
STPS1L30MF	F1L3	STmite flat	16 mg	12000	Tape and reel

4 Revision history

Date	Revision	Changes
21-Aug-2006	1	First issue.

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