

STW3040

High voltage fast-switching NPN power transistor

Features

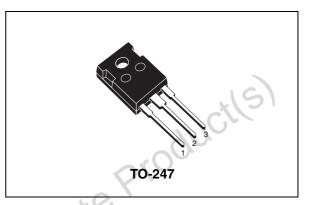
- High voltage capability
- High DC current gain
- Minimum lot-to-lot spread for reliable operation
- Wide safe operating areas (forward and reverse biased)

Applications

■ Switching mode power supplies

Description

The STW3040 is manufactured using diffused collector in planar technology adopting base island layout. The device is designed for use in u. productis productis Tat





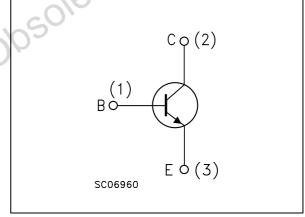


Table 1.	Device	summary
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Order code	Marking	Package	Packaging
STW3040	W3040	TO-247	Tube

Absolute maximum ratings 1

Symbol	Parameter	Value	Unit
V _{CES}	Collector-emitter voltage ($V_{CE} = 0$)	700	V
V _{CEO}	Collector-emitter voltage $(I_B = 0)$	400	V
V _{EBO}	Emitter-base voltage $(I_C = 0)$	9	V
۱ _C	Collector current	30	А
I _{CM}	Collector peak current	50	А
Ι _Β	Base current	10	А
I _{BM}	Base peak current	15	А
P _{TOT}	Total dissipation at $T_c = 25^{\circ}C$	160	W
T _{stg}	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 3. Thermal data

			Value	Unit	
R _{thj-case}	Thermal resistance junction-case	max	0.78	°C/W	
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2 Electrical characteristics

 $(T_{case} = 25^{\circ}C; unless otherwise specified)$

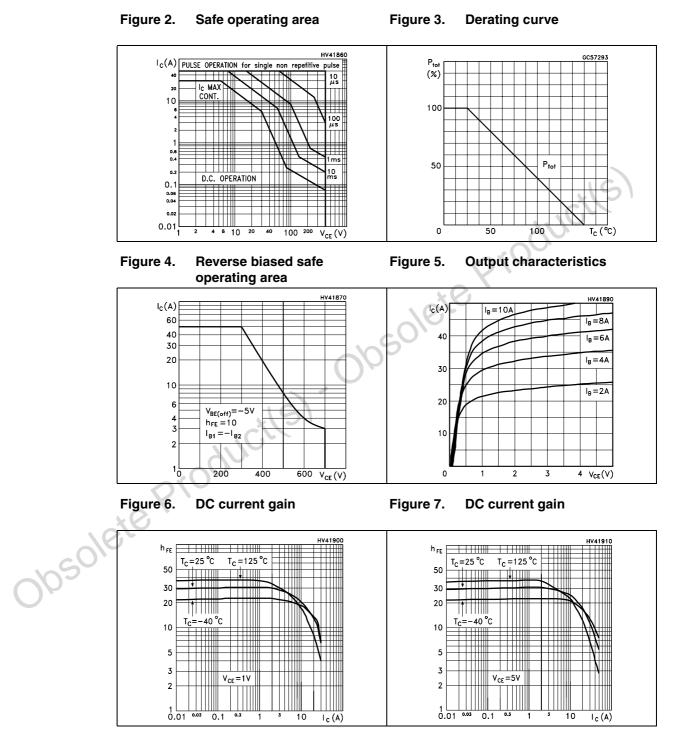
				_		
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CES}	Collector cut-off current $(V_{BE} = 0)$	V _{CE} = 700 V			1	mA
I _{EBO}	Emitter cut-off current $(I_{\rm C} = 0)$	V _{EB} = 9 V			1	mA
V _{(BR)CEO}	Collector-emitter breakdown (I _B = 0)	I _C = 10 mA	400			V
) <i>(</i> 1)	Collector-emitter	I _C = 6 A I _B = 1.2 A		0.1		v
V _{CE(sat)} ⁽¹⁾	saturation voltage	$I_{\rm C} = 20 \text{ A}$ $I_{\rm B} = 4 \text{ A}$	07	0.4	0.8	V
. <i>(</i> 1)	Base-emitter saturation	$I_{\rm C} = 6 {\rm A}$ $I_{\rm B} = 1.2 {\rm A}$			1.2	V
V _{BE(sat)} ⁽¹⁾	voltage	$I_{\rm C} = 20 \text{ A}$ $I_{\rm B} = 4 \text{ A}$			1.5	V
		I _C = 10 mA V _{CE} = 5 V	10			
h _{FE} ⁽¹⁾	DC current gain	$I_{C} = 6 A$ $V_{CE} = 5 V$	18	30	40	
	($I_{\rm C} = 20 \text{ A}$ $V_{\rm CE} = 5 \text{ V}$	10			
	Resistive load					
t _{on}	Turn-on time	$I_{\rm C} = 20 \ {\rm A}$ $V_{\rm CC} = 200 \ {\rm V}$			0.5	μs
t _s	Storage time	$I_{b(on)} = 4 A$ $I_{b(off)} = -8 A$			2.5	μs
t _f	Fall time				0.3	μs

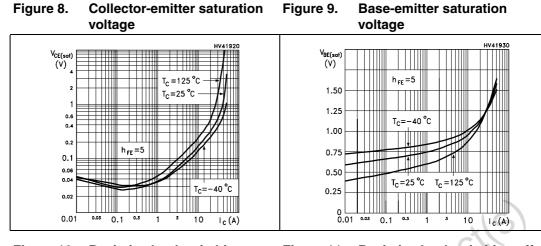
Table 4. Electrical characteristics

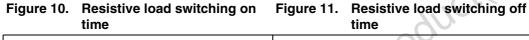
1. Pulse duration = 300 ms, duty cycle ≤1.5 %

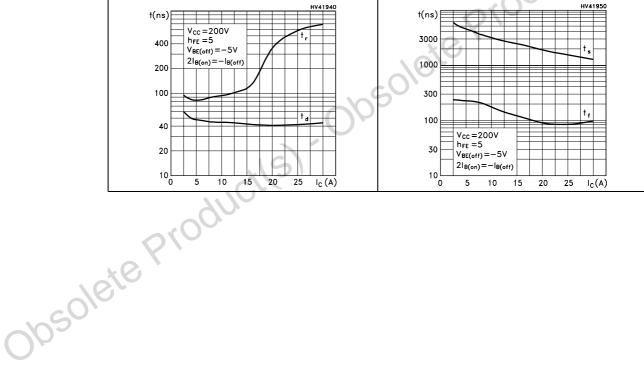
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Electrical characteristics (curves) 2.1









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3 Test circuit

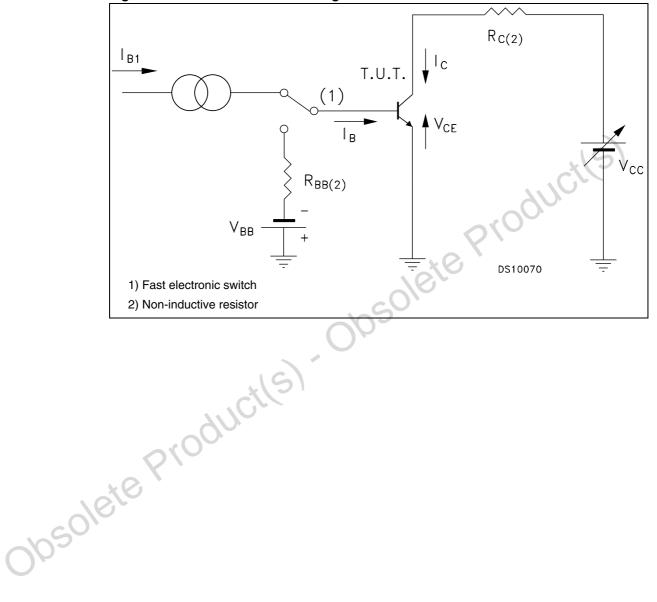


Figure 12. Resistive load switching test circuit



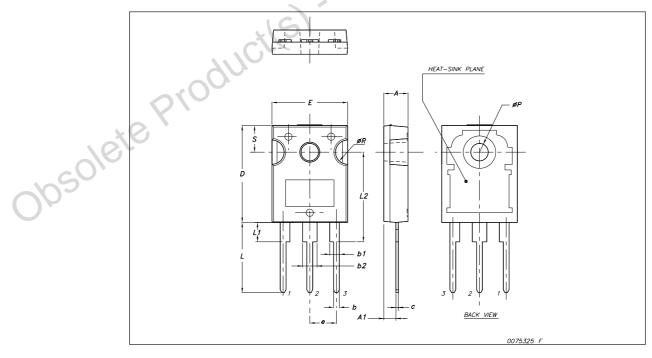
4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: <u>www.st.com</u>

Obsolete Product(s). Obsolete Product(s)

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TO-247 Mechanical data			
Dim.	mm.		
	Min.	Тур	Max.
A	4.85		5.15
A1	2.20		2.60
b	1.0		1.40
b1	2.0		2.40
b2	3.0		3.40
С	0.40		0.80 5
D	19.85		20.15
E	15.45		15.75
е		5.45	(OV
L	14.20	8	14.80
L1	3.70	×C	4.30
L2		18.50	
øP	3.55	COV	3.65
øR	4.50	~03	5.50
S		5.50	





5 Revision history

Table 5.Document revision history

	Date	Revision	Changes
	12-Dec-2007	1	Initial release
	09-Oct-2008	2	Document status promoted from preliminary data to datasheet
	23-Oct-2008	3	Test conditions changed for V _{CE(sat)} in <i>Table 4</i>
obsole	eprod		Test conditions changed for V _{CE(sat)} in <i>Table 4</i>

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