

High voltage fast-switching NPN power transistor

Features

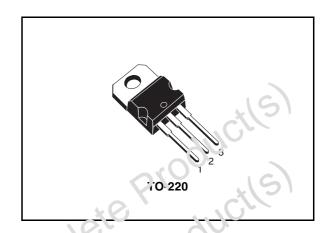
- High voltage capability
- Low spread of dynamic parameters
- Minimum lot-to-lot spread for reliable operation
- Very high switching speed

Description

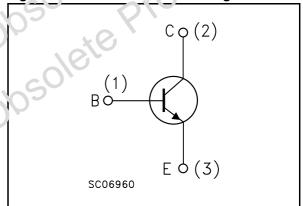
The device is manufactured using high voltage multi-epitaxial planar technology for high switching speeds and high voltage capability. It uses a cellular emitter structure with planar edge termination to enhance switching speeds. The device is designed for use as PFC in high frequency ballast half bridge voltage fed topology

Applications

- Electronic ballast for fluorescent lighting
- Table 1



Figur : Internal scheniatic diagram



Order code Marking		Package	Packaging		
BUL805	BUL805	TO-220	Tube		

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BUL805 Electrical ratings

1 Electrical ratings

Table 2. Absolute maximum rating

Symbol	Parameter	Value	Unit
V _{CES}	Collector-emitter voltage (V _{BE} = 0)	800	V
V _{CEO}	Collector-emitter voltage (I _B = 0)	450	V
V _{EBO}	Emitter-base voltage ($I_C = 0$)	9	V
I _C	Collector current	5	Α
I _{CM}	Collector peak current (t _P < 5ms)	10	А
I _B	Base current	2	Α
I _{BM}	Base peak current (t _P < 5ms)	4	Α
P _{tot}	Total dissipation at T _C = 25°C	80	W
T _{stg}	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 3. Thermal data

	Symbol	Parameter	18/10	Value	Unit
	R _{thj-case}	Thermal resistance jurction-case	max	1.56	°C/W
	R _{thj-amb}	Thermal resistance junction-amb	max	62.5	°C/W
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Electrical characteristics BUL805

Electrical characteristics 2

(T_{case} = 25 °C unless otherwise specified)

Table 4. **Electrical characteristics**

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CES}	Collector cut-off current (V _{BE} = 0)	V _{CE} = 800 V V _{CE} = 800 V T _C = 125 °C			100 500	μ Α μ Α
I _{CEO}	Collector cut-off current (I _B = 0)	V _{CE} = 450 V			250	μА
V _{EBO}	Emitter-base voltage (I _C = 0)	I _E = 10 mA	9			V
V _{CEO(sus)} (1)	Collector-emitter sustaining voltage (I _B = 0)	I _C = 10 mA	150	O's	*19	V
V _{CE(sat)} (1)	Collector-emitter saturation voltage	$I_C = 1 \text{ A}$ $I_B = 0.2 \text{ A}$ $I_C = 2 \text{ A}$ $I_S = 0.4 \text{ A}$ $I_C = 3 \text{ A}$ $I_B = 0.6 \text{ A}$	· 4 O	90	0.4 0.6 0.8	V V V
		$I_C = 1 \text{ A}$ $I_B = 0.2 \text{ A}$			1.1	V
V _{BE(sat)} (1)	Base-emitter saturation voltage	$I_C = 2 \text{ A}$ $I_B = 0.4 \text{ A}$			1.3	V
BE(out)		$I_C = 3 \text{ A}$ $I_B = 0.6 \text{ A}$			1.5	V
h _{FF}	DC current gain	$I_C = 10 \text{ mA}$ $V_{CE} = 5 \text{ V}$	10			
^I IFE	DC current gain	$I_C = 2 A$ $V_{CE} = 5 V$	10		20	
(1) Pulsed du	DC current gain	≤ 1.5%				

3 Package mechanical data

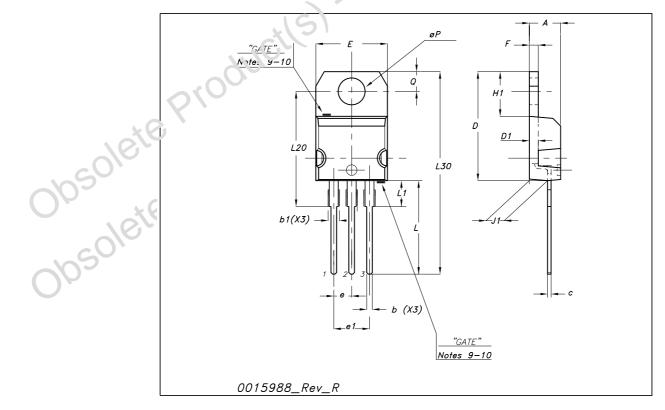
In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

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TO-220 mechanical data

D!		mm			inch	
Dim	Min	Тур	Max	Min	Тур	Max
А	4.40		4.60	0.173		0.181
b	0.61		0.88	0.024		0.034
b1	1.14		1.70	0.044		0.066
С	0.48		0.70	0.019		0.127
D	15.25		15.75	0.6		7.62
D1		1.27			0.050	
E	10		10.40	0.393		0.409
е	2.40		2.70	0.094		0.106
e1	4.95		5.15	0.104		0.202
F	1.23		1.32	0.003		0.051
H1	6.20		6.60	0.2.`4		0.256
J1	2.40		2.72	0.094		0.107
L	13		14	0.511		0.551
L1	3.50		3.73	0.137		0.154
L20		16.40			0.645	
L30		28.70			1.137	
Ø₽	3.75		3.85	0.147		0.151
Q	2.65		2.95	0.104		0.116



BUL805 Revision history

4 Revision history

Table 5. Document revision history

Date	Revision	Changes
19-May-2006	1	Initial release.
15-Jul-2008	2	Document status promoted from preliminary data to datasheet.

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