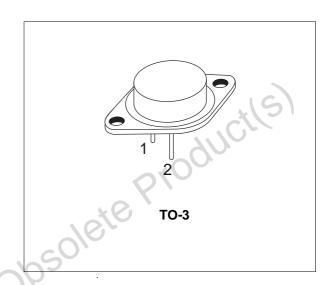


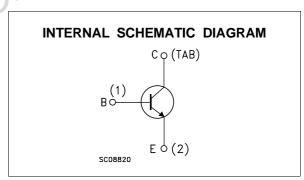
# SILICON NPN POWER TRANSISTOR

 STMicroelectronics PREFERRED SALESTYPE

### **DESCRIPTION**

The MJ802 is a silicon Epitaxial-Base power transistor mounted in Jedec TO-3 metal case. It is intended for general purpose power amplifier and switching applications.





# ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V <sub>CEO</sub>	Collector-emitter Voltage (I <sub>B</sub> = 0)	90	V
V <sub>CBO</sub>	Collector-base Voltage (I <sub>E</sub> = 0)	100	V
V <sub>ЕВО</sub>	Emitter-Base Voltage (Ic = 0)	4	V
I <sub>C</sub>	Collector Current	30	Α
I <sub>B</sub>	Base Current	7.5	Α
P <sub>tot</sub>	Total Dissipation at $T_c \le 25$ °C	200	W
T <sub>stg</sub>	Storage Temperature	-65 to 200	°C
Tj	Max. Operating Junction Temperature	200	°C

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## THERMAL DATA

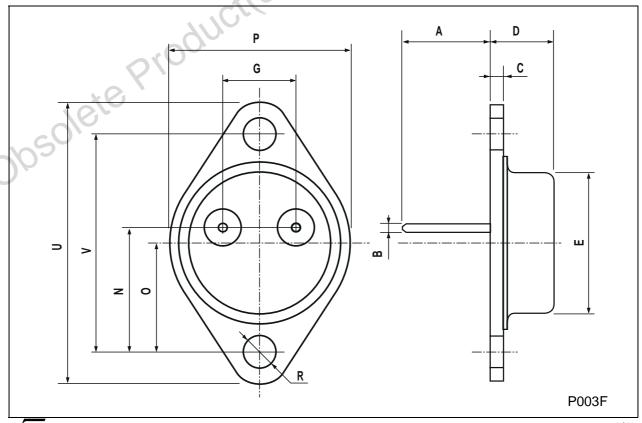
# **ELECTRICAL CHARACTERISTICS** ( $T_{case} = 25$ °C unless otherwise specified)

C	Eurrent ( $I_E = 0$ )  Emitter Cut-off Current $I_C = 0$ )  Collector-Emitter Sustaining Voltage $I_B = 0$ )  Collector-emitter Sustaining Voltage $I_B = 1000$ Collector-Emitter Saturation Voltage $I_B = 1000$	$V_{CB} = 100 \text{ V}$ $V_{CB} = 100 \text{ V}$ $V_{CB} = 4 \text{ V}$ $I_{C} = 200 \text{ mA}$ $I_{C} = 200 \text{ mA}$ $I_{C} = 7.5 \text{ A}$ $I_{C} = 7.5 \text{ A}$	$T_{case} = 150  {}^{\circ}C$ $I_{B} = 0.75  A$ $I_{B} = 0.75  A$	90	091/	0.8	mA mA V V V
VCEO(sus)* CS (II VCER(sus)* CS (II VCE(sat)* CS VBE(sat)* BS VBE* B	$I_{\rm C}=0)$ Collector-Emitter Sustaining Voltage $I_{\rm B}=0)$ Collector-emitter Sustaining Voltage $R_{\rm BE}=100~\Omega)$ Collector-Emitter Saturation Voltage Base-Emitter Saturation Voltage	I <sub>C</sub> = 200 mA I <sub>C</sub> = 200 mA I <sub>C</sub> = 7.5 A			09/1	Ctl	V
VCER(sus)* CS (IF VCE(sat)* CS VBE(sat)* BS VBE* B	Sustaining Voltage  I <sub>B</sub> = 0)  Collector-emitter Sustaining Voltage  R <sub>BE</sub> = 100 \( \Omega\)  Collector-Emitter Saturation Voltage  Base-Emitter Saturation Voltage	I <sub>C</sub> = 200 mA			091,	0.8	V
VCE(sat)* CS VBE(sat)* BS VBE* B	Sustaining Voltage  R <sub>BE</sub> = 100 Ω)  Collector-Emitter Saturation Voltage  Base-Emitter Saturation Voltage	I <sub>C</sub> = 7.5 A		100	ogi	0.8	•
V <sub>BE</sub> (sat)* B S V <sub>BE</sub> * B	Saturation Voltage Base-Emitter Saturation Voltage			91	0	0.8	V
S V <sub>BE</sub> * B	Saturation Voltage	I <sub>C</sub> = 7.5 A	I <sub>B</sub> = 0.75 A				
	Base-Emitter Voltage					1.3	V
hee* D		$I_{C} = 7.5 A$	$V_{CE} = 2 V$			1.3	V
IIFE: D	OC Current Gain	$I_{C} = 7.5 A$	$V_{CE} = 2 V$	25		100	
		$I_C = 1 A$ f = 1 MHz	V <sub>CE</sub> = 10 V	2			MHz
10	duration = 300 μs, duty cycle 1.	ci(S)					

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# **TO-3 MECHANICAL DATA**

DIM.	mm		inch			
2	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А	11.00		13.10	0.433		0.516
В	0.97		1.15	0.038		0.045
С	1.50		1.65	0.059		0.065
D	8.32		8.92	0.327		0.351
Е	19.00		20.00	0.748	41)	0.787
G	10.70		11.10	0.421	2100	0.437
N	16.50		17.20	0.649		0.677
Р	25.00		26.00	0.984		1.023
R	4.00		4.09	0.157		0.161
U	38.50		39.30	1.515		1.547
V	30.00	16	30.30	1.187		1.193



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