

MSS40, MSS50

Back to back SCR module

Table 1.Main features

Symbol	Value	Unit
I _{T(RMS)}	55 and 70	А
V _{DRM} /V _{RRM}	800 and 1200	V
I _{GT}	50	mA

Description

Packaged in ISOTOP modules, the MSS40 / MSS50 Series is based on two back-to-back SCR configurations, providing high noise immunity. They are suitable for high power applications such as solid state relays, heating control systems, welding equipment, motor control circuits...

The compactness of the ISOTOP package allows high power density and optimized power bus connections. Thanks to their internal ceramic pad, they provide high voltage insulation (2500 V_{RMS}), complying with UL standards (File ref: E81734).

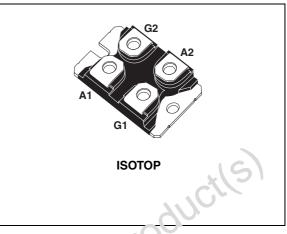
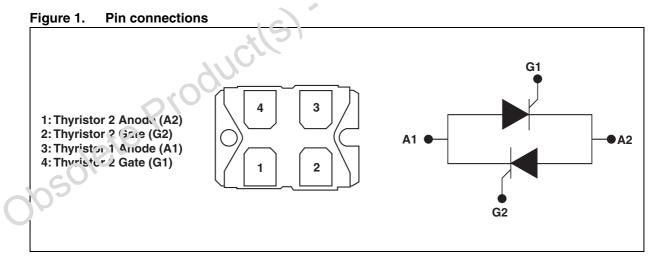


Table Z. Urder Jun's	Table 2.	Order Jugas
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Part numbors	Marking
MSE41-1200	MSS40-1200
M.3850-800	MSS50-800
MSS50-1200	MSS50-1200



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1 Characteristics

Symbol	Parameter			Value		Unit
Symbol	Parameter				MSS50	Unit
V _{DRM} /V _{RRM}	Repetitive peak off-state voltage			1200	800 1200	V
			$T_c = 80^\circ C$	55		Α
I _{T(RMS)}		IS on-state current			70	A
I .	Non repetitive surge peak on-state	t _p = 16.7 ms	– T _j = 25° C	420	630	
ITSM	current	t _p = 20 ms		400	600	Α
l ² t	I ² t Value for fusing	t _p = 10 ms	$T_j = 25^\circ C$	800	1800	A ² s
dl/dt	$ \begin{array}{c} \mbox{Critical rate of rise of on-state current} \\ I_G = 2 \ x \ I_{GT} \ , \ t_r \leq 100 \ ns \end{array} \hspace{0.2cm} F = 120 \ Hz \qquad T_j = 125^{\circ} \ C \\ \end{array} $		$T_j = 125^\circ C$	5	0	A/µs
I _{GM}	Peak gate current $t_p = 20 \ \mu s$ $T_j = 125^{\circ} \ C$		2	4	A	
P _{G(AV)}	Average gate power dissipation $T_j = 125^{\circ} C$		1		W	
T _{stg} T _i	Storage junction temperature range Operating junction temperature range			+ 150 + 125	° C	
V _{RGM}	Maximum peak reverse gate voltage		5		V	

Table 3. Absolute ratings (limiting values)

Table 4.Electrical characteristics ($T_j = 25^\circ C$, unless otherwise specified)

Symbol	Test Conditions			Value		Unit
Symbol				MSS40	MSS50	onit
1		N	MIN.	5	5	mA
I _{GT}	$V_D = 12 V$ $R_L = 33 \Omega$		MAX.	50		ША
V _{GT}	т			1.3		V
V _{GD}	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$	MIN.	0.2		V	
Ι _Η	I _T = 500 mA Gate open	MAX.	80		mA	
١L	I _G = 1.2 I _{GT}	MAX.	120		mA	
dV/dt	$V_D = 67 \% V_{DRM}$ Gate open $T_j = 125^{\circ} C$		MIN.	10	00	V/µs
V	I _{TM} = 80 A t _p = 380 μs			1.7		V
V _{TM}	I _{TM} = 100 A t _p = 380 μs				1.7	v
V _{t0}	Threshold voltage $T_j = 125^{\circ} C$		MAX.	0.85		V
R _d	Dynamic resistance	$T_j = 125^\circ C$	MAX.	11	7	mΩ
IDRM	V _{DRM} = V _{RRM}	$T_j = 25^\circ C$	MAX.	20		μA
I _{RRM}	VDRM - VRRM	$T_j = 125^\circ C$	$r_j = 125^{\circ} C$		10	

Table 5.Thermal reistances

Symbol	Parameter	Value	Unit	
R _{th(j-c)} Junction to case (AC)	lunction to cope (AC)	MSS40	0.6	° C/W
	Junction to case (AC)	MSS50	0.45	0/10



Figure 2. Maximum average power dissipation versus average on-state current

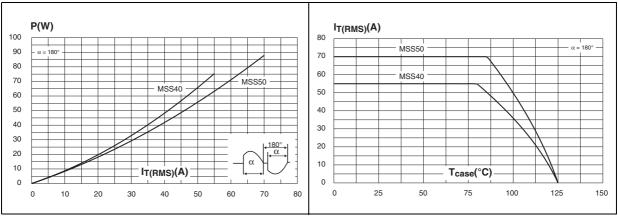
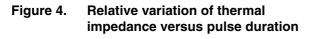
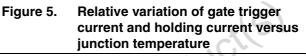


Figure 3.





Average and DC on-state current

versus case temperature

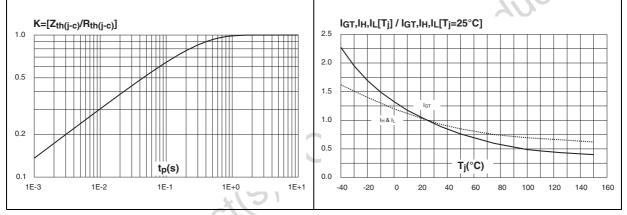
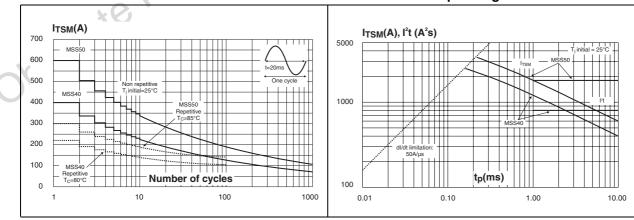


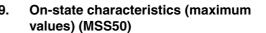
Figure 6. Surge peak on-state current versus Figure 7. number of cycles

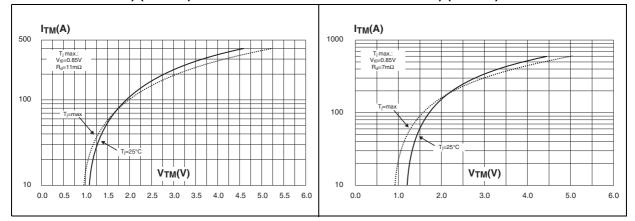
Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10$ ms, and corresponding values of l^2t



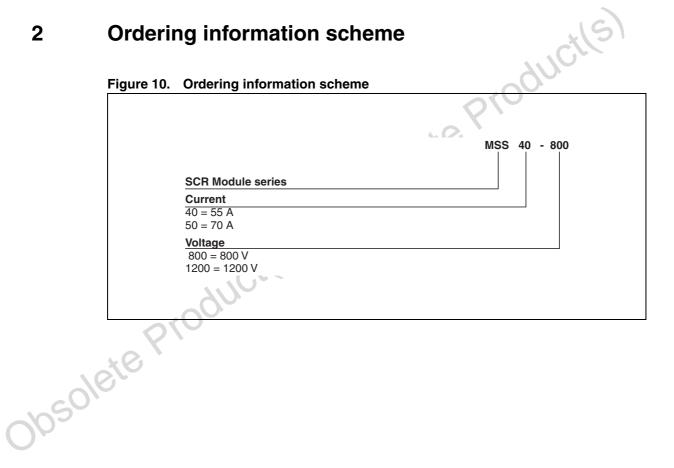
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On-state characteristics (maximum Figure 9. Figure 8. values) (MSS40)





Ordering information scheme 2





3 Package information

- Epoxy meets UL94, V0
- Cooling method: by conduction (C)
- Recommended torque value: 0.9 Nm (max. 1.2 Nm) for the 6 x M4 screws (2 x M4 screws recommended for mounting the package on the heatsink and the 4 provided screws).
- The screws supplied with the package are adapted for mounting on a board (or other types of terminals) with a thickness of 0.6 mm min. and 2.2 mm max.

Dimensions Millimeters Ref. Inches Min. Max. Min. Max. 11.80 12.20 0.465 0.480 А G2 С 8.90 9.10 0.350 A1 0.358 В 7.8 8.20 0.307 0.323 Δ A С 0.85 0.75 0.030 0.033 C2 E2 C2 1.95 2.05 0.077 0.081 D 37.80 38.20 1.488 1.504 D1 31.50 31.70 1.240 1.248 Е 25.15 25.50 0.990 1.004 23.85 24.15 0.939 0.951 E1 G D1 E2 24.80 typ. 0.976 typ. S D G 14.90 15.10 0.587 0.594 в G1 12.60 12.80 0.496 0.504 G2 3.50 4.30 0.138 0.169 F 4.10 4.30 0.161 0.169 ØP Jbsolete G1 5.00 F1 4.60 0.181 0.197 E1 Р 4.00 4.30 0.157 0.69 P1 4.00 4.40 0.157 0.173 S 30.10 30.30 1.185 1.193

Table 6. ISOTOP dimensions

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

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4 Ordering information

Table 7. Ordering information

Part number	Marking	Package	Weight	Base qty	Delivery mode
MSS40-1200	MSS40-1200		07	10	
MSS50-800	MSS50-800	ISOTOP	27 g (without screws)	10 (with screws)	Tube
MSS50-1200	MSS50-1200		(minour conone)	(mar corono)	

5 Revision history

	Date	Revision	Changes
	Sep-2000	3	Last release.
	11-Jul-2007	4	Reformated to current standards. Removed MSS40-800 product.
obsole	stepro	ducil	obsolete Pros

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