

Complementary power transistors

Features

Complementary NPN-PNP devices

Applications

Power linear and switching

Description

The device is manufactured in Planar technology with "Base Island" layout. The resulting transistor shows exceptional high gain performance coupled with very low saturation voltage. The PNP type is BD244C.

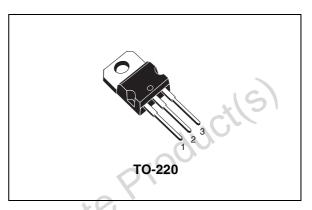
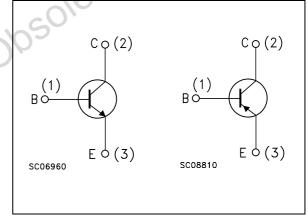


Figure 1. Internal schematic diagram



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Table 1.	Device summary

Order code	Marking	Package	Packaging
BD243C BD244C	BD243C BD244C	TO-220	Tube

Absolute maximum ratings 1

Table 2.	Absolute maximum ratings
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Symbol		Parameter	Value		Unit
			BD243C (NPN)	BD244C (PNP)	
	V _{CBO}	Collector-base voltage ($I_E = 0$)	10	00	V
	V _{CEO}	Collector-emitter voltage (I _B = 0)	lector-emitter voltage (I _B = 0) 100		
	V _{EBO}	Emitte-base voltage (I _C = 0)	Ę	5	V
	Ι _C	Collector current	6	3 10	А
	I _{CM}	Collector peak current (t _P < 5ms)	1	0	А
	Ι _Β	Base current	2		А
	P _{TOT}	Total dissipation at $T_c = 25^{\circ}C$	6	5	W
	T _{stg}	Storage temperature	-65 to	o 150	°C
	TJ	Max. operating junction temperature	15	50	°C
	ete P	roducils			

2 Electrical characteristics

 $(T_{case} = 25^{\circ}C; unless otherwise specified)$

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I _{CES}	Collector cut-off current $(V_{BE} = 0)$	V _{CE} = 100V			0.4	mA
I _{CEO}	Collector cut-off current $(I_B = 0)$	V _{CE} = 60V			0.7	mA
I _{EBO}	Emitter cut-off current (I _C = 0)	V _{EB} = 5V		(mA
V _{CEO(sus)} ⁽¹⁾	Collector-emitter sustaining voltage (I _B = 0)	I _C = 30mA	100	20		V
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	I _C = 6A I _B = 1A			1.5	v
V _{BE} ⁽¹⁾	Base-emitter voltage	I _C = 6A I _B = 1A			2	V
h _{FE} ⁽¹⁾	DC current gain	$I_{C} = 0.3mA \qquad V_{CE} = 4V$ $I_{C} = 3A \qquad V_{CE} = 4V$	30 15			

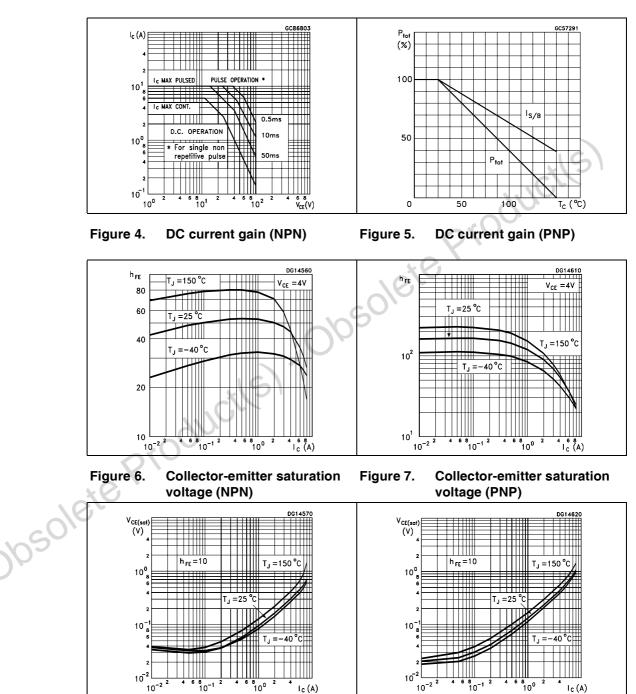
Table 3. Electrical characteristics

1. Pulsed duration = 300 ms, duty cycle \leq 2%.

Note: For PNP types voltage and current values are negative.

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2.1 **Typical characteristics**

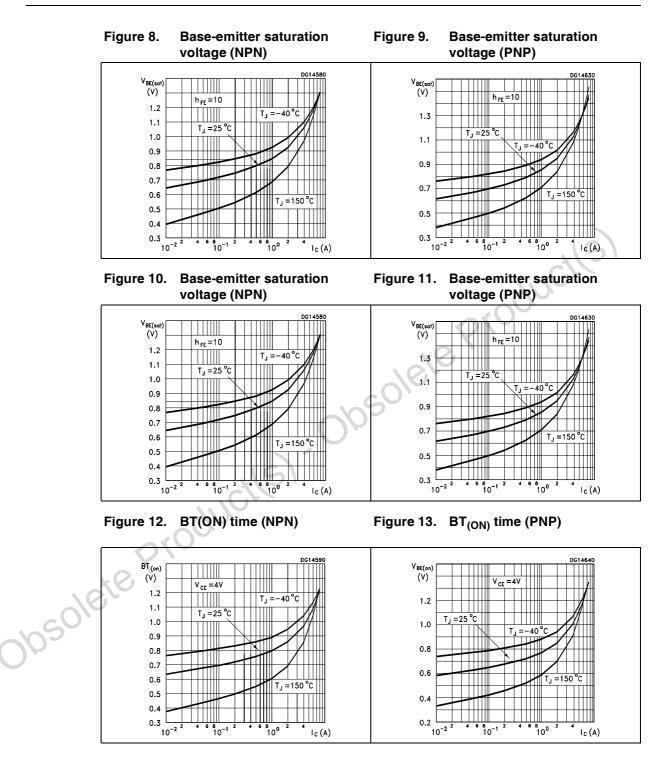


I_C (A)

Figure 2. Safe operating area

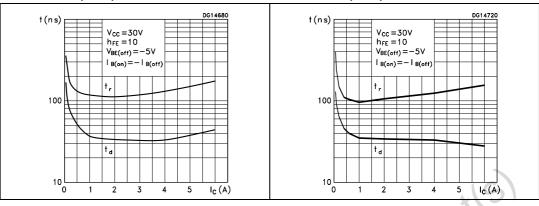
Figure 3. **Derating curve**

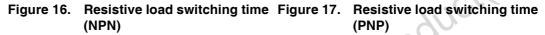
I_C (A)



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Figure 14. Resistive load switching time Figure 15. Resistive load switching time (NPN) (PNP)





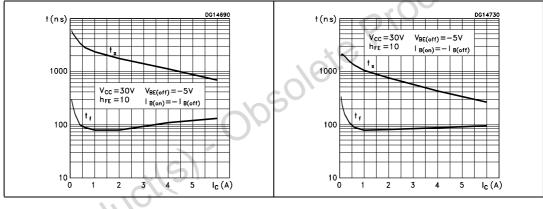
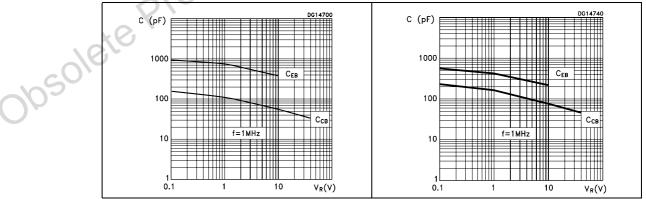


Figure 18. Collector-base and collector- Figure 19. Collector-base and collectoremitter capacitance (NPN) emitter capacitance (PNP)



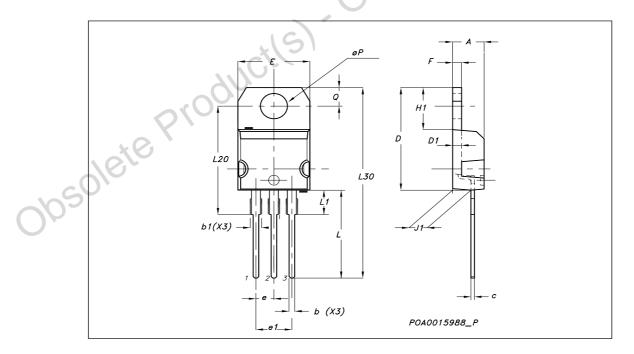


3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available obsolete Product(s). Obsolete Product(s) at: www.st.com



TO-220 Mechanical data				
DIM.				
	MIN.	ТҮР	MAX.	
A	4.40		4.60	
b	0.61		0.88	
b1	1.14		1.70	
с	0.49		0.70	
D	15.25		15.75	
D1		1.27	1.0	
E	10		10.40	
е	2.40		2.70	
e1	4.95		5.15	
F	1.23		1.32	
H1	6.20		6.60	
J1	2.40		2.72	
L	13		14	
L1	3.50		3.93	
L20		16.40		
L30		28.90		
øP	3.75	100	3.85	
Q	2.65		2.95	



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4 Revision history

Table 4. Revision history

	Date	Revision	Changes
	13-Sep-2005	4	New datasheet according to MLD-PWR/05/1267
	25-Jul-2007	5	Figures 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18 and figure 19, added
obsole	teptod	uctl	figure 19, added

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