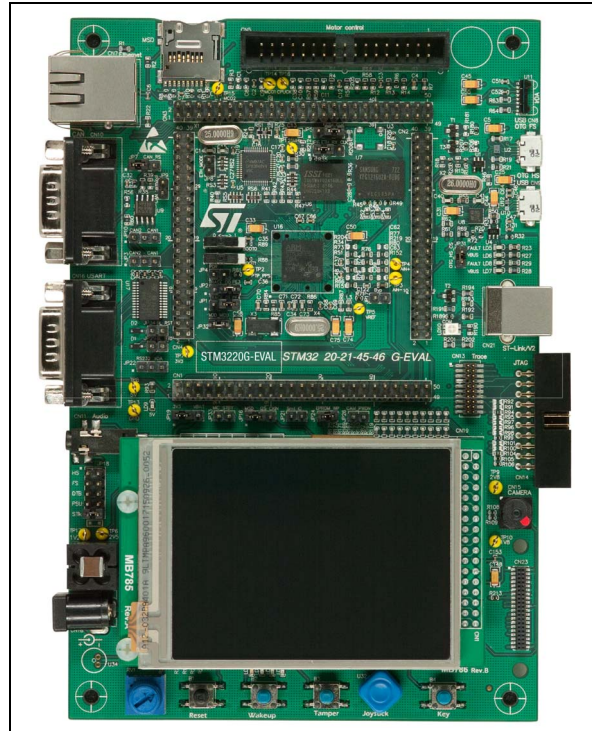


Features

- 16-Mbit SRAM
- 1-Gbyte or more microSD™ card
- Boot from user Flash memory, system memory or SRAM
- Both ISO/IEC 14443 type A and B smartcard support
- I²C compatible 64-Kbit serial interface EEPROM, ST MEMS and I/O expander
- IEEE 802.3-2002 compliant ethernet connector
- Two CAN 2.0 A/B channels on the same DB connector
- RS-232 communication
- IrDA transceiver (supported up to version MB786 C-07 of the board)
- USB OTG (HS and FS) with Micro-AB connector
- Inductor motor control connector
- I²S audio DAC, stereo audio jack for headset
- 3.2" 240x320 TFT color LCD with touch screen
- 4-color LEDs
- Camera module and extension connector for ST camera plug-in
- Joystick with 4-direction control and selector
- Reset, wakeup, tamper and user button
- RTC with backup battery
- Extension connector for daughterboard or wrapping board
- JTAG, SW and trace debug support
- Embedded ST-LINK/V2
- Five 5V power supply options: power jack, USB FS connector, USB HS connector, ST-LINK/V2 or daughterboard



1. Picture is not contractual.

Description

The STM3220G-EVAL evaluation board is a complete demonstration and development platform for STMicroelectronics ARM®Cortex®-M3 core-based STM32F2 Series and embeds a high-performance STM32F207IGH6.

The full range of hardware features on the board helps users to evaluate all the peripherals (USB OTG HS, USB OTG FS, ethernet, motor control, CAN, microSD™ card, smartcard, USART, audio DAC, RS-232, IrDA transceiver supported up to board version MB786 C-07, SRAM, ST MEMS, EEPROM and others) and to develop applications. The embedded in-circuit ST-LINK/V2 provides debugger and programmer facilities for the STM32.

1 System requirements

- Windows® OS (XP, 7, 8)
- USB type A to type B or Mini-B cable

2 Development toolchains

- IAR EWARM (IAR Embedded Workbench®)
- Keil® MDK-ARM™
- GCC-based IDEs (free AC6: SW4STM32, Atollic® TrueSTUDIO®,...)

3 Demonstration software

Demonstration software is preloaded in the STM32F207IGH6 Flash memory for easy demonstration of the device peripherals in standalone mode. For more information and to download the latest version, refer to STM3220G-EVAL demonstration software at the www.st.com website.

4 Ordering information

To order the STM3220G-EVAL evaluation board, refer to [Table 1](#):

Table 1. Ordering information

Order code	Target STM32
STM3220G-EVAL	STM32F207IGH6

5 Revision history

Table 2. Document revision history

Date	Revision	Changes
24-Mar-2011	1	Initial release.
25-Oct-2016	2	Updated IrDA transceiver version in <i>Features</i> and <i>Description</i> . Added sections: <i>Section 1: System requirements</i> , <i>Section 2: Development toolchains</i> .

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