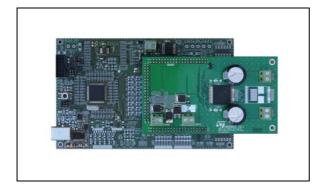


## STEVAL-VNH5019A

#### Motor driver evaluation board based on VNH5019A

Data brief



#### **Features**

Туре	R <sub>DS(on)</sub>	l <sub>out</sub>	V <sub>CC(max)</sub>
VNH5019A-E	18 mΩ typ (per leg)	30 A	41 V

- Handling up to 30 A of maximum motor current output
- Undervoltage and overvoltage shutdown
- Device thermal protection
- Cross-conduction protection
- Very low standby power consumption
- Programmable PWM operation (up to 20 kHz)
- Protection against loss of ground and loss of V<sub>CC</sub>
- Motor current monitoring (thanks to VNH5019A current sense output)
- On-board charge pump output for reverse polarity protection
- Device output protected against short to ground and short to V<sub>CC</sub>
- Graphic User Interface (GUI)

### **Description**

STEVAL-VNH5019A offers dedicated power stage and controls suitable for electric DC motor driving. This evaluation board comes pre-assembled with VNH5019A H-bridge belonging to the VNH Motor Driver series based on VIPower<sup>®</sup> proprietary technology. Typical applications are Windows Lift, Sliding Door, Seat regulation.

This Evaluation Board consists of a motherboard (STM8 Universal Board) and a daughterboard.

The motherboard, based on STM8 microcontroller, provides the logic section for monitoring and driving the VNH5019A assembled in the daughterboard.

With the aim to make simpler the board usage and settings, ST provides a dedicated and user-friendly software with a Graphic User Interface (GUI). This enables the user to set VNH5019A parameters (PWM, Motor direction...) and at the same time it shows real time device diagnostic information like current output evolution, battery voltage monitoring, board temperature and much more.

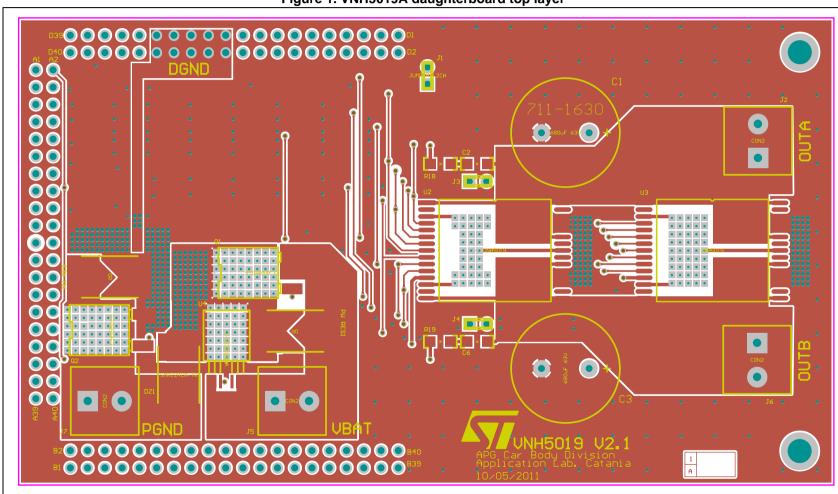
Table 1. Device summary

Order code	Reference
STEVAL-VNH5019A	VNH5019A evaluation board

## 1 Application schematics and layouts

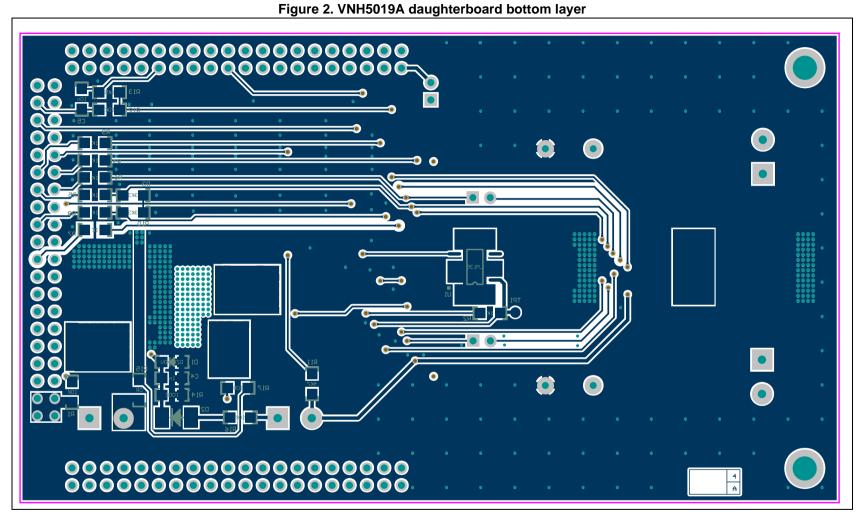
### 1.1 VNH5019A daughterboard

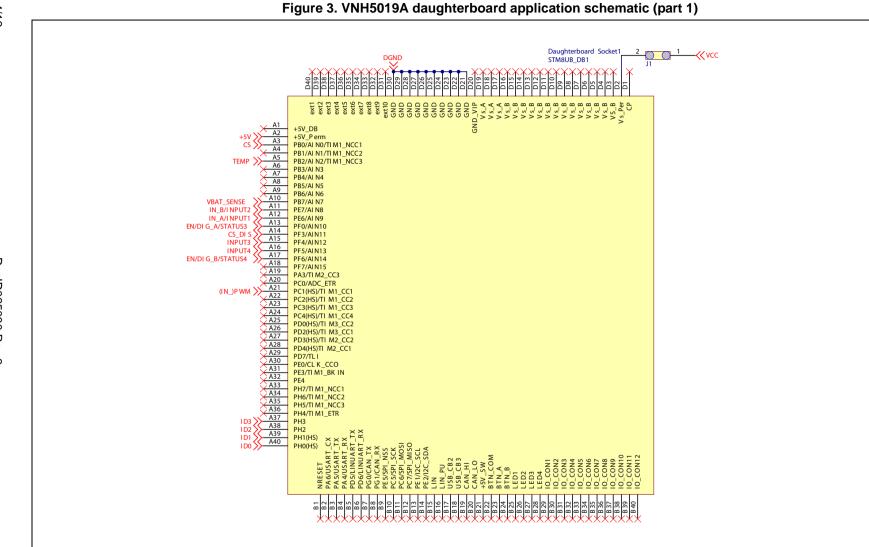
Figure 1. VNH5019A daughterboard top layer



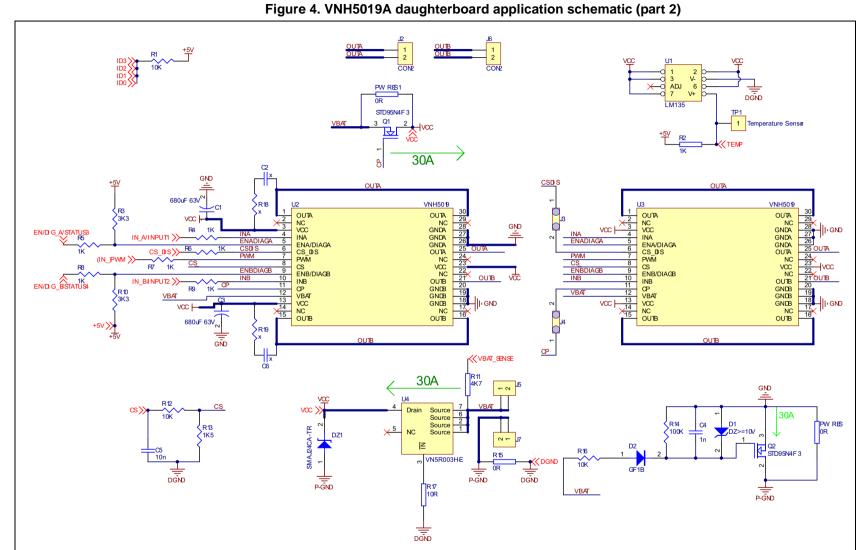








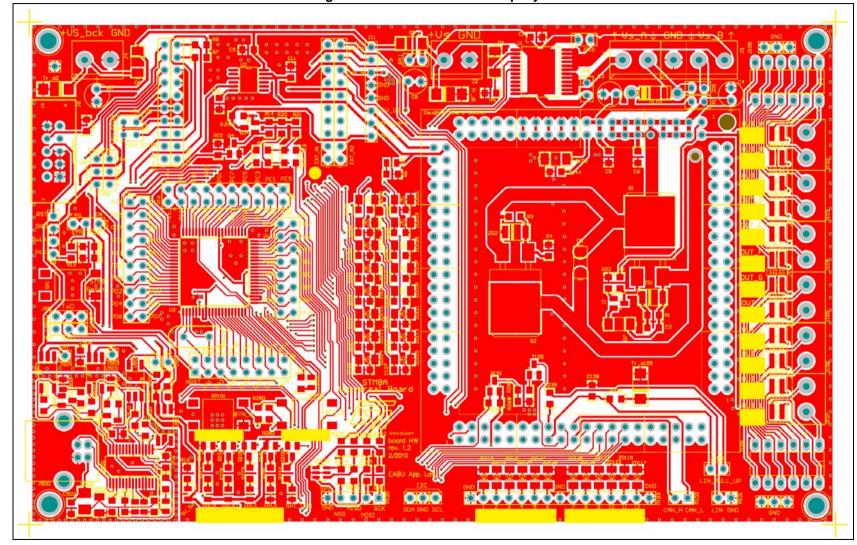




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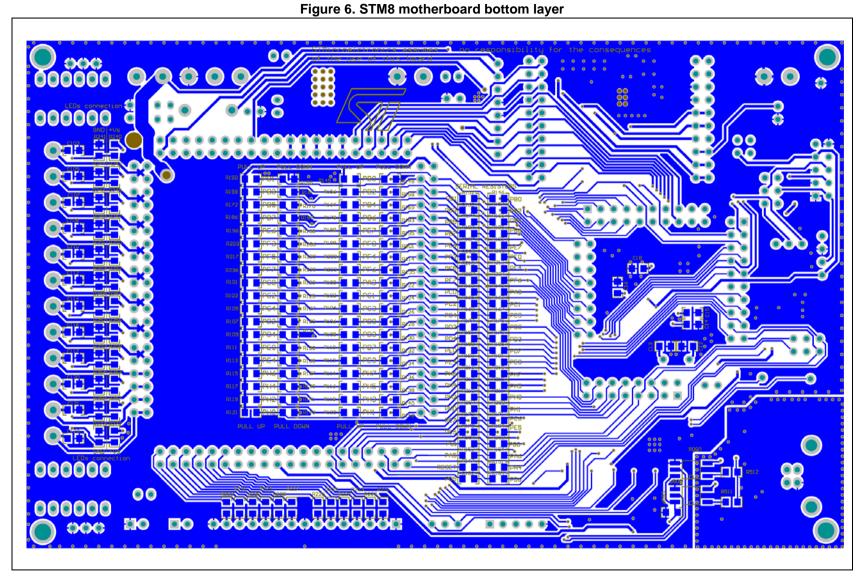
#### 1.2 **STM8** motherboard

Figure 5. STM8 motherboard top layer

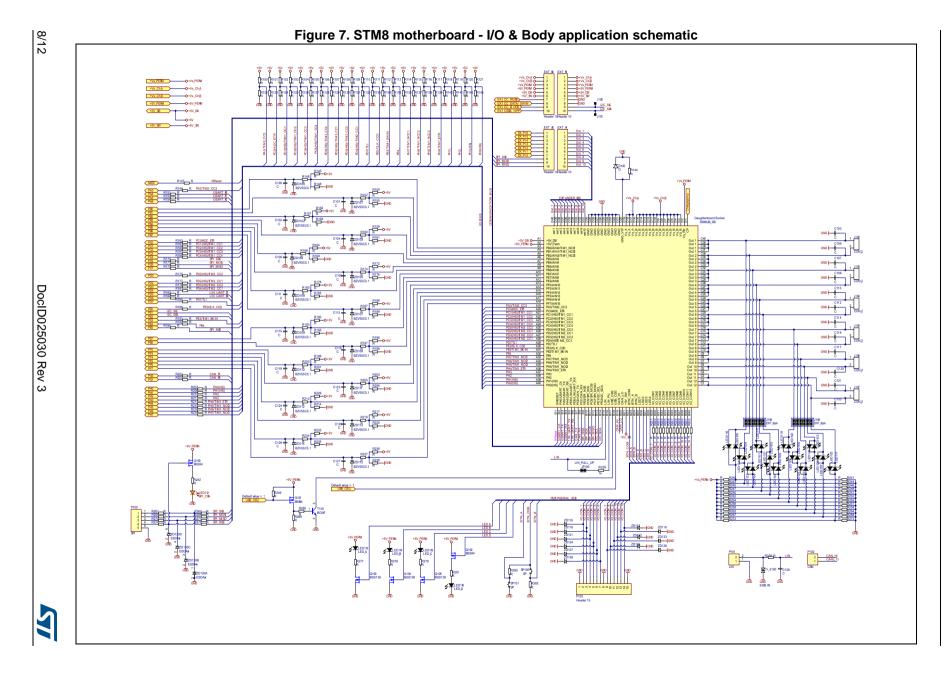




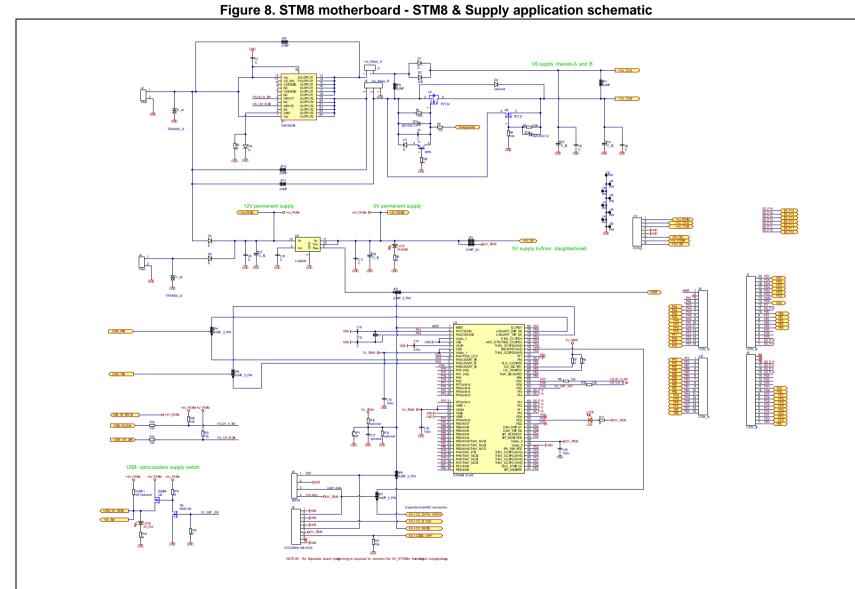




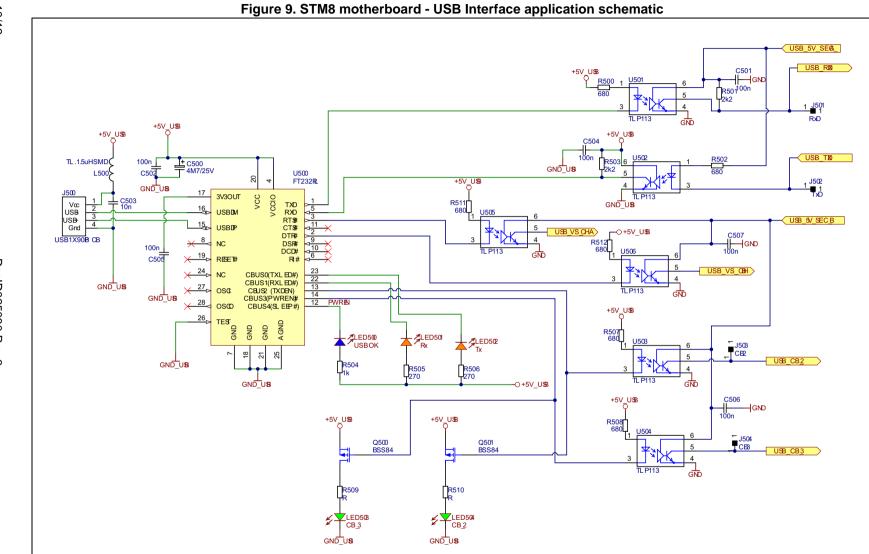
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STEVAL-VNH5019A Revision history

# 2 Revision history

Table 2. Document revision history

Date	Revision	Changes
22-Jul-2013	1	Initial release.
06-Sep-2013	2	Updated Section 1.2: STM8 motherboard
16-Sep-2013	3	Updated disclaimer.

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