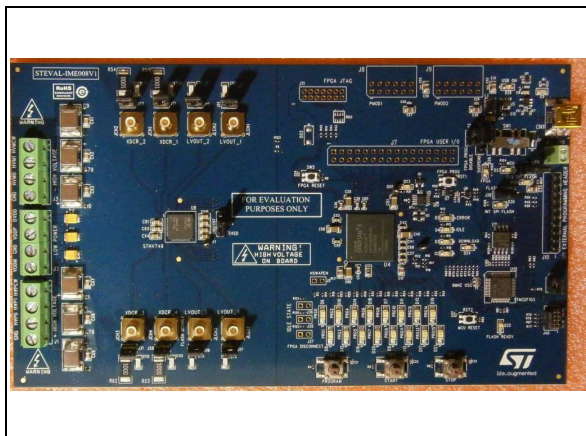


STHV749 Ultrasound Pulser IC evaluation board

Data brief



Description

The STEVAL-IME008V1 is a product evaluation board designed around the STHV749 4-channel 7-level high voltage pulser, a state-of-the-art device designed for ultrasound imaging applications.

The output waveforms can be displayed directly on an oscilloscope by connecting the scope probe to the relative BNCs. 16 preset waveforms are available to test the HV pulser under varying conditions.

Features

- 4-channel outputs: high voltage and low voltage BNC connectors
- Load simulator using signal equivalent circuits
- Possibility to set up own load simulator
- 16 preset waveforms
- USB connector to connect STM32 with PC and supply power to it
- 4 MB serial Flash memory to host FPGA code and waveforms
- Memory expansion connector to add external serial Flash
- Connectors to supply high voltage and low voltage to the STHV749 output stage
- LEDs to monitor the power management stage
- Human machine interface to select, start and stop the generation of the preset waveforms
- 25 LEDs to monitor board behavior
- RoHS compliant

1 Schematic diagrams

Figure 1. STEVAL-IME008V1 circuit schematic (1 of 16)

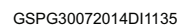


Figure 2. STEVAL-IME008V1 circuit schematic (2 of 16)

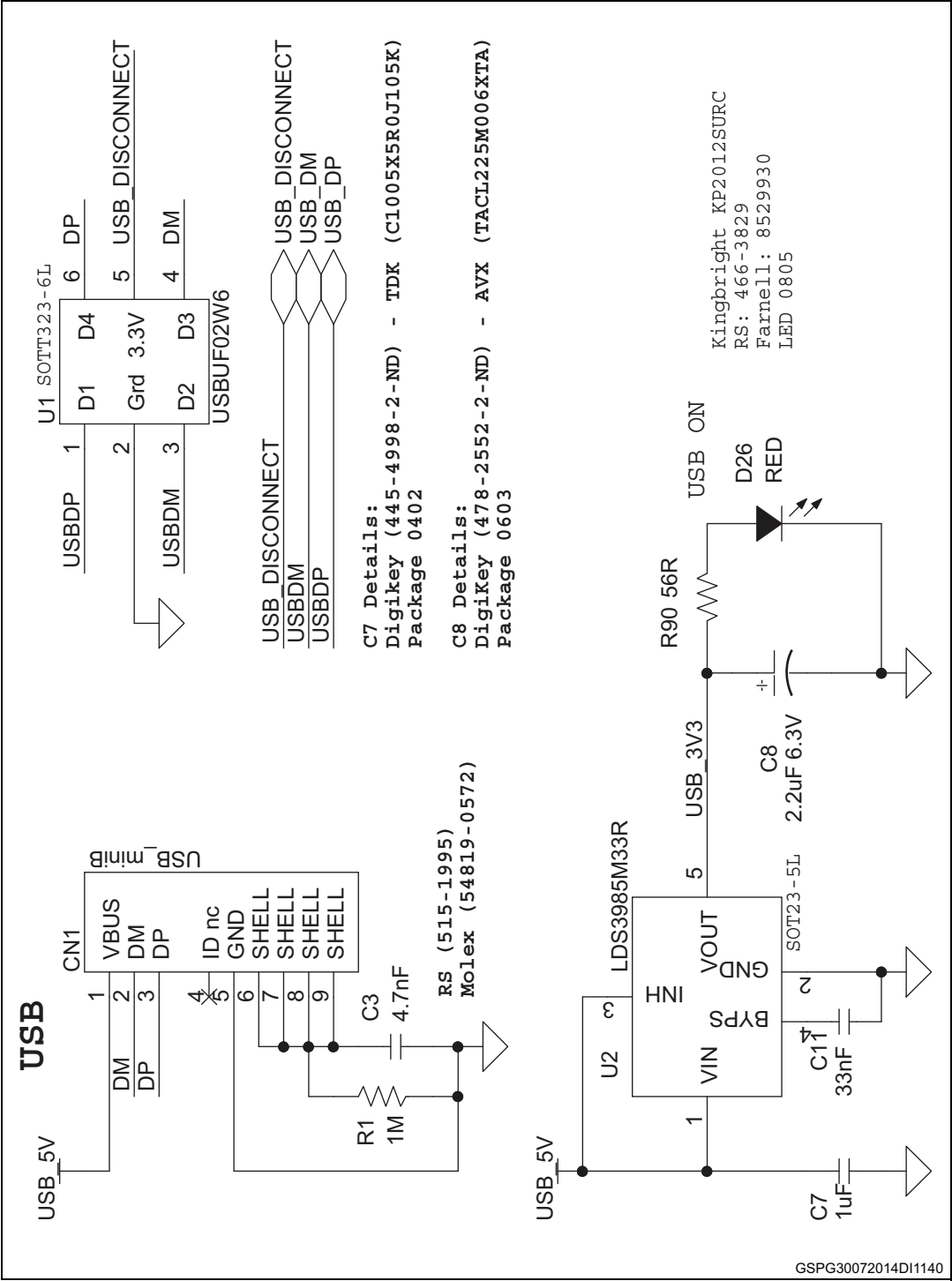


Figure 3. STEVAL-IME008V1 circuit schematic (3 of 16)

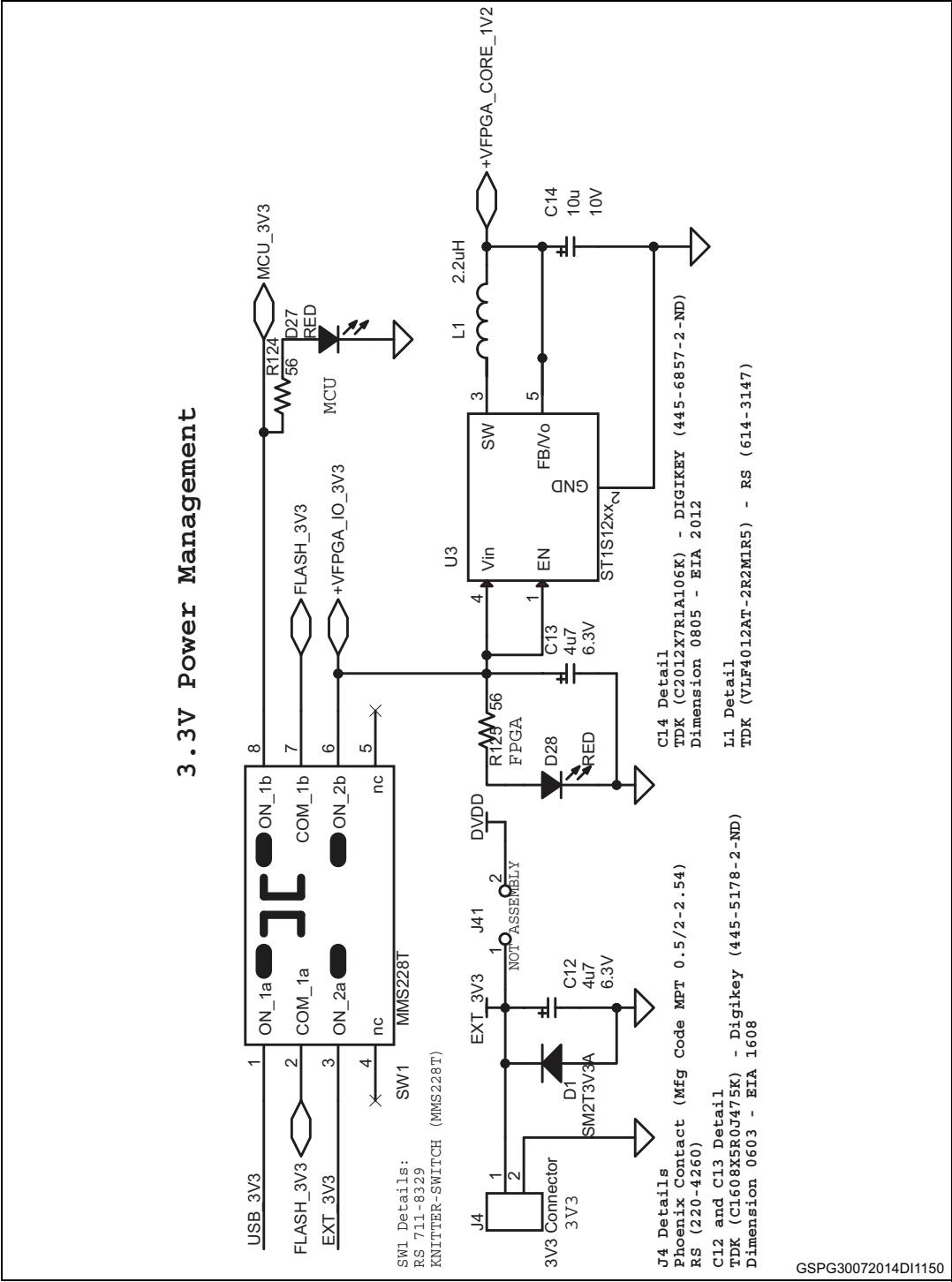


Figure 4. STEVAL-IME008V1 circuit schematic (4 of 16)

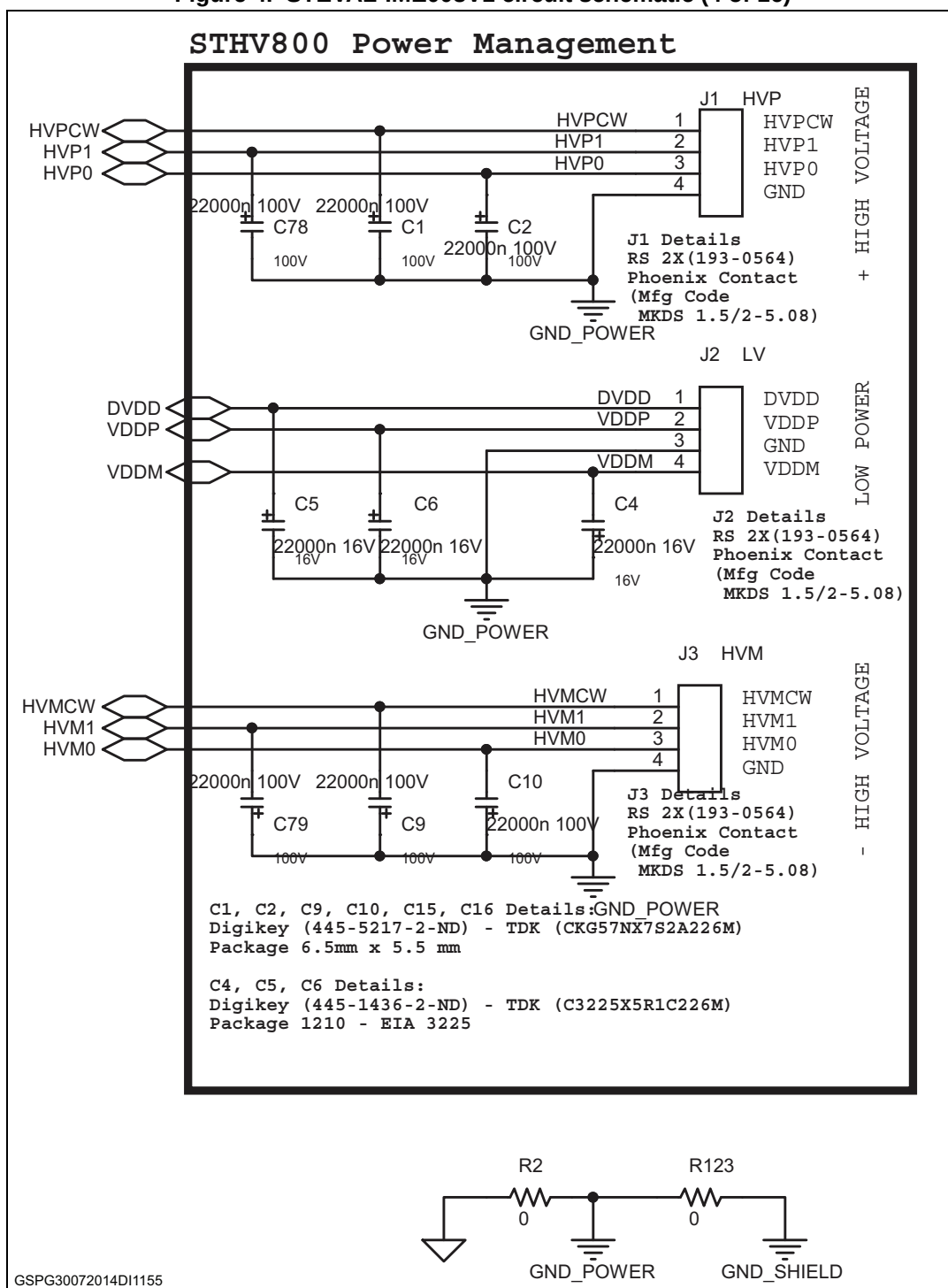


Figure 5. STEVAL-IME008V1 circuit schematic (5 of 16)

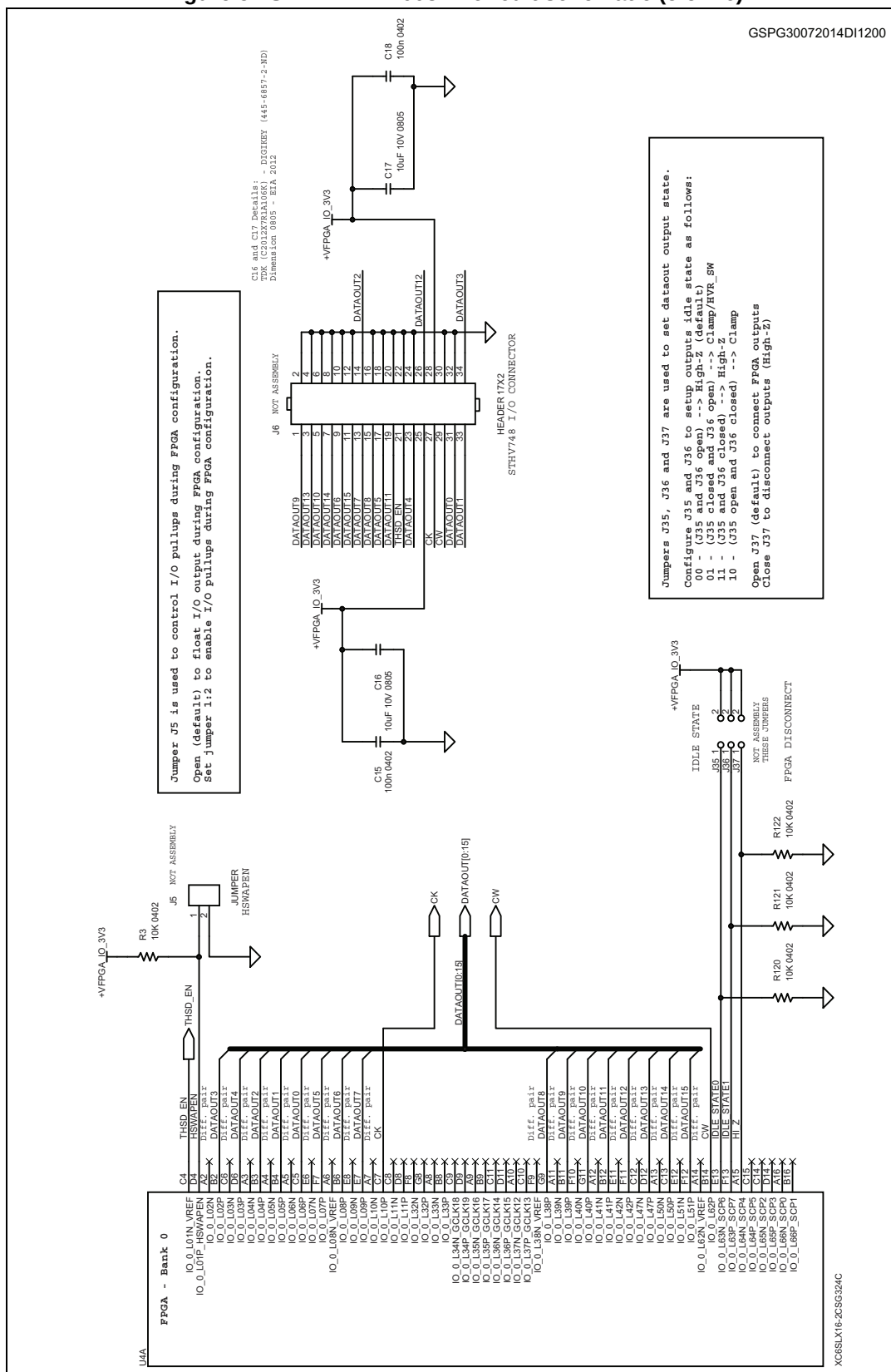


Figure 6. STEVAL-IME008V1 circuit schematic (6 of 16)

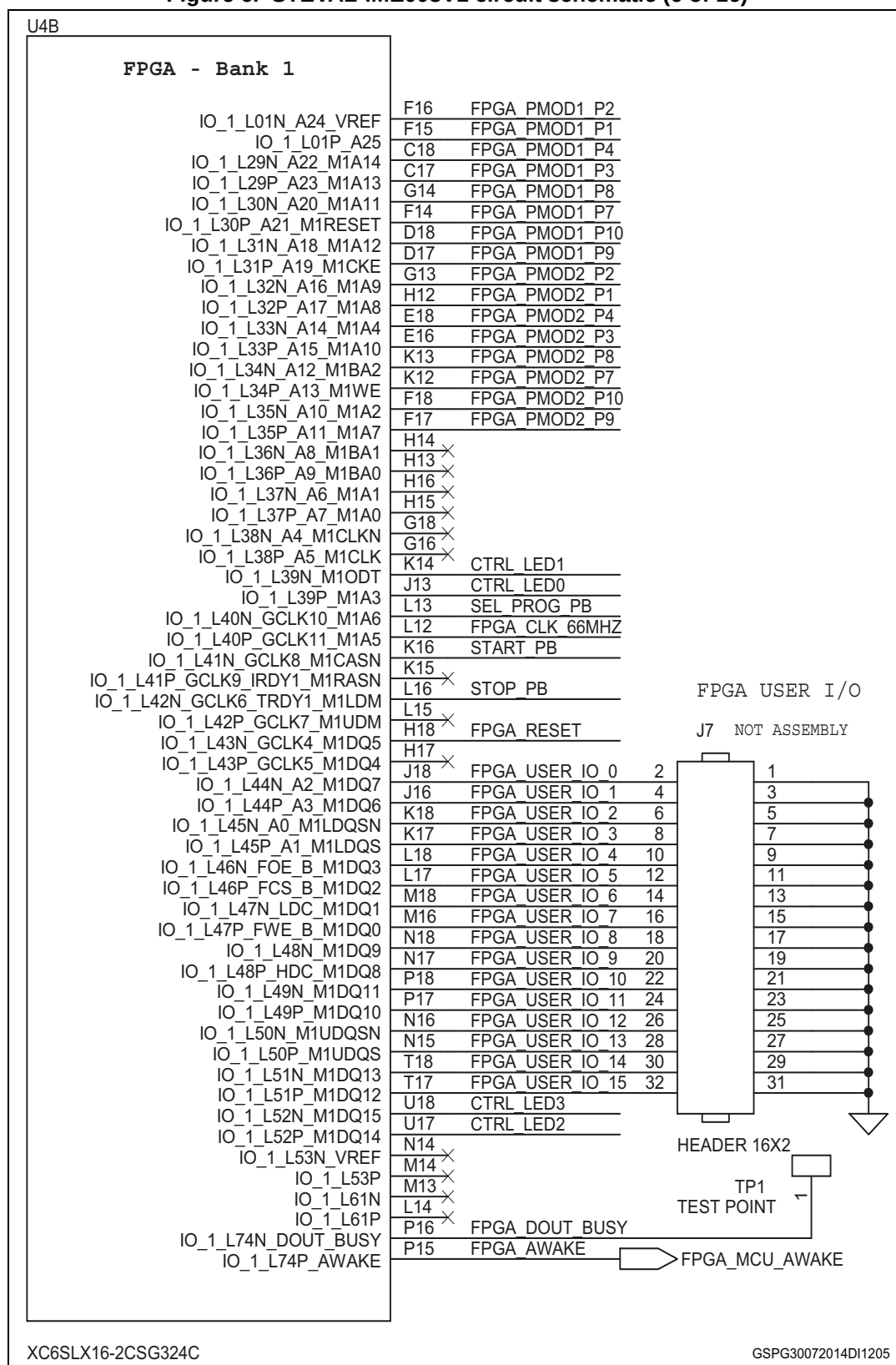


Figure 7. STEVAL-IME008V1 circuit schematic (7 of 16)

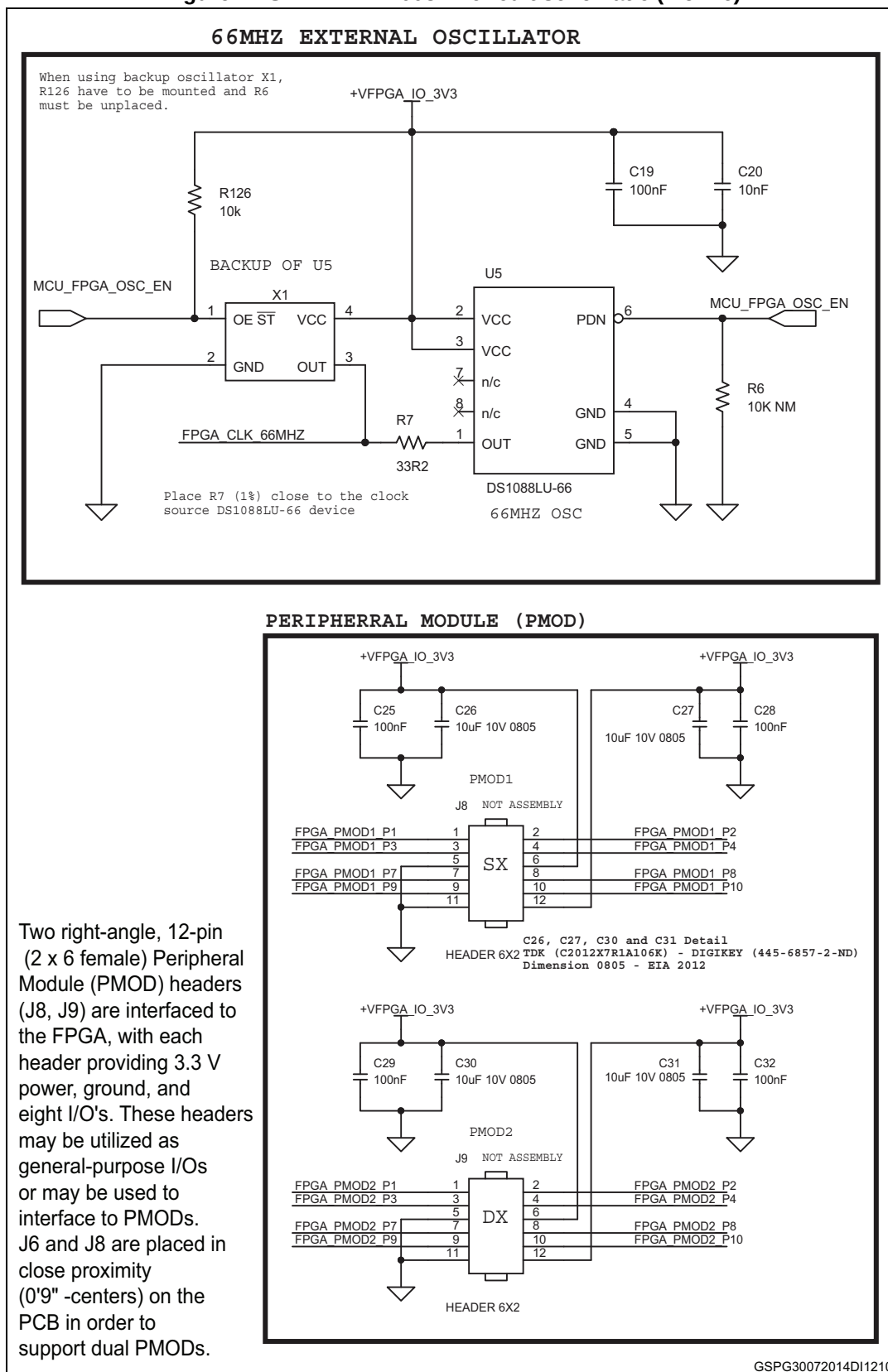
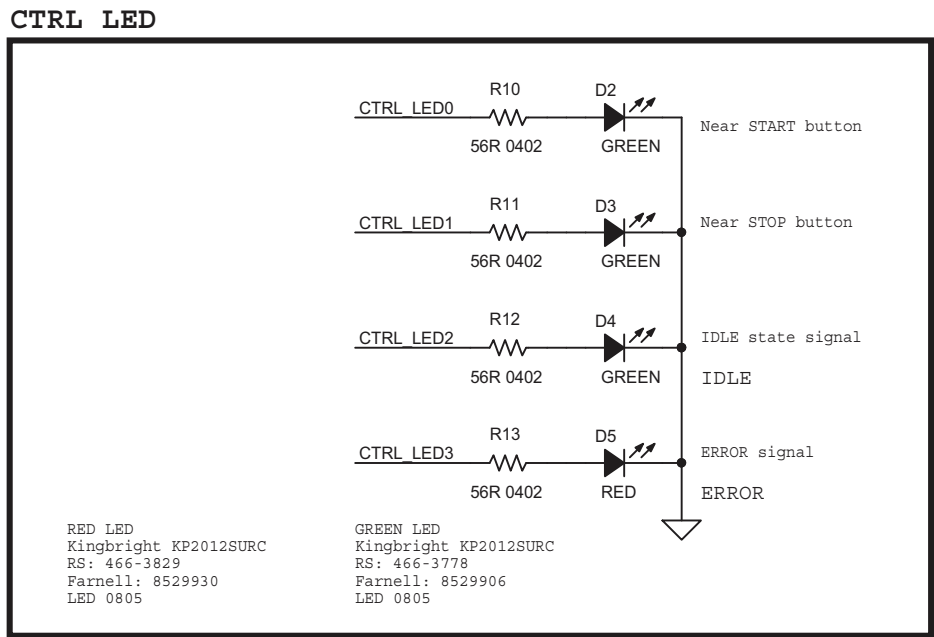
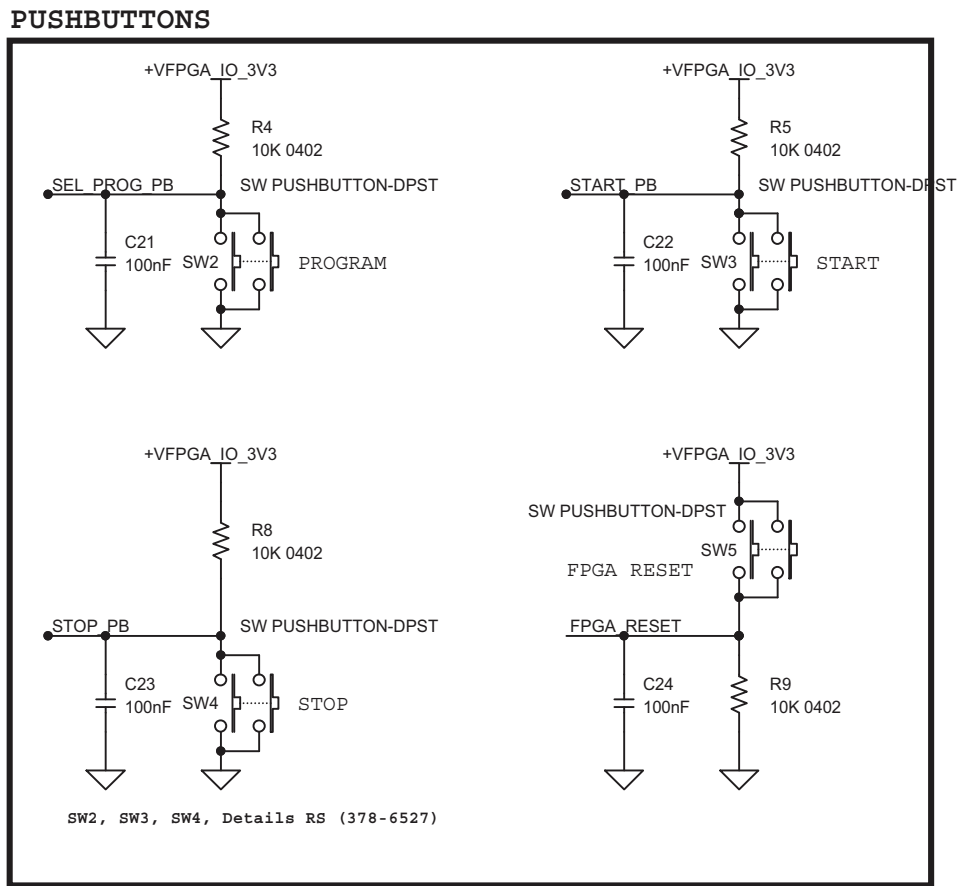


Figure 8. STEVAL-IME008V1 circuit schematic (8 of 16)



GSPG30072014DI1215

Figure 9. STEVAL-IME008V1 circuit schematic (9 of 16)

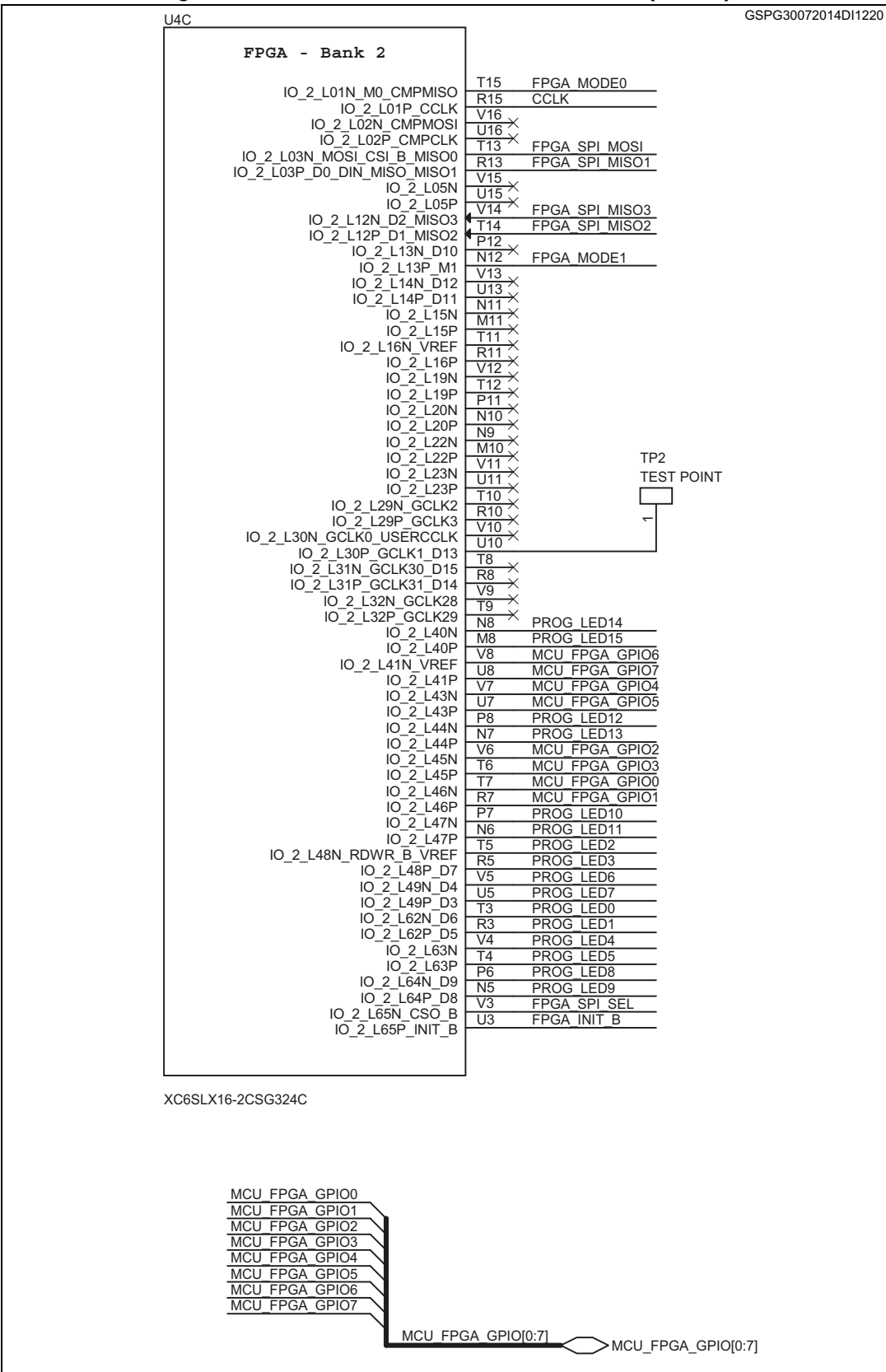
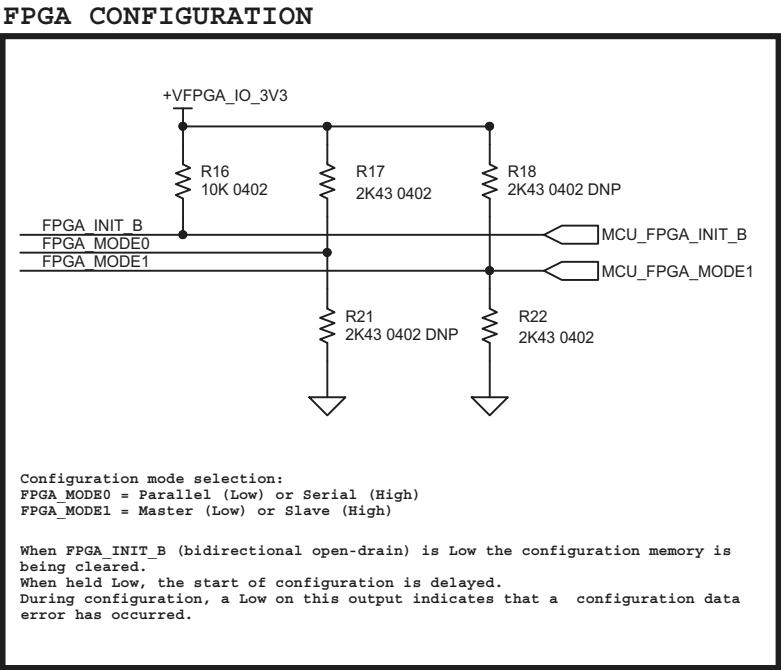
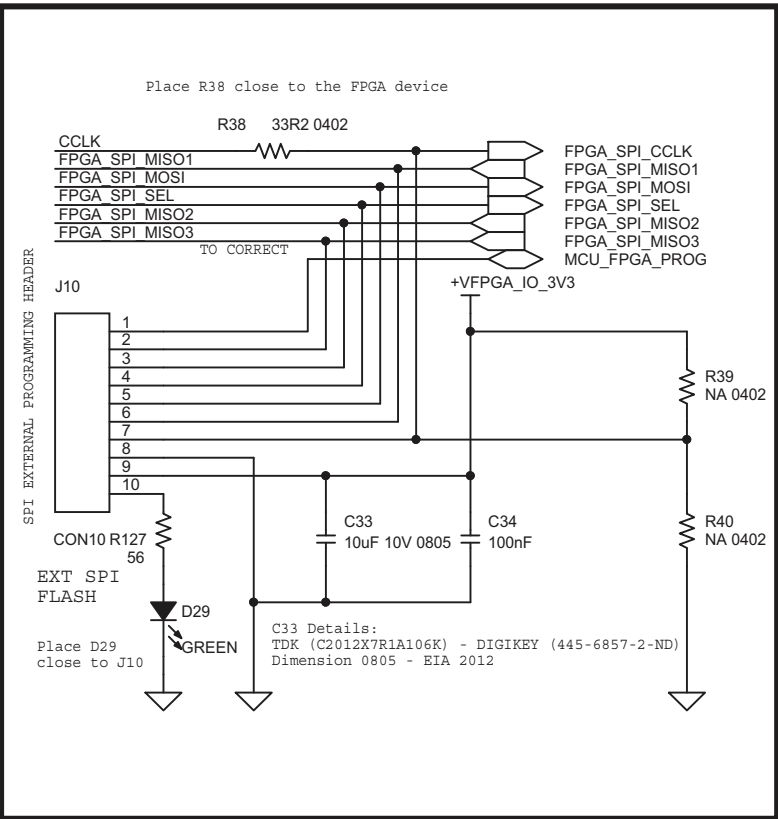


Figure 10. STEVAL-IME008V1 circuit schematic (10 of 16)



SPI FLASH CTRL SIGNALS



GSPG30072014DI1230

Figure 11. STEVAL-IME008V1 circuit schematic (11 of 16)

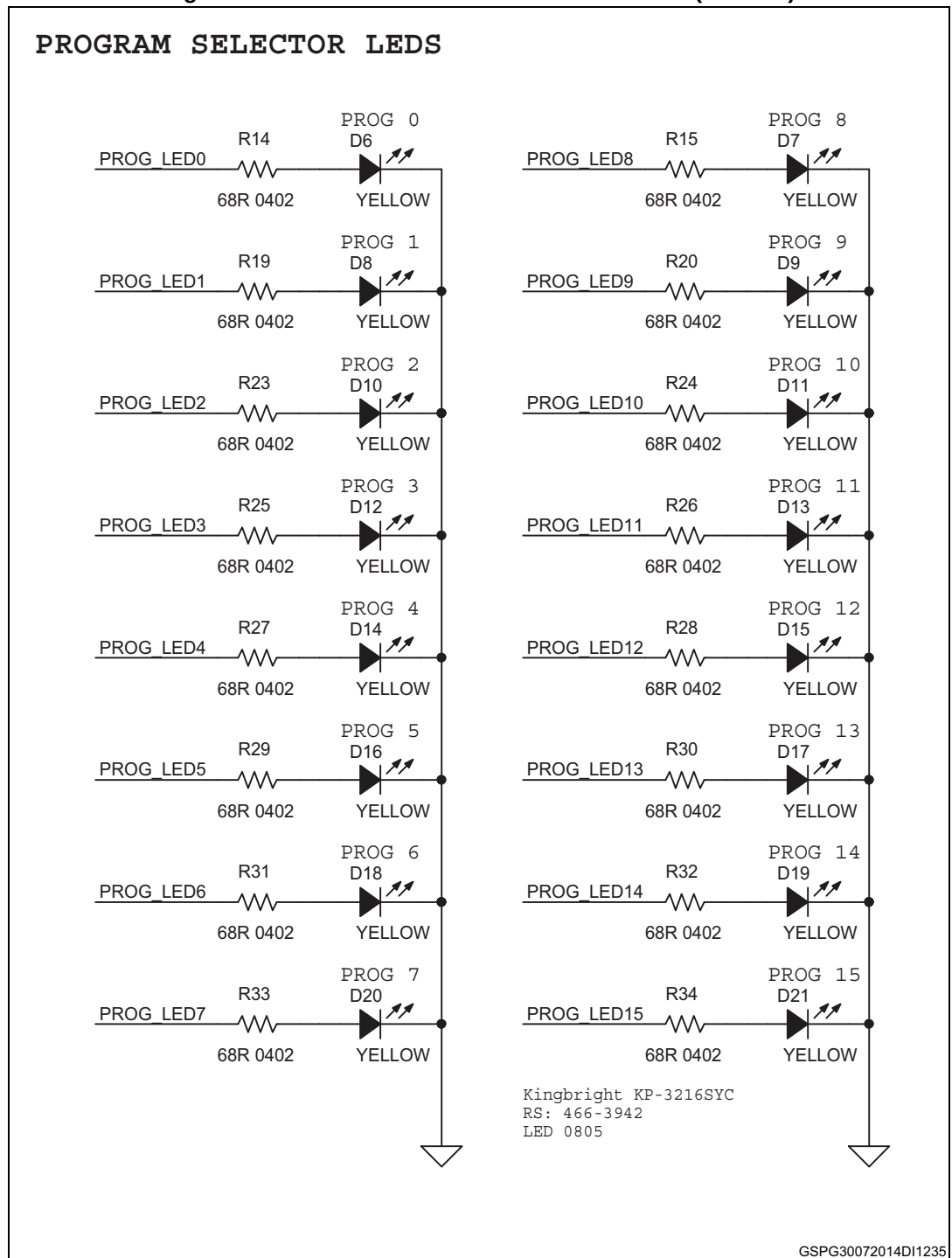
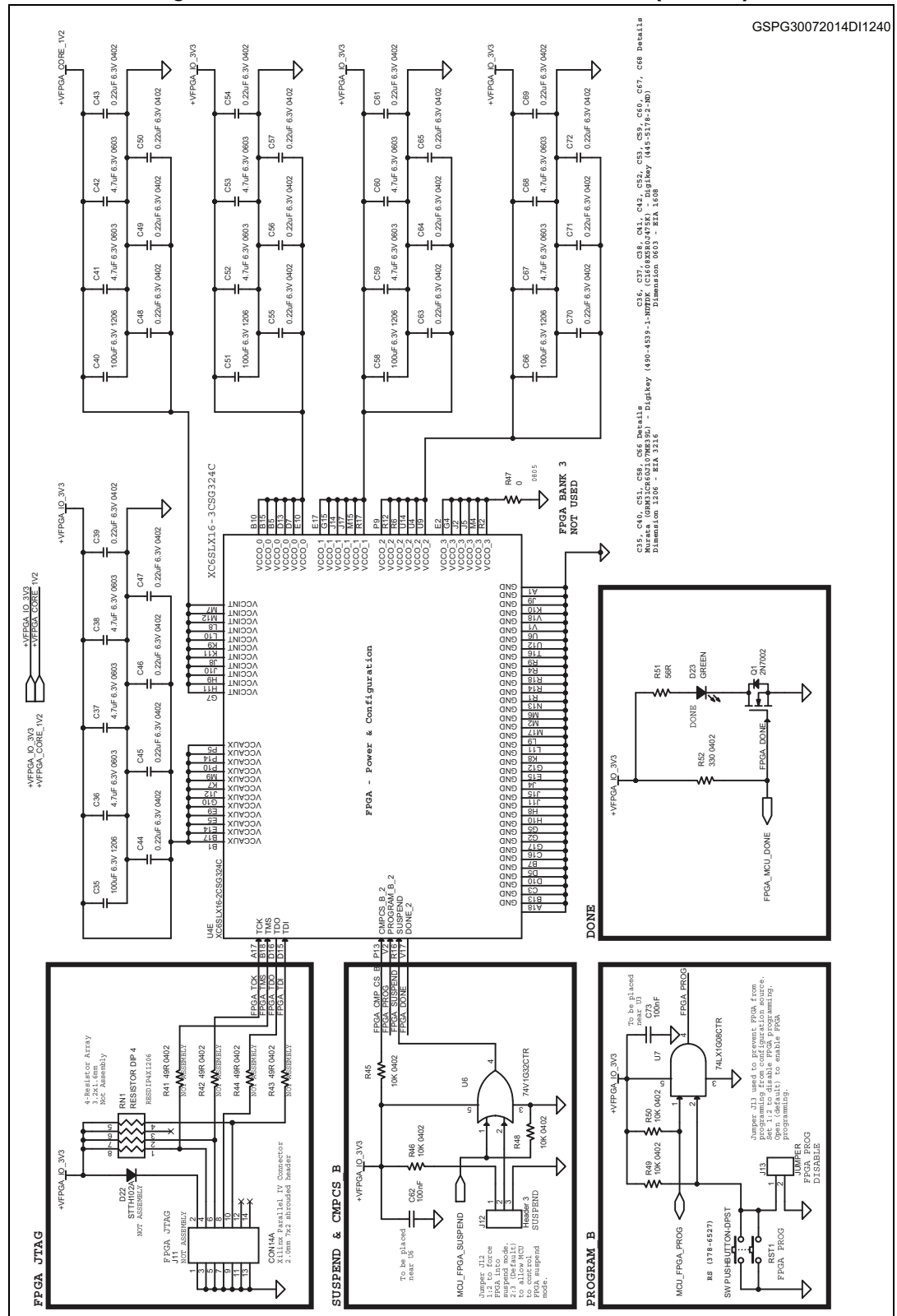


Figure 12. STEVAL-IME008V1 circuit schematic (12 of 16)



GSPG30072014DI1245

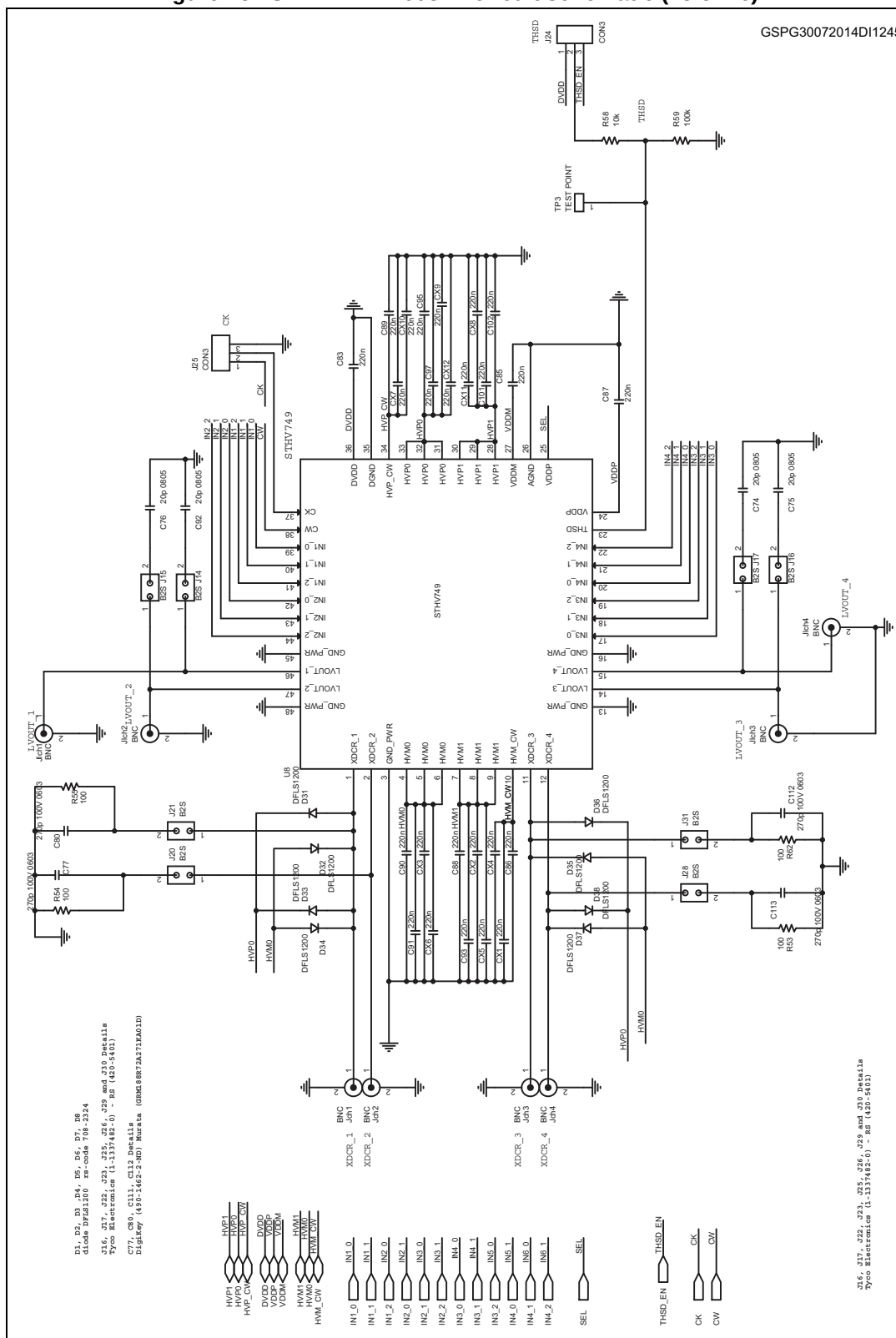


Figure 14. STEVAL-IME008V1 circuit schematic (14 of 16)

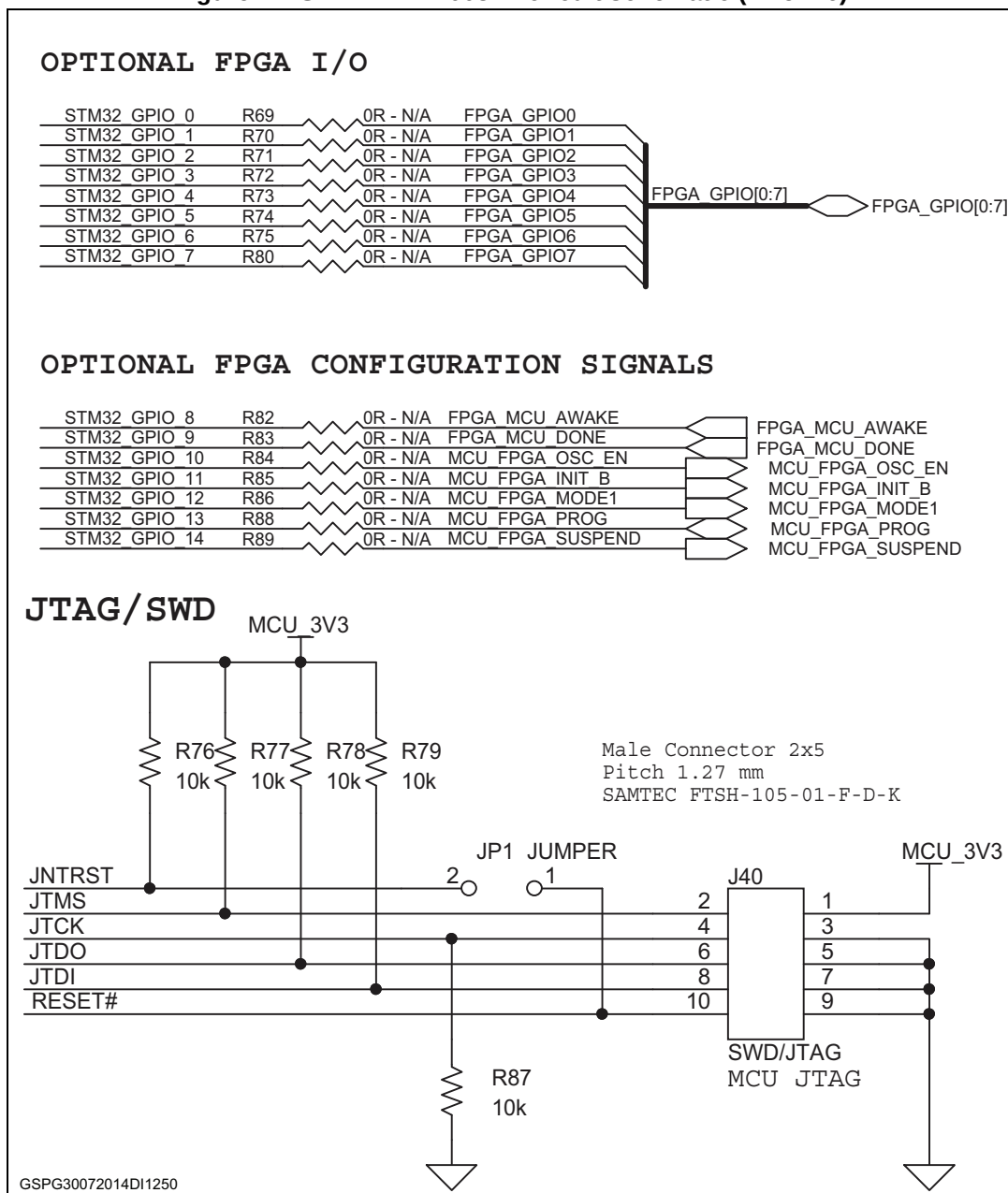


Figure 15. STEVAL-IME008V1 circuit schematic (15 of 16)

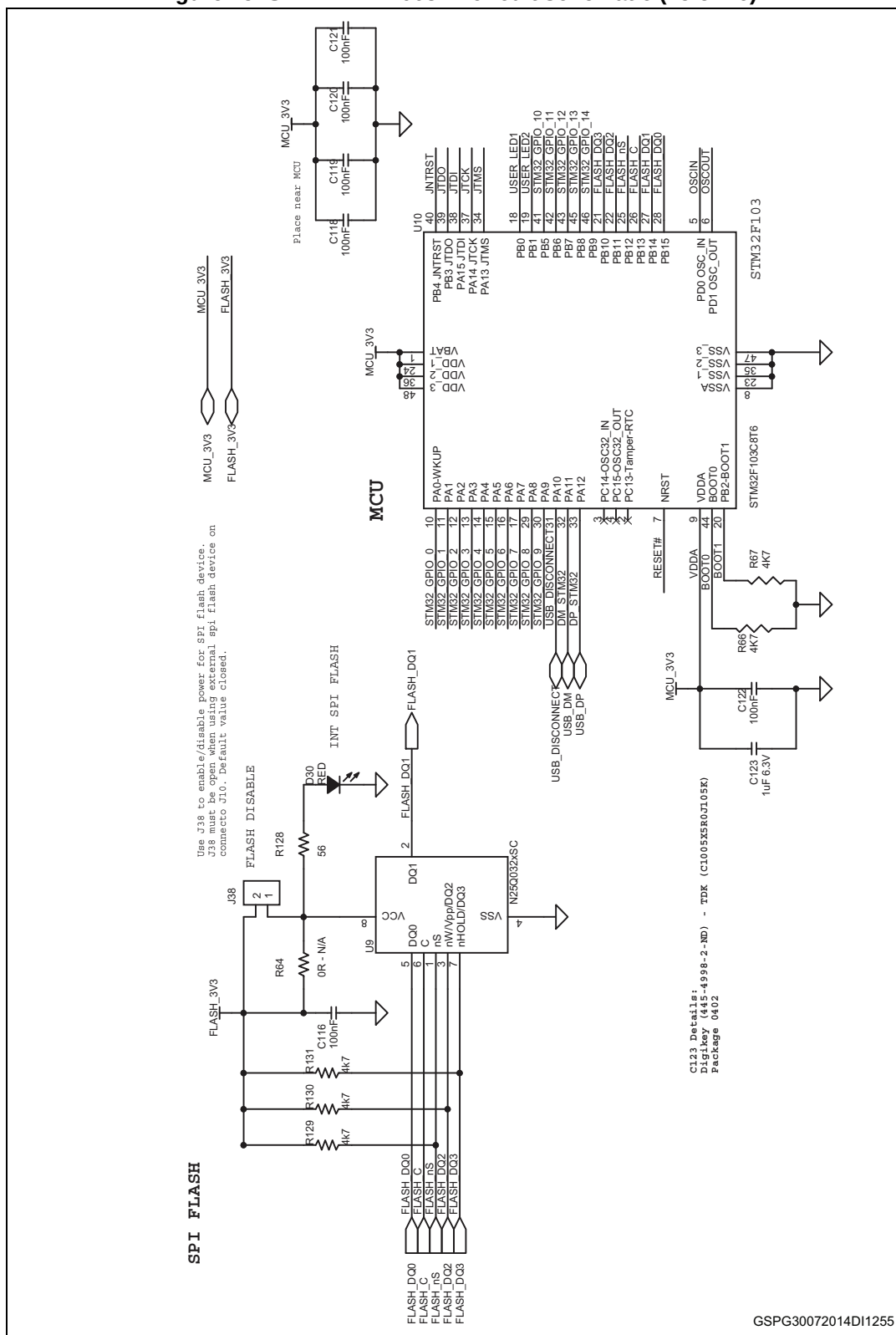
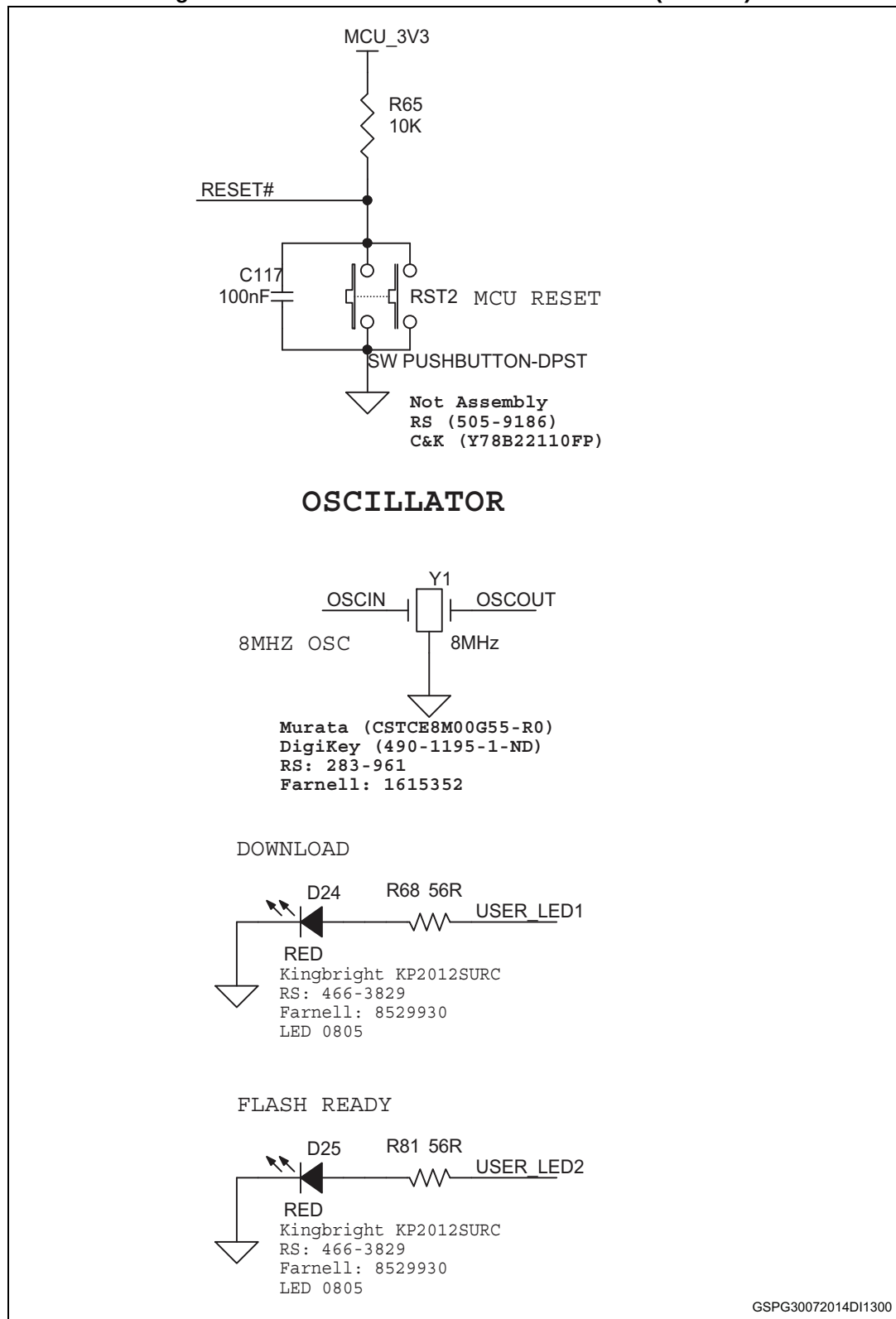


Figure 16. STEVAL-IME008V1 circuit schematic (16 of 16)



2 Revision history

Table 1. Document revision history

Date	Revision	Changes
06-Aug-2014	1	Initial release.
06-Aug-2015	2	Updated title on the cover page.

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