

STD1802T4-A

Low voltage fast-switching NPN power transistor

Features

- This device is qualified for automotive application
- Very low collector to emitter saturation voltage
- High current gain characteristic
- Fast-switching speed
- Surface-mounting DPAK (TO-252) power package in tape & reel (suffix "T4)

Description

The device is manufactured in Planar technology with "Base Island" layout. The resulting transistor shows exceptional high gain performance coupled with very low saturation voltage.

Applications

- CCFL drivers
- Voltage regulators
- Relay drivers
- High efficiency, low voltage, switching applications



Figure 1. Internal schematic diagram



Table 1. Device summary

Order code	Marking	Package	Packaging
STD1802T4-A	D1802	DPAK	Tape & reel

1 Electrical ratings

Table 2. Absolute maximum rating	Table :	2.	Absolute	maximum	rating
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Symbol	Parameter	Value	Unit
V _{CBO}	Collector-base voltage (I _E =0)	80	V
V _{CEO}	Collector-emitter voltage (I _B =0)	60	V
V _{EBO}	Emitter-base voltage (I _C =0)	6	V
Ι _C	Collector current	3	А
I _{CM}	Collector peak current (t _P < 5ms)	6	А
۱ _B	Base current	1	А
P _{tot}	Total dissipation at $T_c \le 25^{\circ}C$	15	W
T _{stg}	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thj-case}	Thermal resistance junction-case max	8.33	°C/W

2 Electrical characteristics

($T_{case} = 25^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Test Co	nditions	Min.	Тур.	Max.	Unit
I _{CBO}	Collector cut-off current (I _E =0)	V _{CB} = 40V				0.1	μA
I _{EBO}	Emitter cut-off current (I _C =0)	V _{EB} = 4V				0.1	μA
V _{(BR)CBO}	Collector-base breakdown voltage (I _E = 0)	Ι _C =100μΑ		80			V
V _{(BR)CEO}	Collector-emitter breakdown voltage (I _B = 0)	I _C =1mA		60			V
V _{(BR)EBO}	Emitter-base breakdown voltage (I _C = 0)	I _E =100μA		6			v
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	I _C =2A I _C =3A	I _B =100mA I _B =150mA		150 200	300 400	mV mV
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	I _C =2A	I _B =100mA		0.9	1.2	V
h _{FE} ⁽¹⁾	DC current gain	I _C =100mA I _C =3A	V _{CE} =2V V _{CE} =2V	200 100		400	
f _T	Transition frequency	V _{CE} =10V	I _C =50mA		150		MHz
C _{CBO}	Collector-base capacitance	V _{CB} =10V	f = 1MHz		50		pF
^t ON t _s t _f	Resistive load Turn-on time Storage time Fall time	I _C =1A I _{B1} =-I _{B2} =0.1.	V _{CC} =30V A		50 1.35 120		ns μs ns

Table 4.	Electrical	characteristics

Note (1) Pulsed duration = 300 $\mu s,$ duty cycle ${\leq}1.5\%$



2.1 **Electrical characteristics (curves)**

Figure 2. **Derating curve**

DC current gain Figure 3.





Figure 4. **Collector-emitter saturation** Figure 5. **Collector-emitter saturation** voltage voltage



Figure 6. **Base-emitter saturation** voltage







Figure 7. Base-emitter on voltage



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Figure 12. Switching times resistive load



2.2 Test circuits







3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com



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DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А	2.20		2.40	0.087		0.094
A1	0.90		1.10	0.035		0.043
A2	0.03		0.23	0.001		0.009
В	0.64		0.90	0.025		0.035
B2	5.20		5.40	0.204		0.213
С	0.45		0.60	0.018		0.024
C2	0.48		0.60	0.019		0.024
D	6.00		6.20	0.236		0.244
Е	6.40		6.60	0.252		0.260
G	4.40		4.60	0.173		0.181
Н	9.35		10.10	0.368		0.398
L2		0.8			0.031	
L4	0.60		1.00	0.024		0.039
V2	0°		8°	0°		0°

TO-252 (DPAK) MECHANICAL DATA



4 Revision history

Table 5. Revision history

Date	Revision	Changes
28-Jun-2007	1	Initial release.



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