

STB95N4F3, STD95N4F3 STP95N4F3

N-channel 40 V, 5.0 mΩ 80 A STripFET™ III Power MOSFET in D²PAK, DPAK, TO-220

Features

Order codes	V _{DSS}	R _{DS(on)} max.	I _D	Pw
STB95N4F3		< 5.8 m Ω		
STD95N4F3	40 V	< 5.0 111 52	80 A	110 W
STP95N4F3		$<$ 6.2 m Ω		

- Standard threshold drive
- 100% avalanche tested

Applications

- Switching applications
 - Automotive

Description

These devices are N-channel enhancement mode Power MOSFETs produced using STMicroelectronics' STripFET™ III technology, which is specifically designed to minimize onresistance and gate charge to provide superior switching performance.

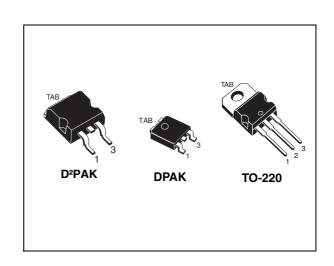


Figure 1. Internal schematic diagram

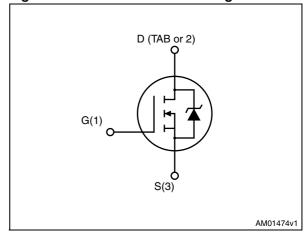


Table 1. Device summary

Order codes	Marking	Package	Packaging
STB95N4F3	95N4F3	D ² PAK	Tape and reel
STD95N4F3	95N4F3	DPAK	Tape and reel
STP95N4F3	95N4F3	TO-220	Tube

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1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{DS}	Drain-source voltage	40	V
V _{GS}	Gate-source voltage	± 20	٧
I _D ⁽¹⁾	Drain current (continuous) at T _C = 25 °C	80	Α
I _D	Drain current (continuous) at T _C = 100 °C	65	Α
I _{DM} ⁽²⁾	Drain current (pulsed)	320	Α
P _{TOT}	Total dissipation at T _C = 25 °C	110	W
	Derating factor	0.73	W/°C
dv/dt (3)	Peak diode recovery voltage slope	8	V/ns
E _{AS} (4)	Single pulse avalanche energy	400	mJ
T _j T _{stg}	Operating junction temperature Storage temperature	-55 to 175	°C

- 1. Current limited by package.
- 2. Pulse width limited by safe operating area.
- $3. \quad I_{SD} \ \leq 80 \ A, \ di/dt \ \leq 400 A/\mu s, \ V_{DS} \ \leq V_{(BR)DSS}, \ Tj \ \leq Tjmax.$
- 4. Starting Tj = 25 °C, I_D = 40 A, V_{DD} = 30 V.

Table 3. Thermal resistance

Symbol	Parameter		Value			
Symbol			DPAK	TO-220	Unit	
R _{thj-case}	Thermal resistance junction-case max	1.36			°C/W	
R _{thj-a}	Thermal resistance junction-ambient max	(62.5	°C/W	
R _{thj-pcb} (1)	Thermal resistance junction-ambient max	30 50			°C/W	
T _I	Maximum lead temperature for soldering purpose			300	°C	

1. When mounted on 1inch² FR-4 2Oz Cu board.

2 Electrical characteristics

(T_{CASE} =25 °C unless otherwise specified)

Table 4. Static

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source breakdown voltage	$I_D = 250 \mu A, V_{GS} = 0$	40			٧
I _{DSS}	Zero gate voltage drain current (V _{GS} = 0)	V _{DS} = 40 V, V _{DS} = 40 V,Tc = 125 °C			10 100	μ Α μ Α
I _{GSS}	Gate body leakage current (V _{DS} = 0)	V _{GS} = ±20 V			±200	nA
V _{GS(th)}	Gate threshold voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2		4	٧
B-s/	Static drain-source on	V _{GS} = 10 V, I _D = 40 A		5.0	5.8	mΩ
R _{DS(on)}	resistance	V_{GS} = 10 V, I_{D} = 40 A for TO-220		5.4	6.2	mΩ

Table 5. Dynamic

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C _{iss}	Input capacitance			2200		pF
Coss	Output capacitance	V_{DS} =25 V, f=1 MHz, V_{GS} =0	-	580		pF
C_{rss}	Reverse transfer capacitance			40		pF
Q_g	Total gate charge	V _{DD} =20 V, I _D = 80 A		40	54	nC
Q_{gs}	Gate-source charge	V _{GS} =10 V	-	11		nC
Q_{gd}	Gate-drain charge	(see Figure 14)		8		nC

Table 6. Switching on/off (inductive load)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(on)}	Turn-on delay time Rise time	V_{DD} =20 V, I_{D} = 40 A, R_{G} =4.7 Ω , V_{GS} =10 V (see Figure 16)	-	15 50	-	ns ns
t _{d(off)}	Turn-off delay time Fall time	V_{DD} =20 V, I_{D} = 40 A, R_{G} =4.7 Ω , V_{GS} =10 V (see Figure 16)	-	40 15	-	ns ns

Table 7. Source drain diode

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{SD}	Source-drain current Source-drain current (pulsed)		-		80 320	A A
V _{SD} ⁽²⁾	Forward on voltage	I _{SD} =80 A, V _{GS} =0	-		1.5	V
t _{rr} Q _{rr} I _{RRM}	Reverse recovery time Reverse recovery charge Reverse recovery current	I _{SD} =80 A, di/dt = 100 A/μs, V _{DD} = 30 V, Tj=150 °C (see <i>Figure 15</i>)	-	45 60 2.8		ns nC A

^{1.} Pulse width limited by safe operating area

^{2.} Pulsed: pulse duration = $300 \mu s$, duty cycle 1.5%

2.1 Electrical characteristics (curves)

Figure 2. Safe operating area

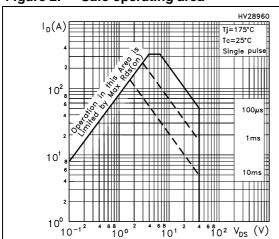


Figure 3. Thermal impedance

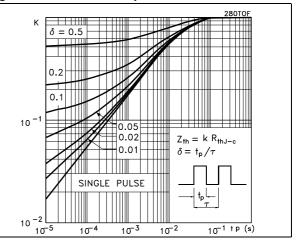


Figure 4. Output characteristics

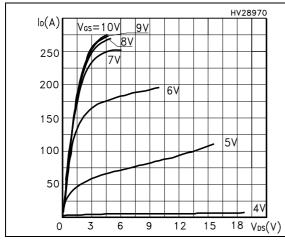


Figure 5. Transfer characteristics

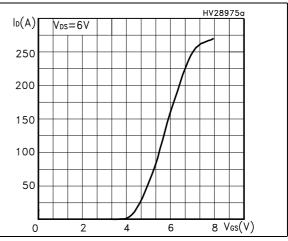


Figure 6. Static drain-source on resistance

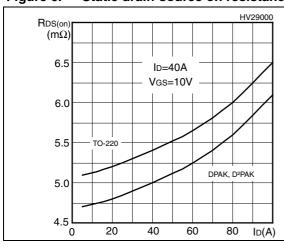
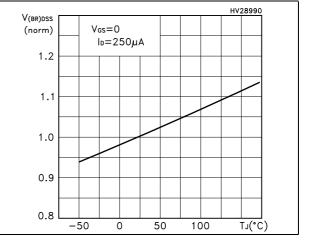


Figure 7. Normalized BVDSS vs temperature



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HV29060 C(pF) Vge(V)f=1MHz V_{GS}=0V 5000 10 $V_{DD}=20V$ ID=80A 8 4000 6 3000 2000 1000 00 10 20 30 40 Qg(nC) 25 Vps(V) 10 15 0

Figure 8. Gate charge vs gate-source voltage Figure 9. Capacitance variations

Figure 10. Normalized gate threshold voltage Figure 11. Normalized on resistance vs vs temperature temperature

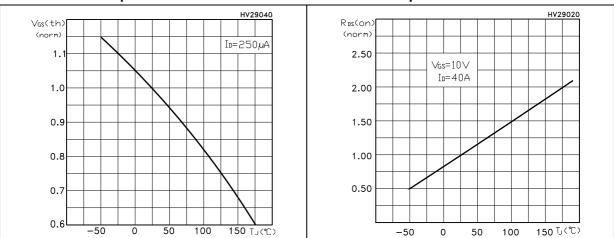
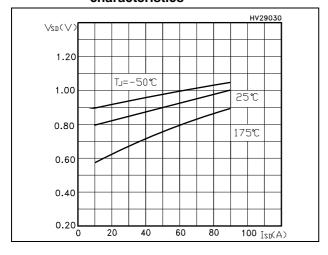


Figure 12. Source-drain diode forward characteristics



3 Test circuits

Figure 13. Switching times test circuit for resistive load

Figure 14. Gate charge test circuit

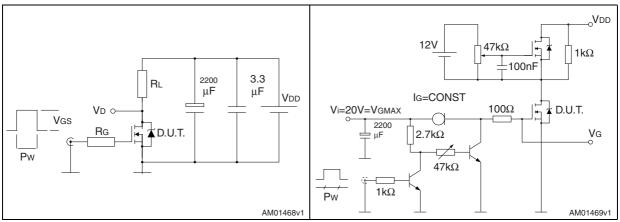


Figure 15. Test circuit for inductive load switching and diode recovery times

Figure 16. Unclamped inductive load test circuit

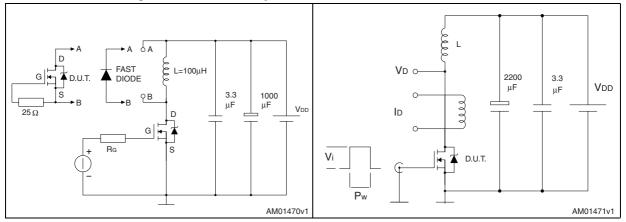
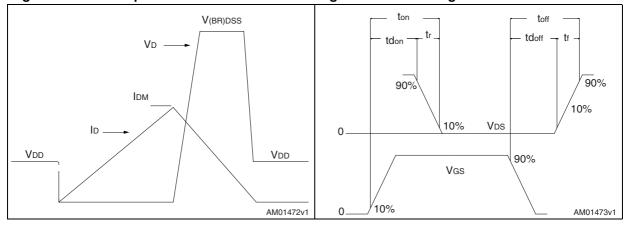


Figure 17. Unclamped inductive waveform

Figure 18. Switching time waveform



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4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

Table 8. D²PAK (TO-263) mechanical data

Dim	mm			
Dim.	Min.	Тур.	Max.	
Α	4.40		4.60	
A1	0.03		0.23	
b	0.70		0.93	
b2	1.14		1.70	
С	0.45		0.60	
c2	1.23		1.36	
D	8.95		9.35	
D1	7.50			
Е	10		10.40	
E1	8.50			
е		2.54		
e1	4.88		5.28	
Н	15		15.85	
J1	2.49		2.69	
L	2.29		2.79	
L1	1.27		1.40	
L2	1.30		1.75	
R		0.4		
V2	0°		8°	

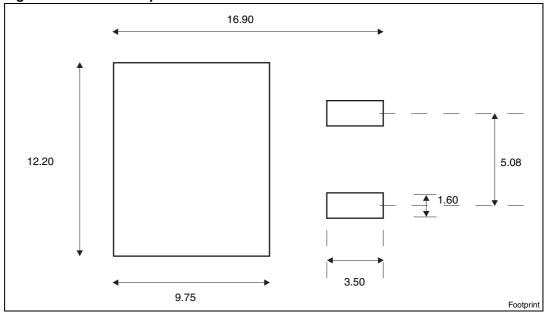
L1 SEATING PLANE COPLANARITY

0.25

GAUGE PLANE

Figure 19. D²PAK (TO-263) drawing





a. All dimension are in millimeters

0079457_S

Table 9. DPAK (TO-252) mechanical data

Dim		mm	
Dim.	Min.	Тур.	Max.
А	2.20		2.40
A1	0.90		1.10
A2	0.03		0.23
b	0.64		0.90
b4	5.20		5.40
С	0.45		0.60
c2	0.48		0.60
D	6.00		6.20
D1		5.10	
E	6.40		6.60
E1		4.70	
е		2.28	
e1	4.40		4.60
Н	9.35		10.10
L	1		1.50
L1		2.80	
L2		0.80	
L4	0.60		1
R		0.20	
V2	0°		8°

Figure 21. DPAK (TO-252) drawing

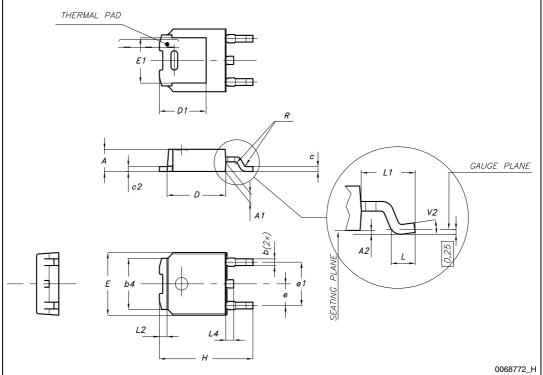
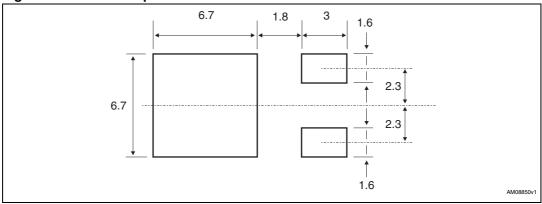


Figure 22. DPAK footprint^(b)



b. All dimension are in millimeters

Table 10. TO-220 type A mechanical data

D:	mm				
Dim.	Min.	Тур.	Max.		
Α	4.40		4.60		
b	0.61		0.88		
b1	1.14		1.70		
С	0.48		0.70		
D	15.25		15.75		
D1		1.27			
Е	10		10.40		
е	2.40		2.70		
e1	4.95		5.15		
F	1.23		1.32		
H1	6.20		6.60		
J1	2.40		2.72		
L	13		14		
L1	3.50		3.93		
L20		16.40			
L30		28.90			
ØP	3.75		3.85		
Q	2.65		2.95		

Figure 23. TO-220 type A drawing

5 Packaging mechanical data

Table 11. D²PAK (TO-263) tape and reel mechanical data

	Таре			Reel		
Dim.	mm		Dim	mm		
	Min.	Max.	Dim.	Min.	Max.	
A0	10.5	10.7	Α		330	
В0	15.7	15.9	В	1.5		
D	1.5	1.6	С	12.8	13.2	
D1	1.59	1.61	D	20.2		
Е	1.65	1.85	G	24.4	26.4	
F	11.4	11.6	N	100		
K0	4.8	5.0	Т		30.4	
P0	3.9	4.1				
P1	11.9	12.1		Base qty 1000		
P2	1.9	2.1		Bulk qty 1000		
R	50					
Т	0.25	0.35				
W	23.7	24.3				

Table 12. DPAK (TO-252) tape and reel mechanical data

Таре				Reel		
Dim.	n	nm	Dim.	mm		
	Min.	Max.	Dilli.	Min.	Max.	
A0	6.8	7	Α		330	
В0	10.4	10.6	В	1.5		
B1		12.1	С	12.8	13.2	
D	1.5	1.6	D	20.2		
D1	1.5		G	16.4	18.4	
E	1.65	1.85	N	50		
F	7.4	7.6	Т		22.4	
K0	2.55	2.75				
P0	3.9	4.1		Base qty.	2500	
P1	7.9	8.1		Bulk qty.	2500	
P2	1.9	2.1				
R	40					
Т	0.25	0.35				
W	15.7	16.3				

Figure 24. Tape

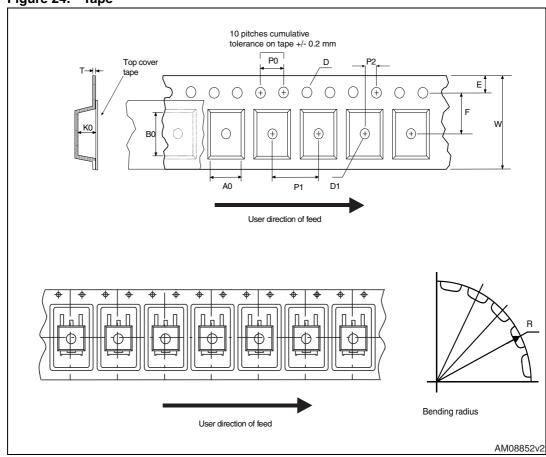
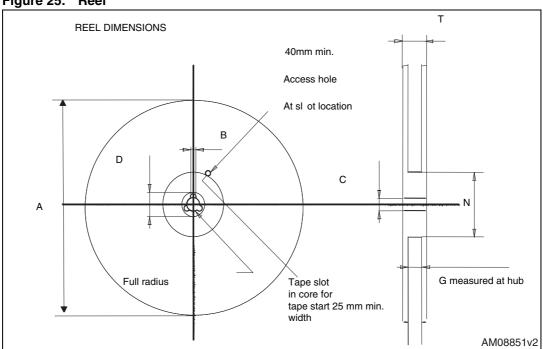


Figure 25. Reel



6 Revision history

Table 13. Dcument revision history

Date	Revision	Changes		
22-Feb-2007 1		First release		
15-May-2007	2	Changes on applications		
10-Sep-2009	3	Removed package, mechanical data: IPAK		
13-Dec-2011	4	New package and mechanical data have been added: - Table 8: D²PAK (TO-263) mechanical data, Figure 19: D²PAK (TO-263) drawing, Figure 20: D²PAK footprint Section 5: Packaging mechanical data has been updated: - Table 11: D²PAK (TO-263) tape and reel mechanical data, Figure 24: Tape, Figure 25: Reel. Minor text changes.		

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