

D1FT10A

Schottky Barrier Diodes 100V, 3A

Feature

- Small SMD
- · High Recovery Speed
- Tj=175°C
- Ultra low I_R
- Based on AEC-Q101
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): 1F Package (JEDEC Code): DO-214AC



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified: Tl=25°C)

| Item | Symbol | Conditions | Ratings | Unit |
|---------------------------------|---------------------|--|------------|------|
| Storage temperrature | Tstg | | -55 to 175 | °C |
| Junction temperature | Tj | | -55 to 175 | °C |
| Repetitive peak reverse voltage | V _{RRM} | | 100 | V |
| Average forward current | I _F (AV) | 50Hz sine wave, Resistance load, Tl=116°C | 3 | Α |
| Average forward current | I _F (AV) | 50Hz sine wave, Resistance load, On alumina substrate, Ta=25°C ※ | 1.9 | А |
| Average forward current | I _F (AV) | 50Hz sine wave, Resistance load, On glass-epoxy substrate, Ta=25°C ※ | 1.4 | А |
| Surge forward current | I _{FSM} | 50Hz sine wave, Non-repetitive, 1cycle, Peak value, Tj=25°C | 60 | А |

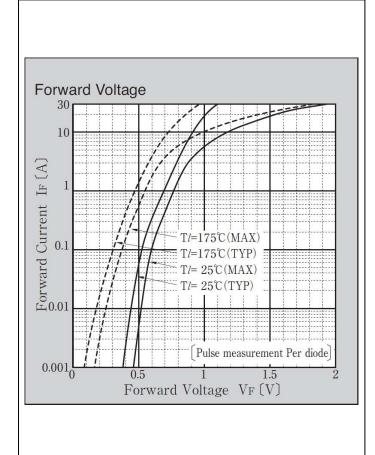
※ ∶See the original Specifications

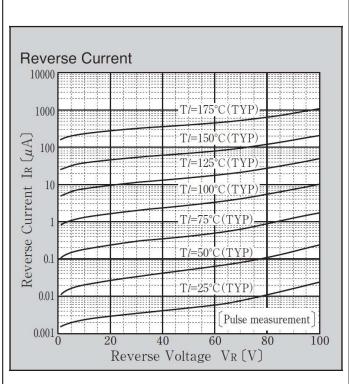
Electrical Characteristics (unless otherwise specified : Tl=25°C)

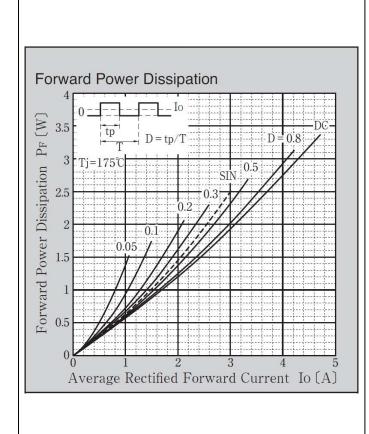
| Item | Symbol | Conditions | Ratings | | | 11 |
|--------------------|----------------|---|---------|-----|-------|------|
| | | | MIN | TYP | MAX | Unit |
| Forward voltage | V_{F} | IF=3A, Pulse measurement | | | 0.86 | V |
| Forward voltage | V _F | IF=1A, Pulse measurement | | | 0.75 | V |
| Reverse current | I _R | VR=100V, Pulse measurement | | | 0.008 | mA |
| Total capacitance | Ct | f=1MHz, VR=10V | | 60 | | pF |
| Thermal resistance | Rth(j-l) | Junction to lead | | | 23 | °C/W |
| Thermal resistance | Rth(j-a) | Junction to ambient, On alumina substrate * | | | 108 | °C/W |
| Thermal resistance | Rth(j-a) | Junction to ambient, On glass-epoxy substrate * | | | 157 | °C/W |

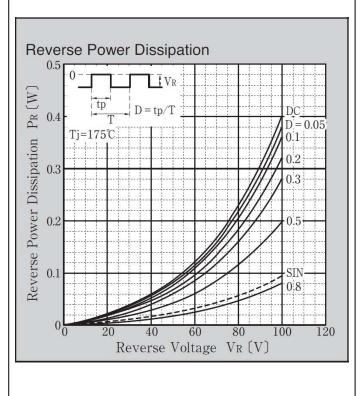
 $[\]divideontimes$: See the original Specifications

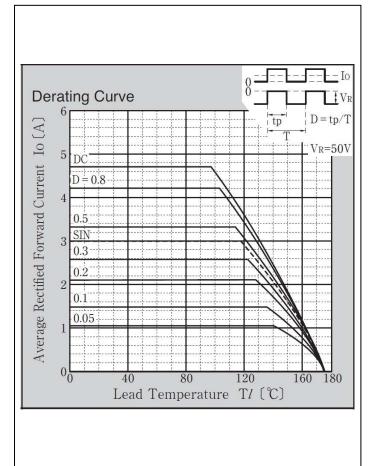
CHARACTERISTIC DIAGRAMS

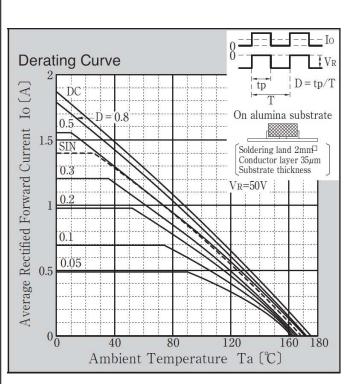


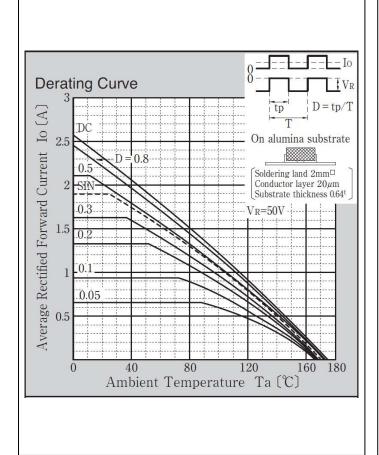


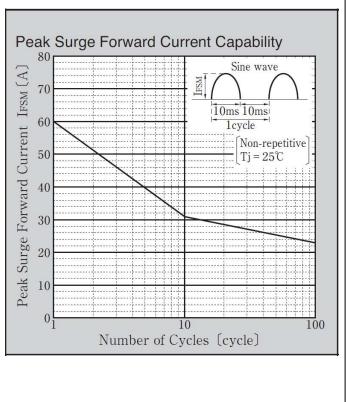


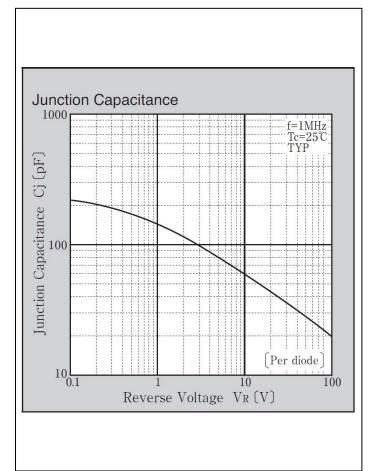


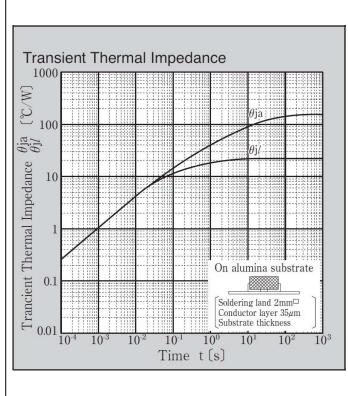


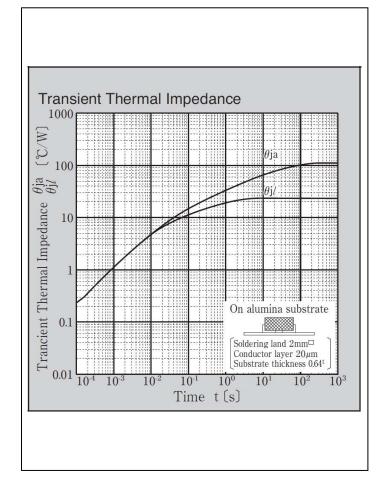










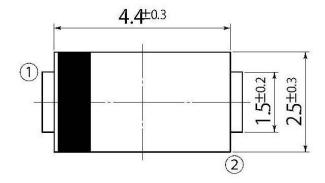


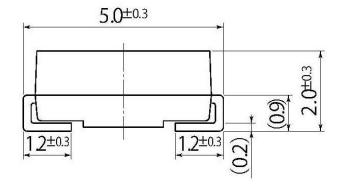
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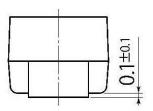
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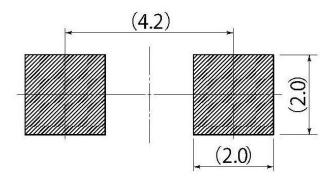
B3

| JEDEC Code | DO-214AC | |
|------------|----------|--|
| JEITA Code | _ | |
| House Name | 1F, CF | |









Referential Soldering Pad

• Optimize soldering pad to the board design and soldering condition.

Notes

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