

PC367NJ0000F Series

Mini-flat Package High CMR, Low Input Current Type Photocoupler



■ Description

PC367NJ0000F contains an IRED optically coupled to a phototransistor.

It is packaged in a 4-pin mini-flat.

Low input current type.

Input-output isolation voltage(rms) is 3.75kV.

Collector-emitter voltage is 80V and CTR is 100% to 500% at input current of 0.5mA.

■ Features

- 1. 4-pin Mini-flat package
- 2. Double transfer mold package (Ideal for Flow Soldering)
- 3. Low input current type (I_F=0.5mA)
- 4. High collector-emitter voltage (V_{CEO}: 80V)
- 5. High noise immunity due to high common mode rejection voltage (CMR : MIN. $10kV/\mu s$)
- 6. High isolation voltage between input and output $(V_{iso(rms)}: 3.75kV)$
- 7. RoHS directive compliant

■ Agency approvals/Compliance

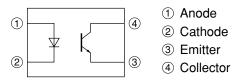
- Recognized by UL1577 (Double protection isolation), file No. E64380 (as model No. PC367)
- 2. Package resin: UL flammability grade (94V-0)

■ Applications

- 1. Programmable controllers
- 2. Facsimiles
- 3. Telephones

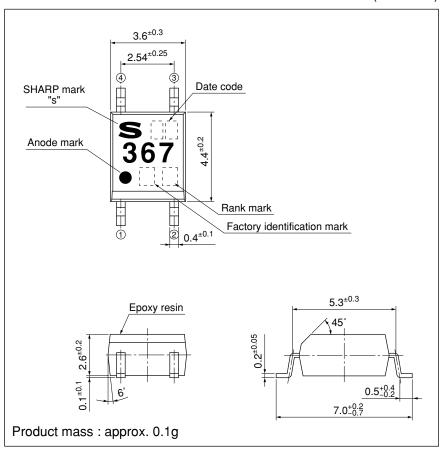


■ Internal Connection Diagram



■ Outline Dimensions

(Unit: mm)



Plating material : SnCu (Cu : TYP. 2%)



Date code (2 digit)

	1st o	digit		2nd digit		
	Year of p	roduction		Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	A	2002	P	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	T	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	X	August	8	
1998	K	2010	A	September	9	
1999	L	2011	В	October	0	
2000	M	2012	С	November	N	
2001	N	:	:	December	D	

repeats in a 20 year cycle

Factory identification mark

Factory identification Mark	Country of origin	
no mark	Ionon	
	Japan	
	Indonesia	
	China	

^{*} This factory marking is for identification purpose only.
Please Contact the local SHARP sales reprsentative to see the actual status of the production.

Rank mark

Refer to the Model Line-up table



Parameter Forward current

Reverse voltage

Power dissipation

Collector current

Total power dissipation Operating temperature

Storage temperature

*2 Isolation voltage

Collector-emitter voltage

Emitter-collector voltage

Collector power dissipation

*1 Peak forward current

■ Absolute Maximum Ratings

 I_{C}

 P_{C}

 P_{tot}

Topr

 $T_{stg} \\$

 $V_{iso\ (rms)}$

 T_{sol}

II nalii	igs	$(T_a=25^{\circ}C)$		
Symbol	Rating	Unit		
I_F	10	mA		
I_{FM}	200	mA		
V_R	6	V		
P	15	mW		
V_{CEO}	80	V		
V _{ECO}	6	V		
Ic	50	mA		

mW

mW

°C

°C

kV

°C

150

170

-30 to +100

-40 to +125

3.75

260

■ Electro-optical Characteristics

 $(T_a=25^{\circ}C)$

	•							$(1_a=25 C)$
Parameter Symbol Conditions		MIN.	TYP.	MAX.	Unit			
Input	Forward voltage		V_F	$I_F=10mA$	_	1.2	1.4	V
	Reverse current		I_R	$V_R=4V$	_	-	10	μΑ
	Terminal capacitance		C_{t}	V=0, f=1kHz	_	30	250	pF
	Collector dark	current	I_{CEO}	$V_{CE}=50V, I_{F}=0$	-	-	100	nA
Output	Collector-emitter breakdown voltage		BV _{CEO}	$I_{C}=0.1 \text{ mA}, I_{F}=0$	80	_	-	V
	Emitter-collector breakdown voltage		BV _{ECO}	$I_{E}=10\mu A, I_{F}=0$	6	_	-	V
	Collector current		I_{C}	$I_F=0.5$ mA, $V_{CE}=5$ V	0.5	_	2.5	mA
	Collector-emitter saturation voltage		V _{CE (sat)}	$I_F=10\text{mA}, I_C=1\text{mA}$	-	_	0.2	V
	Isolation resistance		R _{ISO}	DC500V, 40 to 60%RH	5×10 ¹⁰	1×10 ¹¹	-	Ω
Transfer	Floating capacitance		$C_{\rm f}$	V=0, f=1MHz	-	0.6	1.0	pF
charac- teristics	D	Rise time	t_r	V 2V I 2 A D 1000	-	4	18	μs
teristics	Response time	Fall time	$t_{\rm f}$	$V_{CE}=2V$, $I_{C}=2mA$, $R_{L}=100\Omega$	_	3	18	μs
	Common mode rejection voltage		CMR	T_a =25°C, R_L =470 Ω , V_{CM} =1.5kV(peak) I_F =0mA, V_{CC} =9V, V_{np} =100mV	10	_	_	kV/μs

^{*3} Soldering temperature *1 Pulse width≤100µs, Duty ratio: 0.001 *2 40 to 60%RH, AC for 1 minute, f=60Hz

^{*3} For 10s



■ Model Line-up

Package	Тар	oing	Rank mark	I _C [mA]
	3 000pcs/reel	750pcs/reel	Kalik iliaik	$(I_F=0.5\text{mA}, V_{CE}=5\text{V}, T_a=25^{\circ}\text{C})$
	PC367NJ0000F	PC367NT0000F	with or without	0.5 to 2.5
Model No.	PC367N1J000F	PC367N1T000F	A	0.75 to 1.5
	PC367N2J000F	PC367N2T000F	В	1.0 to 2.0

Please contact a local SHARP sales representative to inquire about production status.



Fig.1 Test Circuit for Common Mode Rejection Voltae

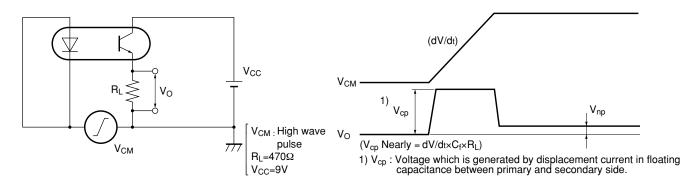


Fig.2 Forward Current vs. Ambient Temperature

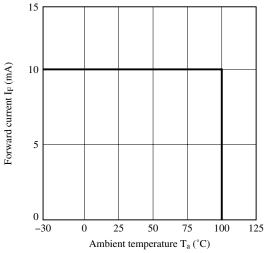


Fig.4 Collector Power Dissipation vs. Ambient Temperature

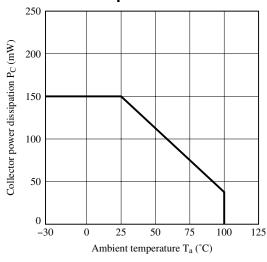


Fig.3 Diode Power Dissipation vs. Ambient Temperature

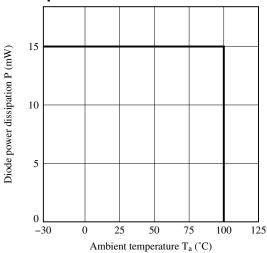


Fig.5 Total Power Dissipation vs. Ambient Temperature

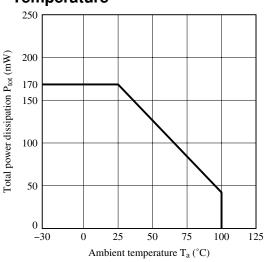




Fig.6 Peak Forward Current vs. Duty Ratio

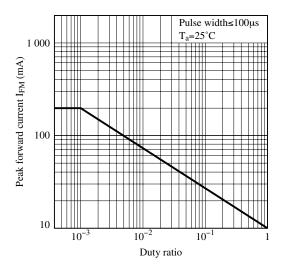


Fig.8 Current Transfer Ratio vs. Forward Current

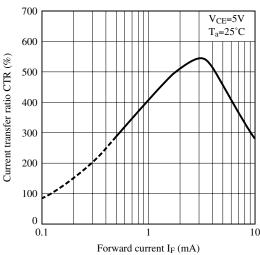


Fig.10 Relative Current Transfer Ratio vs.
Ambient Temperature

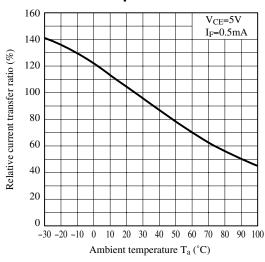


Fig.7 Forward Current vs. Forward Voltage

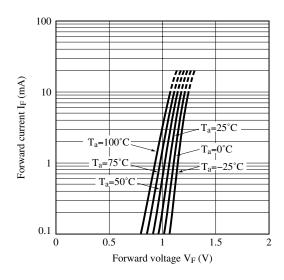


Fig.9 Collector Current vs. Collector-emitter Voltage

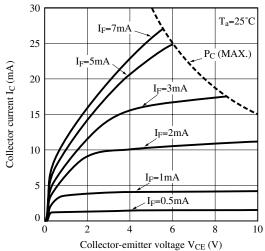


Fig.11 Collector - emitter Saturation Voltage vs. Ambient Temperature

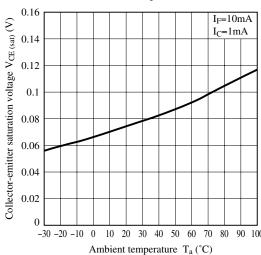




Fig.12 Collector Dark Current vs. Ambient Temperature

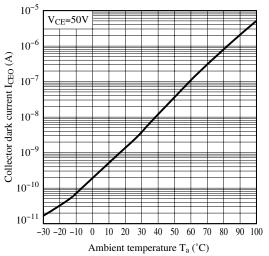


Fig.14 Response Time vs. Load Resistance (Saturation region)

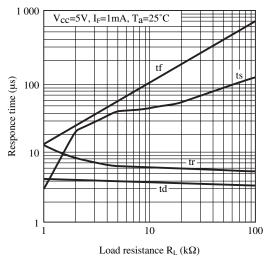


Fig.16 Frequency Response

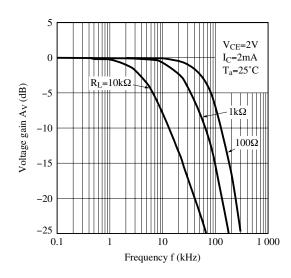


Fig.13 Response Time vs. Load Resistance (Active region)

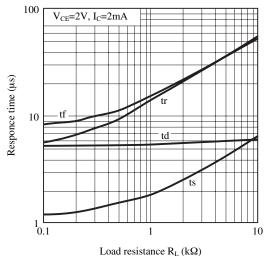
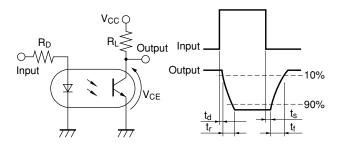
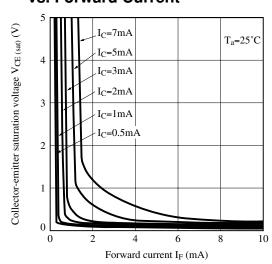


Fig.15 Test Circuit for Response Time



Please refer to the conditions in Fig.13 and Fig.14

Fig.17 Collector-emitter Saturation Voltage vs. Forward Current



Remarks: Please be aware that all data in the graph are just for reference and not for guarantee.



■ Design Considerations

Design guide

While operating at I_F<0.5mA, CTR variation may increase.

Please make design considering this fact.

In case that some sudden big noise caused by voltage variation is provided between primary and secondary terminals of photocoupler some current caused by it is floating capacitance may be generated and result in false operation since current may go through IRED or current may change.

If the photocoupler may be used under the circumstances where noise will be generated we recommend to use the bypass capacitors at the both ends of IRED.

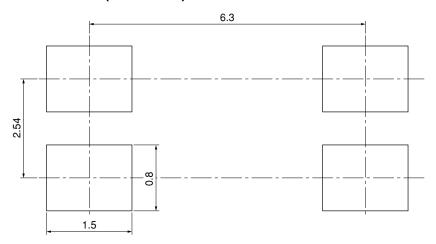
This product is not designed against irradiation and incorporates non-coherent IRED.

Degradation

In general, the emission of the IRED used in photocouplers will degrade over time.

In the case of long term operation, please take the general IRED degradation (50% degradation over 5 years) into the design consideration.

Recommended Foot Print (reference)



(Unit: mm)

[☆] For additional design assistance, please review our corresponding Optoelectronic Application Notes.



■ Manufacturing Guidelines

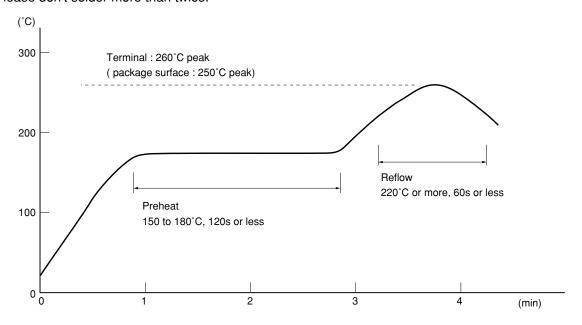
Soldering Method

Reflow Soldering:

Reflow soldering should follow the temperature profile shown below.

Soldering should not exceed the curve of temperature profile and time.

Please don't solder more than twice.



Flow Soldering:

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 260°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please don't solder more than twice.

Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.

Please don't solder more than twice.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.



Cleaning instructions

Solvent cleaning:

Solvent temperature should be 45°C or below Immersion time should be 3 minutes or less

Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this product.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.

This product shall not contain the following materials banned in the RoHS Directive (2002/95/EC).

•Lead, Mercury, Cadmium, Hexavalent chromium, Polybrominated biphenyls (PBB), Polybrominated diphenyl ethers (PBDE).



■ Package specification

● Tape and Reel package

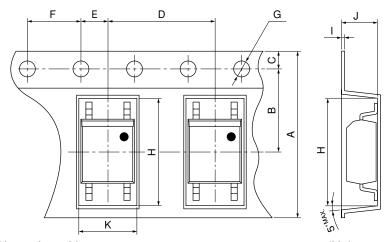
1. 3 000pcs/reel Package materials

Carrier tape: A-PET (with anti-static material)

Cover tape: PET (three layer system)

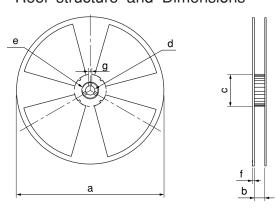
Reel: PS

Carrier tape structure and Dimensions



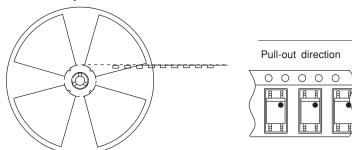
Dimensions List					(L	Init: mm)	
	A	В	C	D	Е	F	G
	12.0 ^{±0.3}	5.5 ^{±0.1}	1.75 ^{±0.1}	8.0 ^{±0.1}	2.0 ^{±0.1}	4.0 ^{±0.1}	φ1.5 + 0.1
	Н	I	J	K			
	$7.4^{\pm0.1}$	0.3 ^{±0.05}	3.1 ^{±0.1}	4.0 ^{±0.1}			

Reel structure and Dimensions



Dimensio	ns List	(Unit: mm)		
a	b	c	d	
370	13.5 ^{±1.5}	80 ^{±1.0}	13±0.5	
e	f	g		
21±1.0	2.0±0.5	2.0±0.5		

Direction of product insertion



[Packing: 3 000pcs/reel]



2. 750 pcs / reel

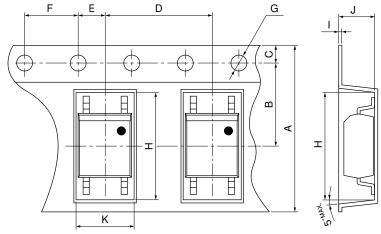
Package materials

Carrier tape: A-PET (with anti-static material)

Cover tape: PET (three layer system)

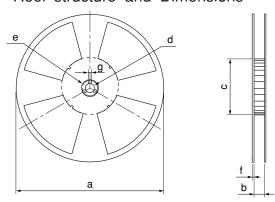
Reel: PS

Carrier tape structure and Dimensions



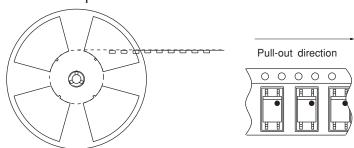
Dimensions List					(Unit: mm)		
A	В	С	D	Е	F	G	
12.0±0.3	5.5 ^{±0.1}	1.75 ^{±0.1}	8.0 ^{±0.1}	2.0 ^{±0.1}	4.0 ^{±0.1}	φ1.5 + 0.1	
Н	I	J	K				
7.4 ^{±0.1}	0.3±0.05	3.1 ^{±0.1}	4.0 ^{±0.1}				

Reel structure and Dimensions



Dimensio	ns List	(Unit: mm)		
a	b	С	d	
180	13.5 ^{±1.5}	80 ^{±1.0}	13 ^{±0.5}	
e	f	g		
21 ^{±1.0}	2.0±0.5	2.0 ^{±0.5}		

Direction of product insertion



[Packing: 750pcs/reel]



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- --- Various safety devices, etc.
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