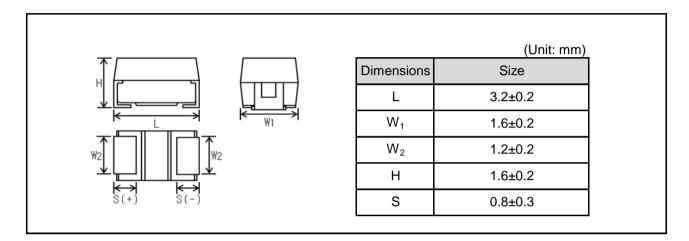




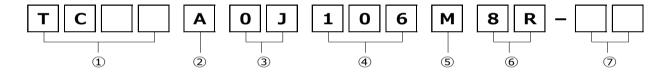
#### Features

- 1) Small package, large capacitance chip tantalum capacitor.
- 2) Low impedance capacitors.
- 3) Screening by thermal shock.

#### Dimensions



### Part No. Explanation



① Series name TC

2 Case style

A: 3216-3216(18)size

3 Rated voltage

rtated renage	•
CODE	Rated voltage(V)
0E	2.5
0G	4
0J	6.3
1A	10
1C	16
1D	20
1E	25
1V	35
1H	50

4 Nominal capacitance

Nominal capacitance in pF in 3 digits:

2 significant figures followed by the figure representing the number of 0's.

**5** Capacitance tolerance

M: ±20%

**6** Taping

8: Tape width

R: Positive electrode on the side opposite to sprocket hole

#### Rated table

Impedance( $\Omega$ )

									mpe	dance(32)
Capa	citance	Rated voltage (V.DC)								
(h	ıF)	2.5	4	6.3	10	16	20	25	35	50
1.0	(105)					7	7	7	7	
1.5	(155)				8.8	5.6				
2.2	(225)				5.6	4.9				
3.3	(335)			5.6	4.9	4.8		4.8		
4.7	(475)		5.6	4.9	4.2	3.9	3.9	3.4		
6.8	(685)			4.2	4	3.8				
10	(106)			4	3	3.5				
15	(156)		4	3	3.5					
22	(226)		3	3.5	3.2	2.3				
33	(336)		3.5	3.2	1.7					
47	(476)		3.2	3.2						
68	(686)		3	3						
100	(107)		3	<b>☆3</b>						
150	(157)									

#### Marking

The indications listed below should be given on the surface of a capacitor.

- (1) Polarity: The polarity should be shown by bar. (on the anode side)
- (2) Rated DC voltage: A voltage code is shown as below table.
- (3) Capacitance: A capacitance code is shown as below table.

Voltago Codo	Rated DC				
Voltage Code	Voltage (V)				
е	2.5				
g	4				
j	6.3				
Α	10				
С	16				
D	20				
E	25				
V	35				
Н	50				

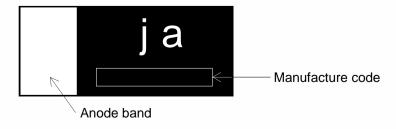
Capacitance	Nominal	Capacitance	Nominal
Code	Capacitance (µF)	Code	Capacitance (µF)
<u>E</u>	0.15	е	15
<u>N</u>	0.33	j	22
<u>s</u>	0.47	n	33
А	1.0	S	47
E	1.5	×	68
J	2.2	а	100
N	3.3	e	150
S	4.7	j	220
W	6.8	c	330
а	10	s	470

Visual typical example

voltage code and capacitance code are variable with parts number.

[TC series A case]

- (1) voltage code
- (2) capacitance code



## Characteristics

Item		Performance	Test conditions (based on JIS C 5101-1 and JIS C 5101-3)					
Operating Temp	erature	-55°C~+125°C	Voltage reduction when temperature exceeds +85°C					
Maximum operate temperature with voltage derating	•	+85℃						
Rated voltage (V	'.DC)	Refer to " Standard list ".	at 85℃					
Category voltage		Refer to " Standard list ".	at 125°C					
Surge voltage (V		Refer to " Standard list ".	at 85℃					
DC Leakage cur	•	Shall be satisfied the value on	As per 4.9 JIS C 5101-1					
Do Lounago ourrom		" Standard list ".	As per 4.5.1 JIS C 5101-3					
			Voltage : Rated voltage for 1min					
Capacitance tole	rance	Shall be satisfied allowance range.	As per 4.7 JIS C 5101-1					
		±20%	As per 4.5.2 JIS C 5101-3					
			Measuring frequency :120 ± 12Hz					
			Measuring voltage :0.5Vrms + 1.5V.DC					
			Measuring circuit :DC Equivalent series circui					
Tangent of loss	angle	Shall be satisfied the value on	As per 4.8 JIS C 5101-1					
(Df,tanδ)	Ü	" Standard list ".	As per 4.5.3 JIS C 5101-3					
,			Measuring frequency :120 ± 12Hz					
			Measuring voltage :0.5Vrms + 1.5V.DC					
			Measuring circuit :DC Equivalent series circui					
Impedance		Shall be satisfied the value on	As per 4.10 JIS C 5101-1					
		" Standard list ".	As per 4.5.4 JIS C 5101-3					
			Measuring frequency :100 ± 10kHz					
			Measuring voltage :0.5Vrms or less					
			Measuring circuit :DC Equivalent series circui					
Resistance to	Appe-	There should be no significant	As per 4.14 JIS C 5101-1					
Soldering	arance	abnormality.	As per 4.6 JIS C 5101-3					
heat		The indications should be clear.	Dip in the solder bath					
	L.C.	Less than 200% of initial limit.	Solder temp :260 ± 10°C					
			Duration :5 $\pm$ 0.5s					
	⊿C/C	Within ±20% of initial value.	Repetition :1					
			After the specimens, leave it at room temperature					
	DF	Less than 200% of initial limit.	for over 24h and then measure the sample.					
	(tanδ)							
Temperature	Appe-	There should be no significant	As per 4.16 JIS C 5101-1					
cycle	arance	abnormality.	As per 4.10 JIS C 5101-3					
		The indications should be clear.	Repetition : 5 cycles					
	L.C.	Less than 200% of initial limit.	(1 cycle : steps 1 to 4) without discontinuation.					
			Temp. Time					
	⊿C/C	Within ±20% of initial value.	1 -55±3°C 30±3min					
	55	1 0000 1 11 11 11	2 Room Temp. 3min or less					
	DF (1 5)	Less than 200% of initial limit.	3 125±2°C 30±3min					
	(tanδ)		4 Room Temp. 3min or less					
			After the specimens, leave it at room temperature					
			for over 24h and then measure the sample.					
			Initial value for ∠C/C shall be the value after					
			mounted.					

Item		Performance	Test conditions (based on JIS C 5101-1 and JIS C 5101-3)					
Moisture	Appe-	There should be no significant	As per 4.22 JIS C 5101-1					
resistance	arance	abnormality.	As per 4.12 JIS C 5101-3					
		The indications should be clear.	After leaving the sample under such atmospheric					
	L.C.	Less than 200% of initial limit.	condition that the temperature and humidity are					
		2000 than 20070 of militar infinit	60±2°C and 90 to 95% RH, respectively, for					
	⊿C/C	Within ±20% of initial value.	500+12/0h leave it at room temperature for					
	20,0	Triami <u>Legio er initial</u> valde.	over 24h and then measure the sample.					
	DF	Less than 200% of initial limit.	Initial value for ∠C/C shall be the value after					
	(tanδ)	2000 than 20070 of findal limit.	mounted.					
Temperature	Temp.:-	1. 55°C	As per 4.29 JIS C 5101-1					
Stability	⊿C/C	Within 0/-15% of initial value.	As per 4.13 JIS C 5101-3					
Otability	20/0	VVIIIII 07 1370 OF ITILIAI VAIGE.	Initial value for ∠C/C shall be the value after					
	DF	Shall be satisfied the value on	mounted.					
	(tanδ)	" Standard list "	mountou.					
	L.C.	_	┥					
	Temp.:	<b>L</b> ⊦85°C	┥					
	⊿C/C	Within +15/0% of initial value.	┥					
	25/5	The state of the s						
	DF	Shall be satisfied the value on	┥					
	(tanδ)	" Standard list "						
	L.C.	Less than 1000% of initial limit.	┪					
		Loos than 100070 of limital limit.						
	Temp.:	-125°С	┪					
	⊿C/C	Within +20/0% of initial value.	7					
	DF	Shall be satisfied the value on						
	(tanδ)	" Standard list "						
	L.C.	Less than 1250% of initial limit.	$\neg$					
Surge	Appe-	There should be no significant	As per 4.26JIS C 5101-1					
voltage	arance	abnormality.	As per 4.14JIS C 5101-3					
ū		The indications should be clear.	Apply the specified surge voltage via the serial					
	L.C.	Less than 200% of initial limit.	resistance of 1kΩ ever 5±0.5 min. for 30±5 s.					
			each time in the atmospheric condition of					
	⊿C/C	Within ±20% of initial value.	85±2°C. Repeat this procedure 1,000 times.					
			After the specimens, leave it at room temperature					
			<del></del>					
	DF	Less than 200% of initial limit.	for over 24h and then measure the sample.					
	DF (tanδ)	Less than 200% of initial limit.	for over 24h and then measure the sample.  Initial value for ∠C/C shall be the value after					
		Less than 200% of initial limit.						
Loading at		Less than 200% of initial limit.  There should be no significant	Initial value for ⊿C/C shall be the value after mounted.					
	(tanδ)		Initial value for ∠C/C shall be the value after mounted.  As per 4.23 JIS C 5101-1					
High	(tanδ)	There should be no significant	Initial value for ∠C/C shall be the value after mounted.  As per 4.23 JIS C 5101-1 As per 4.15 JIS C 5101-3					
High	(tanδ)  Appe- arance	There should be no significant abnormality. The indications should be clear.	Initial value for ∠C/C shall be the value after mounted.  As per 4.23 JIS C 5101-1 As per 4.15 JIS C 5101-3  After applying the rated voltage for 2000+72/0 h					
Loading at High temperature	(tanδ)	There should be no significant abnormality.	Initial value for ∠C/C shall be the value after mounted.  As per 4.23 JIS C 5101-1 As per 4.15 JIS C 5101-3 After applying the rated voltage for 2000+72/0 h without discontinuation via the serial resistance					
High	Appe- arance	There should be no significant abnormality. The indications should be clear. Less than 200% of initial limit.	Initial value for ∠C/C shall be the value after mounted.  As per 4.23 JIS C 5101-1 As per 4.15 JIS C 5101-3  After applying the rated voltage for 2000+72/0 h without discontinuation via the serial resistance of 3Ω or less at a temperature of 85±2°C, leave					
High	(tanδ)  Appe- arance	There should be no significant abnormality. The indications should be clear.	Initial value for ∠C/C shall be the value after mounted.  As per 4.23 JIS C 5101-1 As per 4.15 JIS C 5101-3  After applying the rated voltage for 2000+72/0 h without discontinuation via the serial resistance of 3Ω or less at a temperature of 85±2°C, leave the sample at room temperature / humidity for					
High	Appe- arance	There should be no significant abnormality. The indications should be clear. Less than 200% of initial limit.	Initial value for ∠C/C shall be the value after mounted.  As per 4.23 JIS C 5101-1 As per 4.15 JIS C 5101-3  After applying the rated voltage for 2000+72/0 h without discontinuation via the serial resistance of 3Ω or less at a temperature of 85±2°C, leave					

Item		Performance	Test conditions			
<del>-</del> · ·	1 0	7	(based on JIS C 5101-1 and JIS C 5101-3)			
Terminal	Capa-	The measured value should be	As per 4.35 JIS C 5101-1			
strength	citance	stable.	As per 4.9 JIS C 5101-3			
	Appe-	There should be no significant	A force is applied to the terminal until it bends to			
	arance	abnormality.	1mm and by a prescribed tool maintains the			
			condition for 5s.			
			(See the figure below)			
Adhesiveness		The terminal should not come off.	F(Apply force)  1.0mm  thickness=1.6mm  45 45  As per 4.34 JIS C 5101-1			
			As per 4.8 JIS C 5101-3			
			Apply force of 2N in the two directions shown in			
			the figure below for 10±1s after mounting the			
			terminal on a circuit board.			
			Apply force A circuit board			
Dimensions		Refer to "External dimensions".	Measure using a caliper of JIS B 7507 Class			
			2 or higher grade.			
Resistance to		The indication should be clear.	As per 4.32 JIS C 5101-1			
solvents			As per 4.18 JIS C 5101-3			
			Dip in the isopropyl alcohol for 30±5s, at room			
			temperature.			
Solderability		3/4 or more surface area of the	As per 4.15.2 JIS C 5101-1			
		solder coated terminal dipped in	As per 4.7 JIS C 5101-3			
		the soldering bath should be	Dip speed=25±2.5mm / s			
		covered with the new solder.	Pre-treatment (accelerated aging):			
			Leave the sample on the boiling distilled water			
			for 1h.			
			Solder temp. : 245±5°C			
			Duration: 3±0.5s			
			Solder : M705			
			Flux : Rosin 25% IPA 75%			
Vibration	Capa-	Measure value should not fluctuate	As per 4.17 JIS C 5101-1			
	citance	during the measurement.	Frequency: 10 to 55 to 10Hz/min.			
	Appe-	There should be no significant	Amplitude : 1.5mm			
			•			
	arance	abnormality.	Time: 2h each in X and Y directions			
	arance	abnormality.	Time : 2h each in X and Y directions  Mounting : The terminal is soldered on a print			

# Standard products list

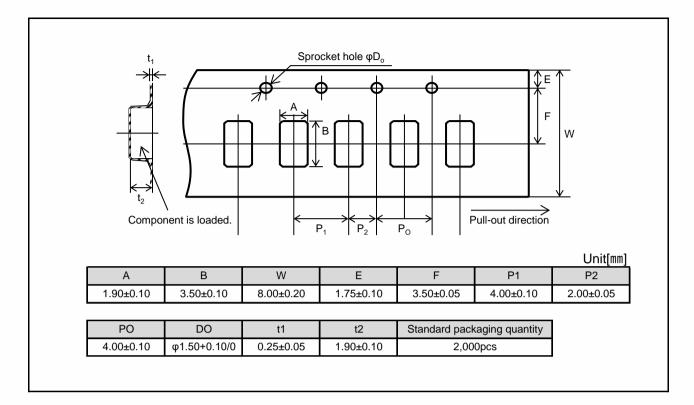
	Rated voltage	Category voltage	Surge voltage	Сар.	Tole- rance	Leakage current		tanδ 120Hz		Impedance
	85°C	125°C	85°C	120Hz	Tance	25°C	120112		100kHz	
Part No.						1WV	-55℃	25℃	125°C	
						1min				
	(V)	(V)	(V)	(µF)	(%)	(μΑ)	(%)	(%)	(%)	(Ω)
TCA0G475M8R	4	2.5	5	4.7	±20	0.5	10	6	8	5.6
TCA0G156M8R	4	2.5	5	15	±20	0.6	12	8	10	4
TCA0G226M8R	4	2.5	5	22	±20	0.9	12	8	10	3
TCA0G336M8R	4	2.5	5	33	±20	1.3	14	10	12	3.5
TCA0G476M8R TCA0G686M8R	4	2.5 2.5	5 5	47 68	±20 ±20	1.9 2.7	30 34	12 18	16 24	3.2
TCA0G886W8R	4	2.5	5	100	±20 ±20	4.0	54	30	36	3
TCA0J335M8R	6.3	4	8	3.3	±20 ±20	0.5	10	6	8	5.6
TCA0J475M8R	6.3	4	8	4.7	±20	0.5	12	8	10	4.9
TCA0J685M8R	6.3	4	8	6.8	±20	0.5	12	8	10	4.2
TCA0J106M8R	6.3	4	8	10	±20	0.6	12	8	10	4
TCA0J156M8R	6.3	4	8	15	±20	0.9	12	8	10	3
TCA0J226M8R	6.3	4	8	22	±20	1.4	14	10	12	3.5
TCA0J336M8R	6.3	4	8	33	±20	2.1	30	12	16	3.2
TCA0J476M8R	6.3	4	8	47	±20	3.0	34	18	24	3.2
TCA0J686M8R	6.3	4	8	68	±20	4.3	54	30	36	3
* TCA0J107M8R	6.3	4	8	100	±20	31.5	54	30	36	3
TCA1A155M8R	10	6.3	13	1.5	±20	0.5	10	6	8	8.8
TCA1A225M8R	10	6.3	13	2.2	±20	0.5	10	6	8	5.6
TCA1A335M8R	10	6.3	13	3.3	±20	0.5	12	8	10	4.9
TCA1A475M8R	10	6.3	13	4.7	±20	0.5	12	8	10	4.2
TCA1A685M8R	10	6.3	13	6.8	±20	0.7	12	8	10	4
TCA1A106M8R	10	6.3	13	10	±20	1.0	12	8	10	3
TCA1A156M8R	10	6.3	13	15	±20	1.5	14	10	12	3.5
TCA1A226M8R	10	6.3	13	22	±20	2.2	30	12	16	3.2
TCA1A336M8R	10	6.3	13	33	±20	3.3	12	8	10	1.7
TCA1C105M8R TCA1C155M8R	16 16	10 10	20 20	1 1.5	±20 ±20	0.5 0.5	10	6 6	8	7 5.6
TCA1C155M6R TCA1C225M8R	16	10	20	2.2	±20 ±20	0.5	10	6	8	4.9
TCA1C335M8R	16	10	20	3.3	±20	0.5	10	6	8	4.8
TCA1C475M8R	16	10	20	4.7	±20	0.8	10	6	8	3.9
TCA1C685M8R	16	10	20	6.8	±20	1.1	10	6	8	3.8
TCA1C106M8R	16	10	20	10	±20	1.6	12	8	10	3.5
TCA1C226M8R	16	10	20	22	±20	3.5	54	30	36	2.3
TCA1D105M8R	20	13	26	1	±20	0.5	10	6	8	7
TCA1D475M8R	20	13	26	4.7	±20	0.9	10	6	8	3.9
TCA1E105M8R	25	16	32	1	±20	0.5	10	6	8	7
TCA1E335M8R	25	16	32	3.3	±20	0.8	10	6	8	4.8
TCA1E475M8R	25	16	32	4.7	±20	1.2	12	8	10	3.4
TCA1V105M8R	35	22	44	1	±20	0.5	10	6	8	7

<sup>\*</sup>This specification has possibility of charge, due to underdevelopment product.

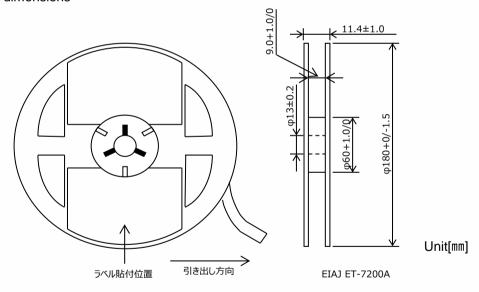
Please ask for latest specification to our sales.



## Packaging specifications



#### Reel dimensions



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# **ROHM Semiconductor:**

<u>TCA1A226M8R TCA0J476M8R TCA1C106M8R TCA1D105M8R TCA1C225M8R TCA1A225M8R TCA0G107M8R TCA0J226M8R TCA0J336M8R TCA1E475M8R TCA0J106M8R TCA1E105M8R TCA0G476M8R TCA0G476M8R</u>