# RV4E031RP HZG

Pch -30V -3.1A Small Signal MOSFET

Datasheet

AEC-Q101 Qualified

V <sub>DSS</sub>	-30V
R <sub>DS(on)</sub> (Max.)	105mΩ
I <sub>D</sub>	±3.1A
PD	1.5W

#### Features

- 1) Low on resistance.
- 2) Small high power package
- 3) Low voltage drive(-4V)
- 4) 100% UIS tested.

Application

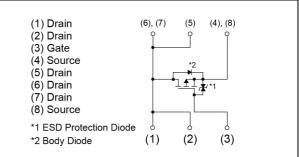
Switching circuits

High side load switch

5) Wettable Flank for automated optical solder inspection(AOI). Electrode part 130µm guarantee.

# • Outline DFN1616-6W

#### Inner circuit



#### Packaging specifications

		Packing	Embossed Tape
	Туре	Reel size (mm)	180
		Tape width (mm)	8
		Quantity (pcs)	3000
		Taping code	TCR1
		Marking	KY

# ● Absolute maximum ratings (T<sub>2</sub> = 25°C, unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	-30	V
Continuous drain current	I <sub>D</sub>	±3.1	Α
Pulsed drain current	۱ <sub>DP</sub> *1	±12	A
Gate - Source voltage	V <sub>GSS</sub>	±20	V
Avalanche current, single pulse	$I_{AS}^{*2}$	3.1	Α
Avalanche energy, single pulse	E <sub>AS</sub> *2	0.7	mJ
Power dissipation	P <sub>D</sub> *3	1.5	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

#### •Thermal resistance

Parameter	Sumbol	Values			Linit
Falameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - ambient	$R_{thJA}^{*3}$	-	-	83.4	°C/W

#### •Electrical characteristics (T<sub>a</sub> = 25°C)

Deverseter	Currente e l	Conditions		Values		Unit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = -1mA	-30	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{i}} I_{D} = -1mA$ referenced to 25°C		-	-24.1	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = -30V, V <sub>GS</sub> = 0V	-	-	-1	μA	
Gate - Source leakage current	$I_{GSS}$ $V_{GS} = \pm 20V, V_{DS} = 0V$		-	-	±10	μA	
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = -10V, I <sub>D</sub> = -1mA	-1.0	-	-2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I <sub>D</sub> = -1mA referenced to 25°C	-	3.3	-	mV/°C	
		V <sub>GS</sub> = -10V, I <sub>D</sub> = -3.1A	-	75	105		
Static drain - source on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -1.5A	-	108	152	mΩ	
		V <sub>GS</sub> = -4V, I <sub>D</sub> = -1.5A	-	122	172		
Forward Transfer Admittance	Y <sub>fs</sub>  *4	V <sub>DS</sub> = -10V, I <sub>D</sub> = -3.1A	1.5	-	-	S	

\*1 Pw $\leq$ 10µs , Duty cycle $\leq$ 1%

\*2 L  $\simeq$  0.1mH, V\_{DD} = -15V, R\_G = 25 $\Omega$ , Starting T\_j = 25°C Fig.3-1,3-2

\*3 Mounted on a Cu board (40mm×40mm×0.8mm)

\*4 Pulsed





## • Electrical characteristics ( $T_a = 25^{\circ}C$ )

Deremeter	Sump of	Conditions	Values			Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	460	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = -10V	-	65	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	40	-	
Turn - on delay time	t <sub>d(on)</sub> *4	$V_{DD} \simeq -15V, V_{GS} = -10V$	-	7	-	
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = -1.55A	-	22	-	20
Turn - off delay time	$t_{d(off)}^{*4}$	$R_L \simeq 9.7\Omega$	-	35	-	ns
Fall time	t <sub>f</sub> *4	R <sub>G</sub> = 10Ω	-	18	-	

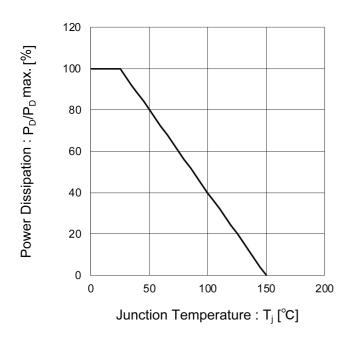
#### • Gate charge characteristics ( $T_a = 25^{\circ}C$ )

Parameter	Sumbol	Conditions	Values			Unit
	Symbol	ymbol Conditions		Тур.	Max.	Unit
Total gate charge	$Q_g^{*4}$	V <sub>DD</sub> ≃ -15V,	-	4.8	-	
Gate - Source charge	Q <sub>gs</sub> *4	I <sub>D</sub> = -3.1A,	-	1.8	-	nC
Gate - Drain charge	${\sf Q}_{\sf gd}{}^{*4}$	V <sub>GS</sub> = -5V	-	1.2	-	

## •Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Parameter	Sumbol	Conditions	Values			Unit
Farameter	Symbol	Conditions	Min.	Тур.	Max.	Ofile
Continuous forward current	I <sub>S</sub>	T - 25°0	-	-	-1.25	А
Pulse forward current	I <sub>SP</sub> *1	T <sub>a</sub> = 25°C	-	-	-12	А
Forward voltage	V <sub>SD</sub> *4	V <sub>GS</sub> = 0V, I <sub>S</sub> = -1.25A	-	-	-1.2	V





#### Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area

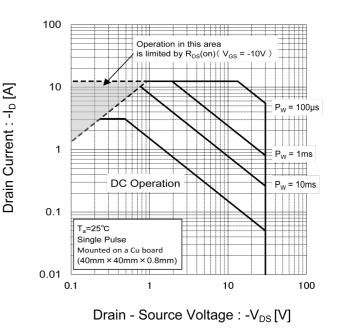
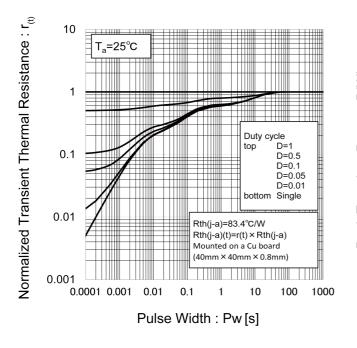
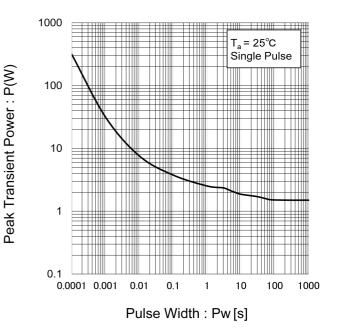


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width



# Fig.4 Single Pulse Maximum Power dissipation





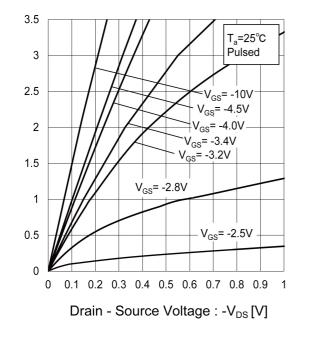


Fig.5 Typical Output Characteristics(I)

Fig.6 Typical Output Characteristics(II)

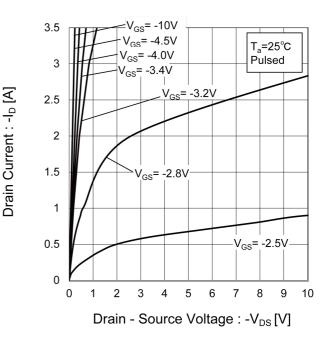


Fig.7 Breakdown Voltage vs. Junction Temperature

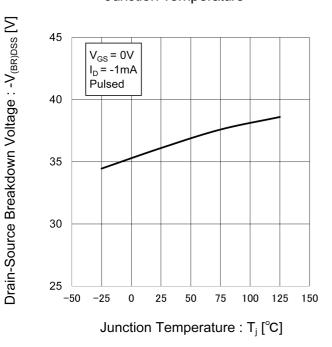
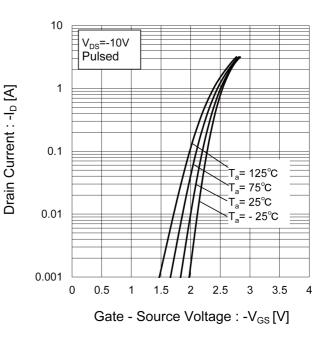


Fig.8 Typical Transfer Characteristics





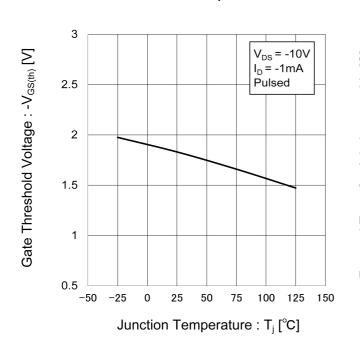


Fig.9 Gate Threshold Voltage vs.

**Junction Temperature** 

#### Fig.10 Forward Transfer Admittance vs. Drain Current

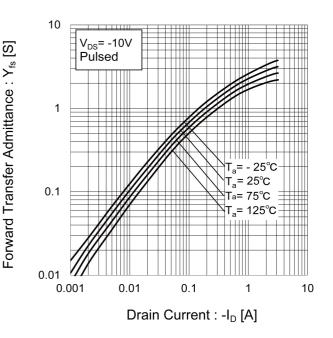
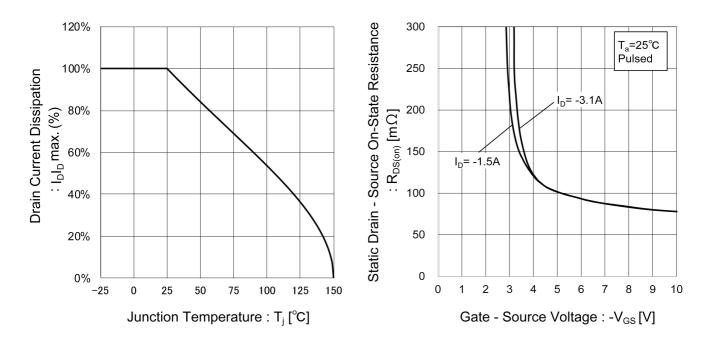


Fig.11 Drain Current Derating Curve

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage





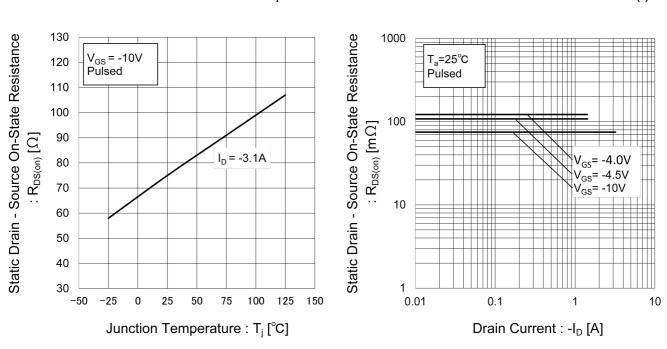


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)





Fig.16 Static Drain - Source On - State

#### • Electrical characteristic curves

Fig.15 Static Drain - Source On - State

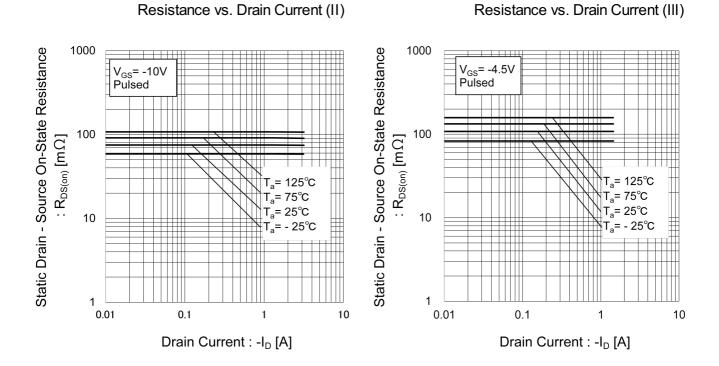
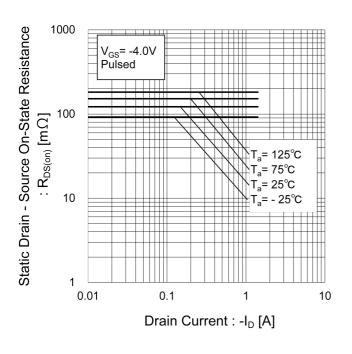
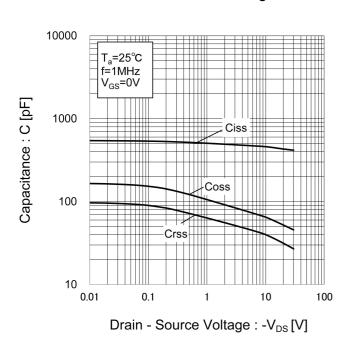


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)









#### Fig.18 Typical Capacitance vs. Drain - Source Voltage

Fig.19 Switching Characteristics

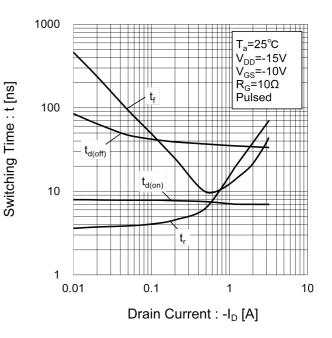


Fig.20 Dynamic Input Characteristics

Gate - Source Voltage : -V<sub>GS</sub> [V]

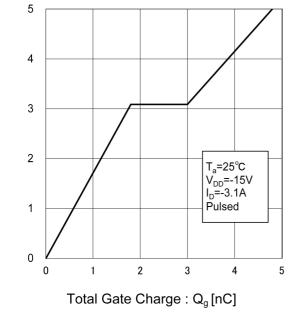
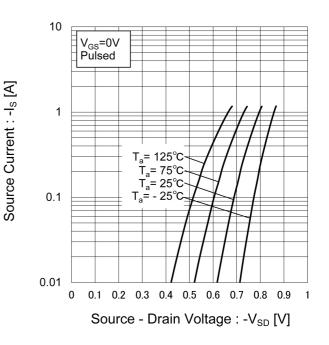


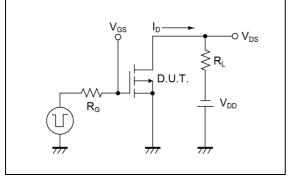
Fig.21 Source Current vs. Source Drain Voltage



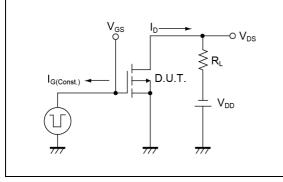


#### Measurement circuits

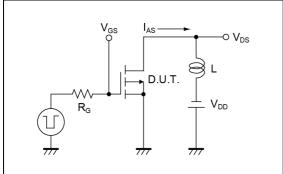
#### Fig. 1-1 SWITCHING TIME MEASUREMENT CIRCUIT



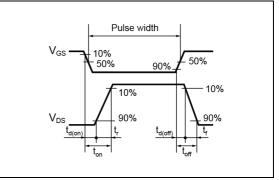
#### Fig. 2-1 GATE CHARGE MEASUREMENT CIRCUIT



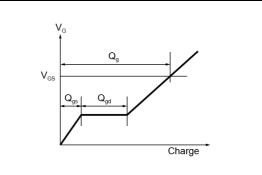
#### Fig.3-1 Avalanche Measurement Circuit



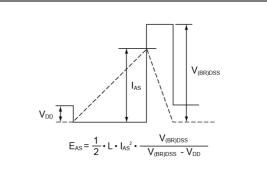
#### Fig. 1-2 SWITCHING WAVEFORMS



#### Fig. 2-2 GATE CHARGE WAVEFORM



#### Fig.3-2 Avalanche Waveform



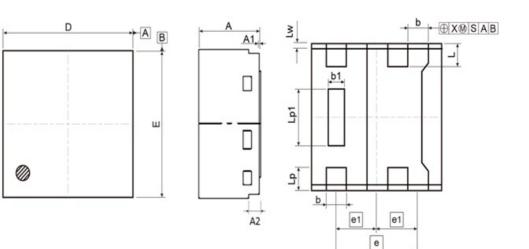
#### Notice

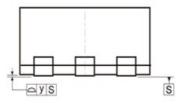
This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



#### Dimensions

### DFN1616-6W





DIM	MILIM	ETERS	INC	HES
DIN	Min	Max	Min	Max
А	0.70	0.80	0.027	0.032
A1	0.00	0.05	0	0.002
A2	0.	13	0.0	005
b	0.20	0.30	0.007	0.012
b1	0.10	0.30	0.003	0.012
D	1.50	1.70	0.059	0.067
E	1.50	1.70	0.059	0.067
е	1.	00	0.039	
e1	0.50		0.0	)19
Lp	0.20	0.30	0.007	0.011
Lp1	0.52	0.72	0.02	0.028
Lw	0.06		0.0	002
Х		0.10	-	0.003
Y		0.10	-	0.003

## Dimension in mm/inches





## Notice

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[b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure

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  - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### Precautions Regarding Application Examples and External Circuits

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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