2.5V Drive Pch MOSFET

RTL030P02

Structure

Silicon P-channel MOSFET

● Features

- 1) Low on-resistance. ($90m\Omega$ at 2.5V)
- 2) High power package.
- 3) High speed switching.
- 4) Low voltage drive. (2.5V)

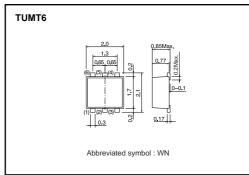
Applications

DC-DC converter

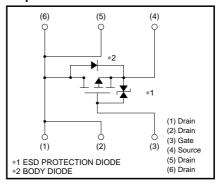
Packaging specifications

	Package	Taping		
Type	Code	TR		
	Basic ordering unit (pieces)	3000		
RTL030P02		0		

●Dimensions (Unit:mm)



●Equivalent circuit



● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol		Limits	Unit	
Drain-source voltage		V _{DSS}		-20	V	
Gate-source voltage		V _{GSS}		±12	V	
Drain current	Continuous	I_D		±3	Α	
Drain current	Pulsed	I _{DP}	*1	±12	Α	
Source current	Continuous	Is		-0.8	Α	
(Body diode)	Pulsed	Isp	*1	-12	Α	
Total power dissipation		PD	*2	1	W	
Channel temperature		Tch		150	°C	
Range of Storage temperature		Tstg		-55 to +150	°C	

^{*1} Pw≤10μs, Duty cycle≤1% *2 Mounted on a ceramic board

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a) *	125	°C/W
* Mounted on a ceramic board.			

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	_	±10	μΑ	Vgs=±12V, Vps=0V
Drain-source breakdown voltage	V _(BR) DSS	-20	_	_	٧	I _D = -1mA, V _{GS} =0V
Zero gate voltage drain current	IDSS	-	_	-1	μΑ	V _{DS} = -20V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	-0.7	_	-2.0	٧	V_{DS} = -10V, I_D = -1mA
Static drain-source on-state resistance		_	50	70	$m\Omega$	I _D = -3.0A, V _G S= -4.5V
	R _{DS (on)} *	_	55	77	$m\Omega$	I _D = -3.0A, V _G S= -4V
resistance		_	90	125	$m\Omega$	I _D = -1.5A, V _G S= -2.5V
Forward transfer admittance	Y _{fs} *	2.0	_	_	S	V _{DS} = -10V, I _D = -1.5A
Input capacitance	Ciss	_	760	_	pF	V _{DS} = -10V
Output capacitance	Coss	_	125	_	pF	Vgs=0V
Reverse transfer capacitance	Crss	_	100	_	pF	f=1MHz
Turn-on delay time	t _{d (on)} *	_	12	_	ns	I _D = -1.5A
Rise time	tr *	-	25	_	ns	V _{DD} ≒ −15V V _{GS} = −4.5V
Turn-off delay time	t _{d (off)} *	_	50	_	ns	$R_{L}=10\Omega$
Fall time	t _f *	-	22	_	ns	R _G =10Ω
Total gate charge	Qg *	-	8.0	-	nC	V _{DD} ≒−15V R _L =5Ω
Gate-source charge	Q _{gs} *	_	1.5	_	nC	$V_{GS}=-4.5V$ $R_{G}=10\Omega$
Gate-drain charge	Q _{gd} *	_	2.5	_	nC	I _D = -3A

^{*}Pulsed

●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	VsD	_	_	-1.2	V	I _S = -0.8A, V _{GS} =0V

•Electrical characteristic curves

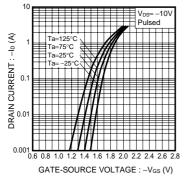


Fig.1 Typical Transfer Characteristics

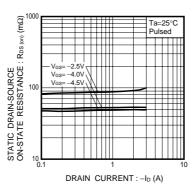


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current

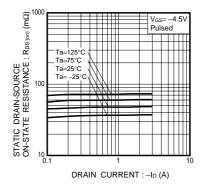


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

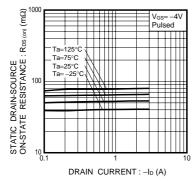


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current

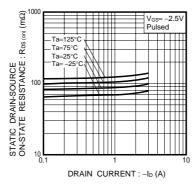


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

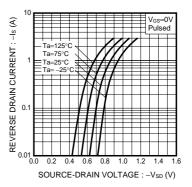


Fig.6 Reverse Drain Current vs. Source-Drain Voltage

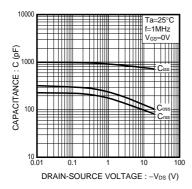


Fig.7 Typical Capacitance vs. Drain-Source Voltage

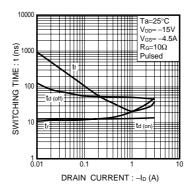


Fig.8 Switching Characteristics

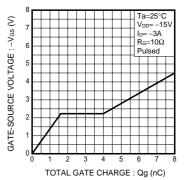


Fig.9 Dynamic Input Characteristics

Measurement circuits

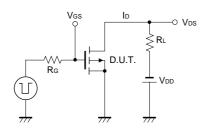


Fig.10 Switching Time Measurement Circuit

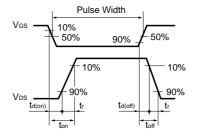


Fig.11 Switching Waveforms

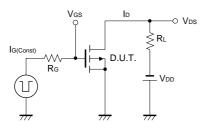


Fig.12 Gate Charge Measurement Circuit

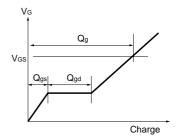


Fig.13 Gate Charge Waveforms

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