



1.8V to 5.5V, 300mA 1ch Synchronous Boost DC/DC Converter

BU33DV7NUX

General Description

The BU33DV7NUX devices provide a power supply solution for products powered by either two-cell alkaline, NiCd or NiMH, or one-cell Li-ion or Li-polymer battery. Output currents can go as high as 300mA while using two alkaline, and discharge it down to 1.8 V. With the MODE pin, the BU33DV7NUX provides mode selection of PWM control or PFM/PWM automatic switching control. When load current is large, the product switches automatically to the PWM mode so that high efficiency is achievable over a wide range of load conditions. The maximum peak current in the boost switch is typically limited to a value of 1.85A. BU33DV7NUX output voltage is fixed by an internal

resistor divider. When VIN voltage is higher than 3.3 V, Vout is connected with Vin.

Features

- Synchronous Boost DC/DC Converter(PFM/PWM) 300mA @Vout=3.3V, Vin=1.8V
- Fixed Output voltage (3.3V)
- Pass-Through Function1 (VIN > VOUT<3.3V>)
- Thermal Shutdown
- VSON010X3030 (Small Package)

•Key Specifications

Input	voltage	range:

Output Voltage range:	3.23V to 3.37V
Output current:	300mA (Max.)
Switching frequency:	0.6MHz (Typ.)
Pch FET ON resistance:	160mΩ (Typ.)
Nch FET ON resistance:	90mΩ (Typ.)
Standby current (MODE=0V):	4.5µA (Max.)
Standby current (MODE=VIN):	1.5µA (Max.)
Operating temperature range:	-40°Cto+85°C

1.8V to 5.5V

•Typical Application Circuit

[] Necessary at PFM



Figure 1. Application Circuit

<Available Features with MODE=0V>

- Pass-Through Function2 during EN-OFF
- Disconnect Function during UVLO
- UVLO-detect Voltage:1.8V(typ)
- UVLO-release Voltage:2.0V(typ)
- PWM(Switching Frequency 600kHz)

<Available Features with MODE=VIN>

- Disconnect Function during EN-OFF and UVLO
- UVLO-detect Voltage:1.8V(typ)
- UVLO-release Voltage:2.0V(typ)
- PFM/PWM(Switching Frequency to 600kHz)

Applications

- Two-Cell Alkaline, NiCd or NiMH or Single-Cell Li Battery-Powered Products
- Portable Audio Players
- PDA
- Cellular Phones
- Personal Medical Products

10-pin small "VSON010X3030" package. <3.1mm (Typ.) x 3.1mm (Typ.) x 0.6mm (Max.)>



VSON010X3030

•Typical Performance characteristics





OProduct structure : Silicon monolithic integrated circuit OThis product is not designed protection against radioactive rays.

Package

•Absolute maximum ratings(Ta=25°C)

Parameter	Symbol	Ratings	Unit	Condition
Maximum applied voltage	Vmax	7.0	V	
Power dissipation1	Pd1	464 (Note1)	mW	1layer(74.2x74.2mm)board (Surface heat radiation copper foil:6.28mm))
Power dissipation2	Pd2	1440 (Note2)	mW	4layer(74.2x74.2mm)board (1,4layer heat radiation copper foil:6.28mm ²) (2,3layer heat radiation copper foil:5500m ²)
Operating temperature range	Topr	-40 to +85	°C	
Storage temperature range	Tstr	-55 to +125	°C	

*1 When it is used by more than $Ta=25^{\circ}$ C, it is reduced by 4.64mW/°C.

*2 When it is used by more than Ta=25°C, it is reduced by 14.4mW/°C.

Operating conditions

Parameter	Symbol	Ratings	Unit	Condition
Power supply voltage range	VCC	1.8 to 5.5	V	VIN terminal voltage

•Electrical characteristics [BU34DV7NUX]

(Unless otherwise specified Ta=25°C, VIN=2.4V)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
Circuit current1(EN=0V)	ICC1A	-	1.6	4.5	μA	EN=0V,MODE=0V
Circuit current2(EN=0V)	ICC2A	-	0.3	1.5	μA	EN=0V,MODE=VIN
Circuit current1 no switching (EN=VIN,VOUT=5V)	ICC1B	-	140	250	μA	EN=VIN,MODE=0V, VOUT=5.0V (not include SW)
Circuit current2 no switching (EN=VIN,VOUT=5V)	ICC2B	-	25	50	μA	EN=VIN,MODE=VIN, VOUT=5.0V (not include SW)
Circuit current1(EN=VIN)	ICC1C	-	3.5	-	mA	EN=VIN,MODE=0V, Io=0mA
Circuit current2(EN=VIN)	ICC2C	-	30	-	μA	EN=VIN,MODE=VIN, Io=0mA
Oscillation frequency	Fsw	0.5	0.6	0.7	MHz	
Output voltage range	Vout	3.23	3.3	3.37	V	lo=1mA
Current limit	llim	1.4	1.85	2.2	А	DC - Current
EN Input High	VIH_EN	0.9	-	-	V	
EN Input Low	VIL_EN	-	-	0.2	V	
MODE Input High	VIH_MODE	0.9	-	-	V	
MODE Input Low	VIL_MODE	-	-	0.2	V	
RSTBO output low voltage	Vrstol	-	0.1	0.2	V	loi=100uA,MODE=0V
RSTBO output high voltage	Vrstoh	VIN-0.2	VIN-0.1	-	V	loi=-100uA,MODE=0V
RSTBC output resistance	Rrstbc	450	600	750	kΩ	
SWN1 switch on resistance	Rswn1	-	90	-	mΩ	VOUT=3.3V
SWP1 switch on resistance	Rswp1	-	160	-	mΩ	
SWN2 switch on resistance	Rswn2	-	1.0	-	kΩ	MODE=VIN,EN=0V
SWP2 switch on resistance	Rswp2	-	60	-	Ω	MODE=0V,EN=0V
UVLO Release Threshold	VuvloR	1.9	2.0	2.1	V	VIN rising
UVLO Detect Threshold	VuvloD	1.75	1.8	1.85	V	VIN falling
UVLO Hysteresis	Vuvlohys	-	0.2	-	V	
VIN Thru	Vinthru	3.2	3.3	3.4	V	
VIN Thru Hysteresis	Vinthruhys	20	50	80	mV	

Block diagram



•	Μ	0	DE	
	IN	ID	ΙТ	DIN

INPUT	PIN		FUNCTION			SW-ON/OFF				
MODE PIN	EN PIN	UVLO	UVLO RELEASE VOLTAGE	DC/DC-control PWM or PFM/PWM	RSTBO	SWP2	SWN2	SWN3	SWP1	SWN1
0V	0V	ON	2.0V(typ)	-	0V	ON	OFF	OFF	OFF	OFF
0V	VIN	ON	2.0V(typ)	PWM	While Soft Start : 0V After Soft Start : VIN	OFF	OFF	ON	Switching	Switching
VIN	0V	OFF	-	-	0V	OFF	ON	OFF	OFF	OFF
VIN	VIN	ON	2.0V(typ)	PFM/PWM	While Soft Start : 0V After Soft Start : VIN	OFF	OFF	ON	Switching	Switching

Pin Configuration

Symbol	Pin No. Function		Terminal circuit
VIN	1	Power supply input	С
SW	2	Inductor connection terminal	С
COMP	3	Phase Compensation Pin	A
MODE	4	Function Select Pin	С
RSTBO	5	Soft Start Output Pin While Soft Start : LOW(GND) After Soft Start : High(VIN)	A
EN	6	EN=VIN: Power-ON EN=GND: Power-OFF	С
RSTBC	7	Low Battery Detect Delay Pin	A
AGND	8	GND	В
PGND	9	GND	В
VOUT	10	Boost voltage output Pin	С

В

VSON010X3030



X Don't use EN PIN and MODE PIN at open.

Input-Output Equivalent Circuit

I/O equivalent circuit diagram is as follows.







Figure 4. Package

•Electrical characteristic curves (Reference data)

Quiescent Current



Quiescent Current - Continued



Frequency



Efficiency





BU33DV7NUX

•Electrical characteristic curves (Reference data) - Continued

Efficiency - Continued





Load Regulation





Load Regulation - Continued

















- Rise - Continued









Io change PWM





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•Electrical characteristic curves (Reference data) - Continued







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Notes

- Load Current 300mA (max)

Timing of possible Load 300mA (max) is dueling RSTBO "H".





-EN: ON<=>OFF PFM (MODE=VIN)

VIN connect to VOUT at Rswp2 MODE=VIN. Please note Drop of VOUT.



•How to select parts of application

• PWM



Parts No.	Name	Value	STYLE(VENDOR)
L1	Inductance	6.8µH	VLF504015M-6R8M(TDK),
			LQH44PN6R8MP0L(Murata)
C1	Capacitor	10µF	X7R,X5R Ceramic
C2	Capacitor	22µF	X7R,X5R Ceramic
C3	Capacitor	470pF	X7R,X5R Ceramic

• PFM



Parts No.	Name	Value	STYLE(VENDOR)
L1	Inductance	6.8uH	VLF504015M-6R8M(TDK),
			LQH44PN6R8MP0L(Murata)
C1	Capacitor	10µF	X7R,X5R Ceramic
C2	Capacitor	22µF	X7R,X5R Ceramic
C3	Capacitor	470pF	X7R,X5R Ceramic
C4	Capacitor	4.7µF	-
R1	Resister	10Ω	-

Notes of board layout

BU33DV7NUX is switching DCDC converter, so characteristics of noise and etc changing by board layout. Please note the following respect besides a general board layout matter when you make PCB.



About heat loss

In the heat design, please operate it in the following condition.

(Please consider the margin etc. because the following temperature is a guarantee temperature.)

- 1. Surrounding temperature Ta must be 85°C or less.
- 2. Loss of IC must be permissible loss Pd or less.

The allowable dissipation (Pd) characteristics are described below.



4layer(74.2 × 74.2mm) board (1,4layer heat radiation copper foil : $6.28mm^2$) (2,3layer heat radiation copper foil : $5500mm^2$)





Caution on use

(1) Absolute Maximum Ratings

An excess in the absolute maximum rating, such as supply voltage, temperature range of operating conditions, etc., can break down devices, thus making impossible to identify breaking mode such as a short circuit or an open circuit. If any special mode exceeding the absolute maximum ratings is assumed, consideration should be given to take physical safety measures including the use of fuses, etc.

(2) The power supply and the GND lines

Design PCB pattern to provide low impedance for the wiring between the power supply and the GND lines. Please take care about interference by common impedance of the wiring pattern when there are two or more power supply and GND line. For the GND line, please note the separation of the large current route and the small signal route including the external circuit.Furthermore, for all power supply terminals to ICs; mount a capacitor between the power supply and the GND terminal. At the same time, in order to use an electrolytic capacitor, thoroughly check to be sure the characteristics of the capacitor to be used present no problem including the occurrence of capacity dropout at a low temperature, thus determining the constant.

(3) GND voltage

Make setting of the potential of the GND terminal so that it will be maintained at the minimum in any operating state.

(4) Short circuit between terminals and erroneous mounting

In order to mount ICs on a set PCB, pay thorough attention to the direction and offset of the ICs. Erroneous mounting can break down the ICs. Furthermore, if a short circuit occurs due to foreign matters entering between terminals or between the terminal and the power supply or the GND terminal, the ICs can break down.

(5) Operation in strong electromagnetic field

Be noted that using ICs in the strong electromagnetic field can malfunction them.

(6) Input terminals

In terms of the construction of IC, parasitic elements are inevitably formed in relation to potential. The operation of the parasitic element can cause interference with circuit operation, thus resulting in a malfunction and then breakdown of the input terminal. Therefore, pay thorough attention not to handle the input terminals, such as to apply to the input terminals a voltage lower than the GND respectively, so that any parasitic element will operate. Furthermore, do not apply a voltage to the input terminals when no power supply voltage is applied to the IC. In addition, even if the power supply voltage is applied, apply to the input terminals a voltage or within the guaranteed value of electrical characteristics.

(7) External capacitor

In order to use a ceramic capacitor as the external capacitor, determine the constant with consideration given to a degradation in the nominal capacitance due to DC bias and changes in the capacitance due to temperature, etc.

(8) Thermal design

Perform thermal design in which there are adequate margins by taking into account the permissible dissipation (Pd) in actual states of use. Moreover, please use it within the range where output Tr doesn't exceed the rated voltage and ASO.

BU33DV7NUX

Caution on use- Continued

(9) Rush current

In CMOS IC, when the power supply is turned on rush current might flow momentarily in logical internal irregular state. Therefore, note drawing the capacity of the power supply coupling, the power supply, and width and drawing the GND pattern wiring, please.

(10) Test terminal and unused terminal processing

Please process a test terminal and unused terminal according to explanations of the function manual and the application note, etc. to be unquestionable while real used. Moreover, please inquire of the person in charge of our company about the terminal without the explanation especially.

(11)Content of material

The application notes etc. are the design material to design the application, and no one of the content securing it. Please decide the application after it examines enough and it evaluates it including external parts.

Status of this document

The Japanese version of this document is formal specification. A customer may use this translation version only for a reference to help reading the formal version.

If there are any differences in translation version of this document formal version takes priority

Ordering part number



VSON010X3030



Lineup

Output Voltage(Typ.)	Package	Orderable Part Number	
3.4V	VSON010X3030	BU34DV7NUX-E2	
3.3V	VSON010X3030	BU33DV7NUX-E2	

•Marking Diagram(s) (TOP VIEW)



Revision History

Date	Revision	Changes
02.Aug.2012	001	New Release

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