Application

- · Motor drive
- · Inverter, Converter
- · Photovoltaics, wind power generation.
- · Induction heating equipment.

Features

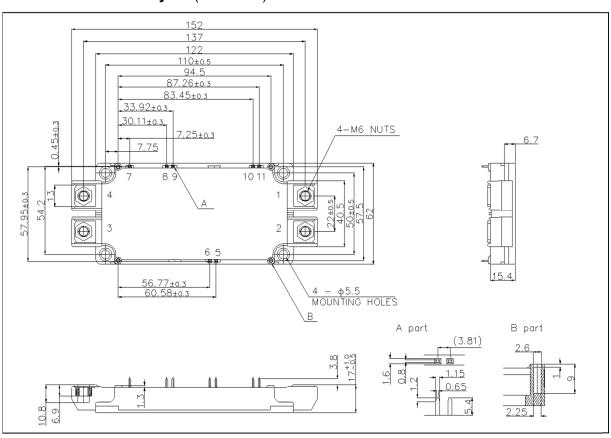
- 1) Low surge, low switching loss.
- 2) High-speed switching possible.
- 3) Reduced temperature dependence.

PCircuit diagram 7 9 8 3,4 6 5 NTC 11 NTC

Construction

This product is a half bridge module consisting of SiC-DMOSFET and SiC-SBD from ROHM.

●Dimensions & Pin layout (Unit : mm)

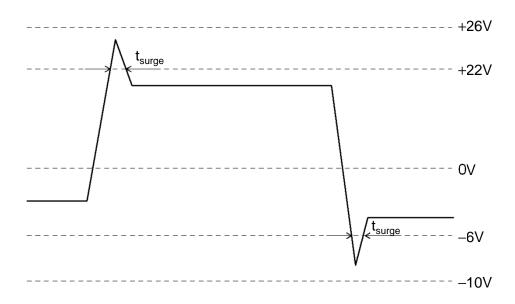


●Absolute maximum ratings (T_j = 25°C)

Parameter	Symbol	Conditions	Limit	Unit	
Drain-source voltage	V_{DSS}	G-S short	1200		
Gate-source voltage(+)	V_{GSS}	D-S short	22	V	
Gate-source voltage(-)	V GSS	D-3 short	- 6		
G - S Voltage (t _{surge} <300nsec)	V_{GSS_surge}	D-S short	-10 to 26		
Drain current *1	I _D	DC (T _c =60°C)	300	300 600 300 600	
	I _{DRM}	Pulse (T _c =60°C) 1ms *2	600		
Source current *1	I _S	DC (T _c =60°C)	300		
	I _{SRM}	Pulse (Tc=60°C) 1ms *2	600		
Total power disspation *3	Ptot	T _c =25°C	1875	W	
Max Junction Temperature	T _{jmax}		175		
Operating junction temperature	T_jop		-40 to150	°C	
Storage temperature	T_{stg}		-40 to125		
Isolation voltage	Visol	Terminals to baseplate, f=60Hz AC 1min.	2500	Vrms	
Mounting torque		Main Terminals : M6 screw	4.5	N · m	
		Mounting to heat shink: M5 screw	3.5]	

^(*1) Case temperature (T_c) is defined on the surface of base plate just under the chips.

Example of acceptable V_{GS} waveform



^(*2) Repetition rate should be kept within the range where temperature rise if die should not exceed T_{j max}.

^(*3) T_i is less than 175°C

●Electrical characteristics (T_i=25°C)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
Static drain-source on-state voltage	V _{DS(on)}		T _j =25°C	-	2.2	2.9	V
		I _D =300A, V _{GS} =18V	T _j =125°C	-	3.0	-	
			T _j =150°C	-	3.4	4.5	
Drain cutoff current	I _{DSS}	V _{DS} =1200V, V _{GS} =0V	-	-	3.2	mA	
Source-drain voltage	V_{SD}		T _j =25°C	-	1.6	2.1	V
		V _{GS} =0V, I _S =300A	T _j =125°C		2.2	-	
			T _j =150°C	-	2.4	3.2	
			T _j =25°C	-	1.4	-	
		V _{GS} =18V, I _S =300A	T _j =125°C		1.6	-	
			T _j =150°C	-	1.7	-	
Gate-source threshold voltage	$V_{GS(th)}$	V_{DS} =10V, I_{D} =68mA		1.6	2.7	4.0	V
Gate-source leakage current	I _{GSS}	V_{GS} =22V, V_{DS} =0V		-	-	0.5	μΑ
		$V_{GS} = -6V, V_{DS} = 0V$		-0.5	-	-	
Switching characteristics	t _{d(on)}	$V_{GS(on)}=18V$, $V_{GS(off)}=0V$		-	80	-	ns
	t _r	V _{DS} =600V	-	70	-		
	t _{rr}	I _D =300A	-	50	-		
	t _{d(off)}	$R_G=0.2\Omega$		-	250	-	
	t _f	inductive load	-	65	-		
Input capacitance	Ciss	V_{DS} =10V, V_{GS} =0V,100k	-	32	-	nF	
Gate Registance	R_{Gint}	T _j =25°C	-	1.6	-	Ω	
NTC Rated Resistance	R25				5.0		$k\Omega$
NTC B Value	B50/25			3370		K	
Stray Inductance	Ls				13	-	nΗ
Creepage Distance	-	Terminal to heat sink			14.5	-	mm
		Terminal to terminal			15.0	-	mm
Clearance Distance	-	Terminal to heat sink			12.0	-	mm
		Terminal to terminal			9.0	-	mm
Junction-to-case thermal resistance		DMOS (1/2 module) *4		-	-	80.0	K/W
		SBD (1/2 module) *4		-	-	0.11	
Case-to-heat sink	R., (c-f)	Case to heat sink, per 1	eat sink, per 1 module,		0.035	-	
Thermal resistance		Thermal grease applied					

- (*4) Measurement of Tc is to be done at the point just under the chip.
- (*5) Typical value is measured by using thermally conductive grease of λ=0.9W/(m K).
- (*6) If the Product is used beyond absolute maximum ratings defined in the Specifications, as its internal structure may be dameged, please replace such Product with a new one.

<Wavelength for Switching Test>

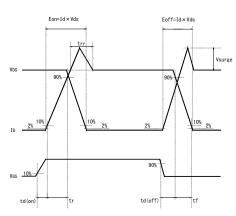
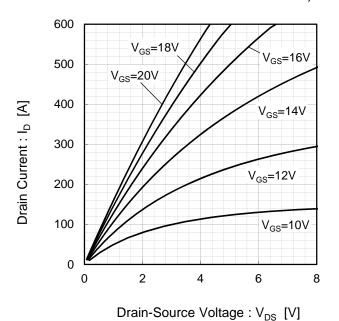
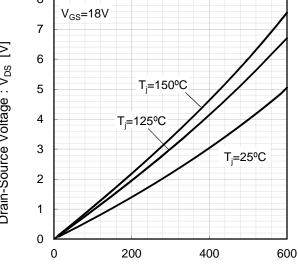


Fig.1 Typical Output Characteristics [T_i =25°C] Fig.2 Drain-Source Voltage vs. Drain Current



Drain-Source Voltage: V_{DS} [V]



Drain Current : I_D [A]

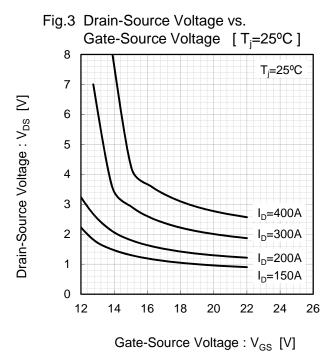
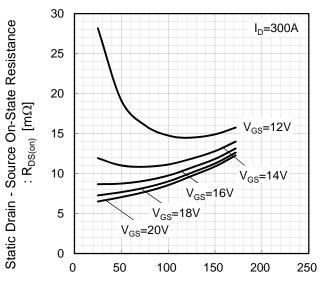
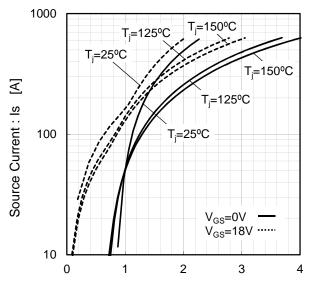


Fig.4 Static Drain - Source On-State Resistance vs. Junction Temperature



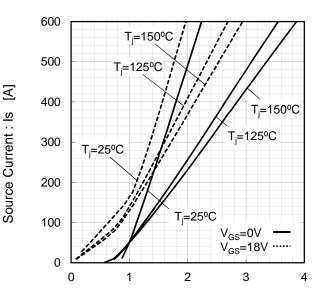
Junction Temperature : T_i [°C]

Fig.5 Forward characteristic of Diode



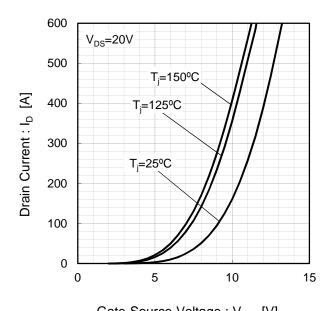
Source-Drain Voltage : V_{SD} [V]

Fig.6 Forward characteristic of Diode



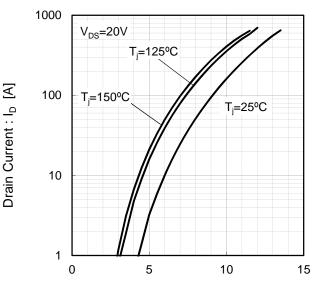
Source-Drain Voltage: V_{SD} [V]

Fig.7 Drain Current vs. Gate-Source Voltage



Gate-Source Voltage : V_{GS} [V]

Fig.8 Drain Current vs. Gate-Source Voltage



Gate-Source Voltage : V_{GS} [V]

Fig.9 Switching Characteristics [T_i=25°C]

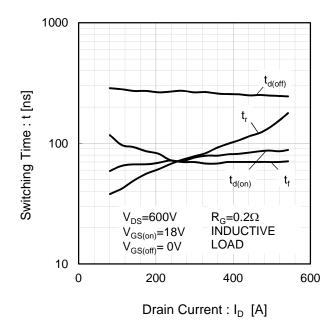
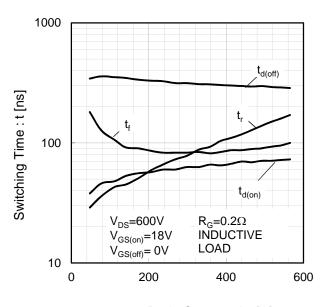


Fig.10 Switching Characteristics [T_i=150°C]



Drain Current : I_D [A]

Fig.11 Switching Loss vs. Drain Current [T=25°C]

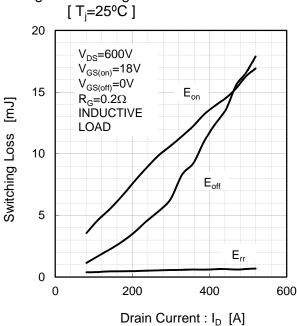
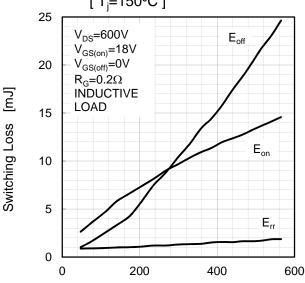
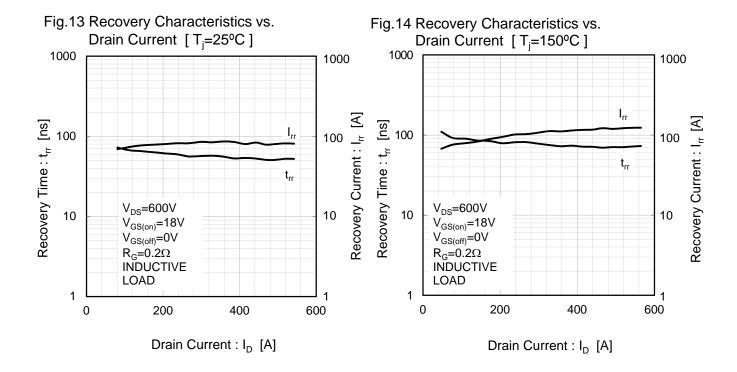
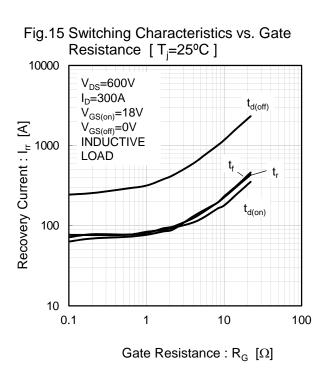


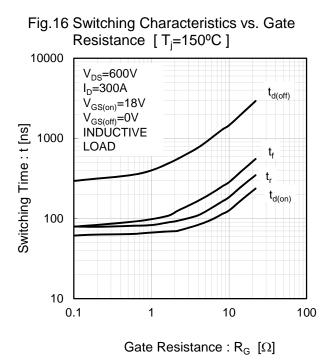
Fig.12 Switching Loss vs. Drain Current [T_i=150°C]



Drain Current : I_D [A]







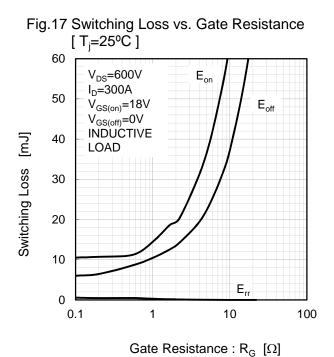


Fig.18 Switching Loss vs. Gate Resistance $[T_i=150^{\circ}C]$ 60 V_{DS} =600V I_{D} =300A 50 $V_{GS(on)}=18V$ $V_{GS(off)}=0V$ INDUCTIVE 40 LOAD 30 E_{off} 20 10 E_{rr} 0 0.1 10 100

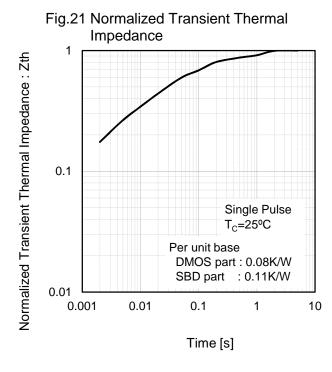
Gate Resistance : R_G [Ω]

Fig.19 Typical Capacitance vs. Drain-Source Voltage 1.E-07 Ciss 1.E-08 Capasitance: C [F] Coss 1.E-09 T_i=25°C Crss $i_{GS}=0V$ 1.E-10 0.01 0.1 1 10 100 1000 Drain-Source Voltage : V_{DS} [V]

Fig.20 Gate Charge Characteristics [T_i=25°C] 25 20 Gate-Source Voltage: V_{GS} [V] 15 10 5 I_D=300A T_i=25°C 0 0 500 1000 1500 2000

Total Gate charge: Qg [nC]

Switching Loss [mJ]



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