Operational Amplifier

High Speed Ground Sense Excellent EMI Immunity CMOS Operational Amplifier

BD77501G BD77502FVM BD77504FV

General Description

BD77501G, BD77502FVM and BD77504FV are single/dual/quad Ground Sense CMOS operational amplifier. An operating voltage range is wide with 7 V to 15 V. This operational amplifier is the most suitable for various applications especially sensor amplifier and so on because it has features of high slew rate and low input bias current.
Also, BD77501G, BD77502FVM and BD77504FV have the advantage of EMI tolerance. It makes easier replacing with conventional products or simpler designing EMI.
Furthermore, this circuit type does not oscillate even with a capacitance of several nF. Set design is possible without worrying about oscillation due to output capacitance.

Features

- EMARMOUR™ Series
- Nano Cap™ integrated OPAMP
- Operating with a Single Power Supply
- Input and output are operable GND sense
- High Slew Rate
- Wide Operating Supply Voltage Range
- High Open Loop Voltage Gain

Applications

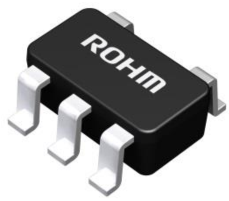
- Sensor Amplifier
- Buffer Application Amplifier
- Current Monitoring Amplifier
- Consumer Electronics

Key Specifications

- Input Offset Voltage: 4 mV (Typ)
- Common-mode Input Voltage Range: V_{SS} to V_{DD}-2.0 V
- Slew Rate: 10 V/µs (Typ)
- Operating Supply Voltage Range
  - Single Supply: 7 V to 15 V
  - Dual Supply: ±3.5 V to ±7.5 V
- Operating Temperature Range: -40 °C to +85 °C

Package

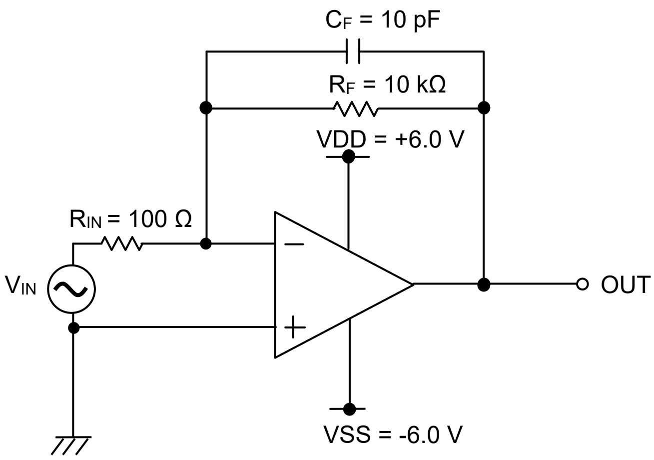
Package	W (Typ) x D (Typ) x H (Max)
SSOP5	2.9 mm x 2.8 mm x 1.25 mm
MSOP8	2.9 mm x 4.0 mm x 0.9 mm
SSOP-B14	5.0 mm x 6.4 mm x 1.35 mm


SSOP5


MSOP8


SSOP-B14

Typical Application Circuit



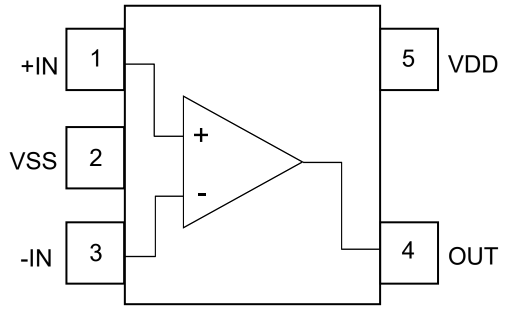
$$V_{OUT} = -\frac{R_F}{R_{IN}} V_{IN}$$

EMARMOUR™ and Nano Cap™ are a trademark or a registered trademark of ROHM Co., Ltd.

○Product structure : Silicon integrated circuit ○This product has no designed protection against radioactive rays.

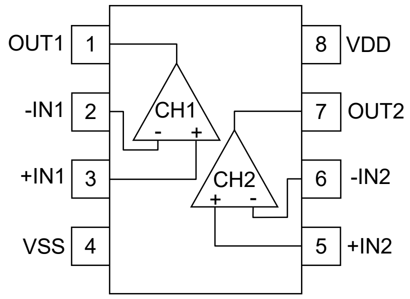
Pin Configuration

BD77501G



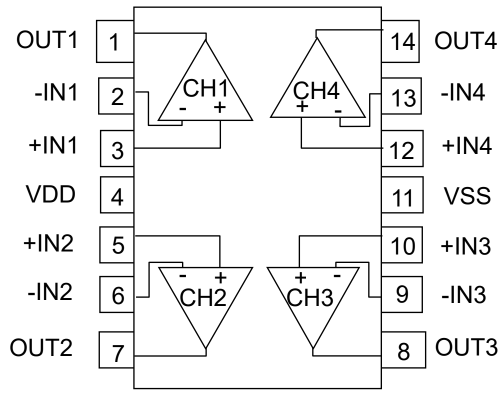
(TOP VIEW)

BD77502FVM



(TOP VIEW)

BD77504FV



(TOP VIEW)

Pin Description

BD77501G

Pin No.	Pin Name	Function
1	+IN	Non-inverting input
2	VSS	Negative power supply / Ground
3	-IN	Inverting input
4	OUT	Output
5	VDD	Positive power supply

BD77502FVM

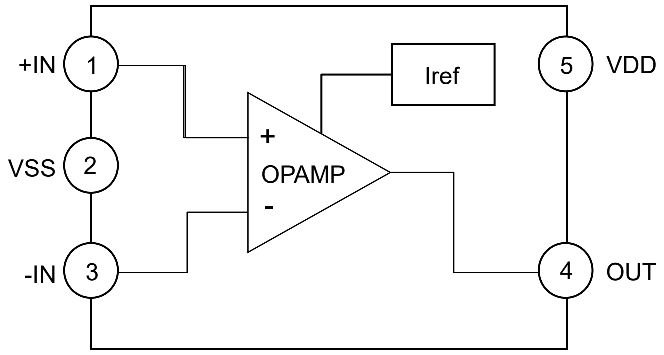
Pin No.	Pin Name	Function
1	OUT1	Output (1ch)
2	-IN1	Inverting input (1ch)
3	+IN1	Non-inverting input (1ch)
4	VSS	Negative power supply / Ground
5	+IN2	Non-inverting input (2ch)
6	-IN2	Inverting input (2ch)
7	OUT2	Output (2ch)
8	VDD	Positive power supply

BD77504FV

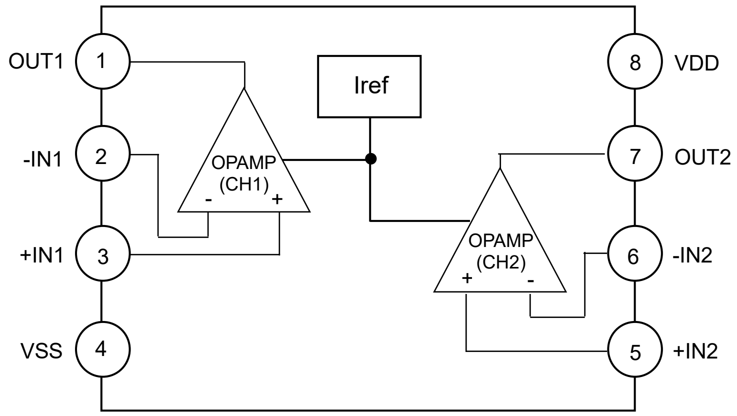
Pin No.	Pin Name	Function
1	OUT1	Output (1ch)
2	-IN1	Inverting input (1ch)
3	+IN1	Non-inverting input (1ch)
4	VDD	Positive power supply
5	+IN2	Non-inverting input (2ch)
6	-IN2	Inverting input (2ch)
7	OUT2	Output (2ch)
8	OUT3	Output (3ch)
9	-IN3	Inverting input (3ch)
10	+IN3	Non-inverting input (3ch)
11	VSS	Negative power supply / Ground
12	+IN4	Non-inverting input (4ch)
13	-IN4	Inverting input (4ch)
14	OUT4	Output (4ch)

Block Diagram

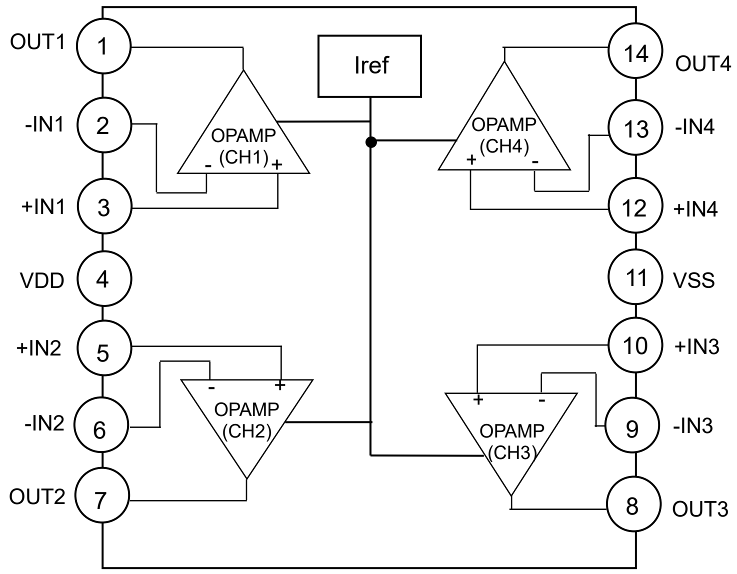
BD77501G



BD77502FVM



BD77504FV



Description of Blocks

1. OPAMP:
   This block is a full-swing output operational amplifier with class-AB output circuit and ground-sense differential input stage.

2. Iref:
   This block supplies reference current which is needed to operate OPAMP block.

Absolute Maximum Ratings (Ta = 25 °C)

Parameter	Symbol	Rating	Unit
Supply Voltage	V_{DD}-V_{SS}	15.5	V
Differential Input Voltage(Note 1)	V_{ID}	V_{DD}-V_{SS}	V
Common-mode Input Voltage Range	V_{ICMR}	(V_{SS} - 0.3) to (V_{DD} + 0.3)	V
Input Current	I_I	±10	mA
Maximum Junction Temperature	Tjmax	150	°C
Storage Temperature Range	Tstg	-55 to +150	°C

Caution 1: *Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operate over the absolute maximum ratings.*

Caution 2: *Should by any chance the maximum junction temperature rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. In case of exceeding this absolute maximum rating, design a PCB with thermal resistance taken into consideration by increasing board size and copper area so as not to exceed the maximum junction temperature rating.*

(Note 1) The differential input voltage indicates the voltage difference between inverting input and non-inverting input. The input pin voltage is set to V_{SS} or more.

Thermal Resistance(Note 2)

Parameter	Symbol	Thermal Resistance (Typ)		Unit
		1s(Note 4)	2s2p(Note 5)	
SSOP5				
Junction to Ambient	θ_{JA}	376.5	185.4	°C/W
Junction to Top Characterization Parameter(Note 3)	Ψ_{JT}	40	30	°C/W
MSOP8				
Junction to Ambient	θ_{JA}	284.1	135.4	°C/W
Junction to Top Characterization Parameter(Note 3)	Ψ_{JT}	21	11	°C/W
SSOP-B14				
Junction to Ambient	θ_{JA}	159.6	92.8	°C/W
Junction to Top Characterization Parameter(Note 3)	Ψ_{JT}	13	9	°C/W

(Note 2) Based on JESD51-2A (Still-Air).
(Note 3) The thermal characterization parameter to report the difference between junction temperature and the temperature at the top center of the outside surface of the component package.
(Note 4) Using a PCB board based on JESD51-3.
(Note 5) Using a PCB board based on JESD51-7.

Layer Number of Measurement Board	Material	Board Size
Single	FR-4	114.3 mm x 76.2 mm x 1.57 mmt

Top	
Copper Pattern	Thickness
Footprints and Traces	70 µm

Layer Number of Measurement Board	Material	Board Size
4 Layers	FR-4	114.3 mm x 76.2 mm x 1.6 mmt

Top		2 Internal Layers		Bottom	
Copper Pattern	Thickness	Copper Pattern	Thickness	Copper Pattern	Thickness
Footprints and Traces	70 µm	74.2 mm x 74.2 mm	35 µm	74.2 mm x 74.2 mm	70 µm

Recommended Operating Conditions

Parameter	Symbol	Min	Typ	Max	Unit
Operating Supply Voltage	V_{DD}	7.0 ±3.5	12.0 ±6.0	15.0 ±7.5	V
Operating Temperature	Topr	-40	+25	+85	°C
Output Load Capacitance(Note 6)	C_L	-	0.01	-	nF

(Note 6) This parameter obtained V_{DD} = 12 V. Not 100 % tested.

Function Explanation

1. EMARMOUR™
   EMARMOUR™ is the brand name given to ROHM products developed by leveraging proprietary technologies covering layout, process, and circuit design to achieve ultra-high noise immunity that limits output voltage fluctuation to ±300 mV or less across the entire noise frequency band during noise evaluation testing under the international ISO11452-2 standard. This unprecedented noise immunity reduces design load while improving reliability by solving issues related to noise in the development of vehicle electrical systems.

2. Nano Cap™
   Nano Cap™ is a combination of technologies which allow stable operation even if output capacitance is connected with the range of nF unit. This circuit type does not oscillate even with a capacitance of several nF. Set design is possible without worrying about oscillation due to output capacitance.

Electrical Characteristics (Unless otherwise specified V_{DD} = 12 V, V_{SS} = 0 V, Ta = 25 °C)

○BD77501G

Parameter	Symbol	Temperature Range	Limit Min	Limit Typ	Limit Max	Unit	Conditions
Input Offset Voltage	V_{IO}	25 °C	-	4	27	mV	Absolute value
Input Offset Current	I_{IO}	25 °C	-	0.001	-	nA	Absolute value
Input Bias Current	I_B	25 °C	-	0.001	-	nA	Absolute value
Supply Current	I_{DD}	25 °C	-	1.3	3.0	mA	R_L = ∞, G = 0 dB
		-40 °C to +85 °C	-	-	4.5		
Output Voltage High	V_{OH}	25 °C	-	0.06	0.25	V	R_L = 10 kΩ, V_{OH} = V_{DD}-V_{OUT}
		-40 °C to +85 °C	-	-	0.3		
Output Voltage Low	V_{OL}	25 °C	-	0.07	0.25	V	R_L = 10 kΩ
		-40 °C to +85 °C	-	-	0.3		
Large Signal Voltage Gain	A_V	25 °C	60	75	-	dB	-
		-40 °C to +85 °C	55	-	-		
Common-mode Input Voltage Range*(Note 1)*	V_{ICMR}	25 °C	0	-	V_{DD}-2.0	V	-
Common-mode Rejection Ratio	CMRR	25 °C	50	70	-	dB	-
		-40 °C to +85 °C	45	-	-		
Power Supply Rejection Ratio	PSRR	25 °C	50	70	-	dB	-
		-40 °C to +85 °C	40	-	-		
Output Source Current*(Note 2)*	I_{OH}	25 °C	2	7.5	-	mA	V_{OUT} = V_{DD}-0.4 V Absolute value
		-40 °C to +85 °C	1	-	-		
Output Sink Current*(Note 2)*	I_{OL}	25 °C	3.5	6.0	-	mA	V_{OUT} = V_{SS}+0.4 V Absolute value
		-40 °C to +85 °C	1	-	-		
Slew Rate	SR	25 °C	-	10	-	V/μs	C_L = 10 pF
Gain Bandwidth Product	GBW	25 °C	-	8	-	MHz	G = 40 dB
Total Harmonic Distortion + Noise	THD+N	25 °C	-	0.05	-	%	V_{OUT} = 4 V_{P-P}, LPF = 80 kHz, f = 1 kHz

(Note 1) Not 100% tested.

(Note 2) Consider the power dissipation of the IC under high temperature environment when selecting the output current value. When the output pin is short-circuited continuously, the output current may decrease due to the temperature rise by the heat generation of inside the IC.

Electrical Characteristics (Unless otherwise specified V_{DD} = 12 V, V_{SS} = 0 V, Ta = 25 °C) - continued

○BD77502FVM

Parameter	Symbol	Temperature Range	Limit Min	Limit Typ	Limit Max	Unit	Conditions
Input Offset Voltage	V_{IO}	25 °C	-	4	27	mV	Absolute value
Input Offset Current	I_{IO}	25 °C	-	0.001	-	nA	Absolute value
Input Bias Current	I_B	25 °C	-	0.001	-	nA	Absolute value
Supply Current	I_{DD}	25 °C	-	2.6	6.0	mA	R_L = ∞, G = 0 dB
		-40 °C to +85 °C	-	-	9.0		
Output Voltage High	V_{OH}	25 °C	-	0.06	0.25	V	R_L = 10 kΩ, V_{OH} = V_{DD}-V_{OUT}
		-40 °C to +85 °C	-	-	0.3		
Output Voltage Low	V_{OL}	25 °C	-	0.07	0.25	V	R_L = 10 kΩ
		-40 °C to +85 °C	-	-	0.3		
Large Signal Voltage Gain	A_V	25 °C	60	75	-	dB	-
		-40 °C to +85 °C	55	-	-		
Common-mode Input Voltage Range*(Note 1)*	V_{ICMR}	25 °C	0	-	V_{DD}-2.0	V	-
Common-mode Rejection Ratio	CMRR	25 °C	50	70	-	dB	-
		-40 °C to +85 °C	45	-	-		
Power Supply Rejection Ratio	PSRR	25 °C	50	70	-	dB	-
		-40 °C to +85 °C	40	-	-		
Output Source Current*(Note 2)*	I_{OH}	25 °C	2	7.5	-	mA	V_{OUT} = V_{DD}-0.4 V Absolute value
		-40 °C to +85 °C	1	-	-		
Output Sink Current*(Note 2)*	I_{OL}	25 °C	3.5	6.0	-	mA	V_{OUT} = V_{SS}+0.4 V Absolute value
		-40 °C to +85 °C	1	-	-		
Slew Rate	SR	25 °C	-	10	-	V/µs	C_L = 10 pF
Gain Bandwidth Product	GBW	25 °C	-	8	-	MHz	G = 40 dB
Total Harmonic Distortion + Noise	THD+N	25 °C	-	0.05	-	%	V_{OUT} = 4 V_{P-P}, LPF = 80 kHz, f = 1 kHz
Channel Separation	CS	25 °C	-	120	-	dB	f = 1 kHz, input referred

(Note 1) Not 100% tested.

(Note 2) Consider the power dissipation of the IC under high temperature environment when selecting the output current value. When the output pin is short-circuited continuously, the output current may decrease due to the temperature rise by the heat generation of inside the IC.

Electrical Characteristics (Unless otherwise specified V_{DD} = 12 V, V_{SS} = 0 V, Ta = 25 °C) - continued
○BD77504FV

Parameter	Symbol	Temperature Range	Limit			Unit	Conditions
			Min	Typ	Max		
Input Offset Voltage	V_{IO}	25 °C	-	4	27	mV	Absolute value
Input Offset Current	I_{IO}	25 °C	-	0.001	-	nA	Absolute value
Input Bias Current	I_B	25 °C	-	0.001	-	nA	Absolute value
Supply Current	I_{DD}	25 °C	-	5.2	12.0	mA	R_L = ∞, G = 0 dB
		-40 °C to +85 °C	-	-	18.0		
Output Voltage High	V_{OH}	25 °C	-	0.06	0.25	V	R_L = 10 kΩ, V_{OH} = V_{DD}-V_{OUT}
		-40 °C to +85 °C	-	-	0.3		
Output Voltage Low	V_{OL}	25 °C	-	0.07	0.25	V	R_L = 10 kΩ
		-40 °C to +85 °C	-	-	0.3		
Large Signal Voltage Gain	A_V	25 °C	60	75	-	dB	-
		-40 °C to +85 °C	55	-	-		
Common-mode Input Voltage Range*(Note 1)*	V_{ICMR}	25 °C	0	-	V_{DD}-2.0	V	-
Common-mode Rejection Ratio	CMRR	25 °C	50	70	-	dB	-
		-40 °C to +85 °C	45	-	-		
Power Supply Rejection Ratio	PSRR	25 °C	50	70	-	dB	-
		-40 °C to +85 °C	40	-	-		
Output Source Current*(Note 2)*	I_{OH}	25 °C	2	7.5	-	mA	V_{OUT} = V_{DD}-0.4 V Absolute value
		-40 °C to +85 °C	1	-	-		
Output Sink Current*(Note 2)*	I_{OL}	25 °C	3.5	6.0	-	mA	V_{OUT} = V_{SS}+0.4 V Absolute value
		-40 °C to +85 °C	1	-	-		
Slew Rate	SR	25 °C	-	10	-	V/µs	C_L = 10 pF
Gain Bandwidth Product	GBW	25 °C	-	8	-	MHz	G = 40 dB
Total Harmonic Distortion + Noise	THD+N	25 °C	-	0.05	-	%	V_{OUT} = 4 V_{P-P}, LPF = 80 kHz, f = 1 kHz
Channel Separation	CS	25 °C	-	120	-	dB	f = 1 kHz, input referred

(Note 1) Not 100% tested.
(Note 2) Consider the power dissipation of the IC under high temperature environment when selecting the output current value. When the output pin is short-circuited continuously, the output current may decrease due to the temperature rise by the heat generation of inside the IC.

Typical Performance Curves

V_{SS} = 0 V

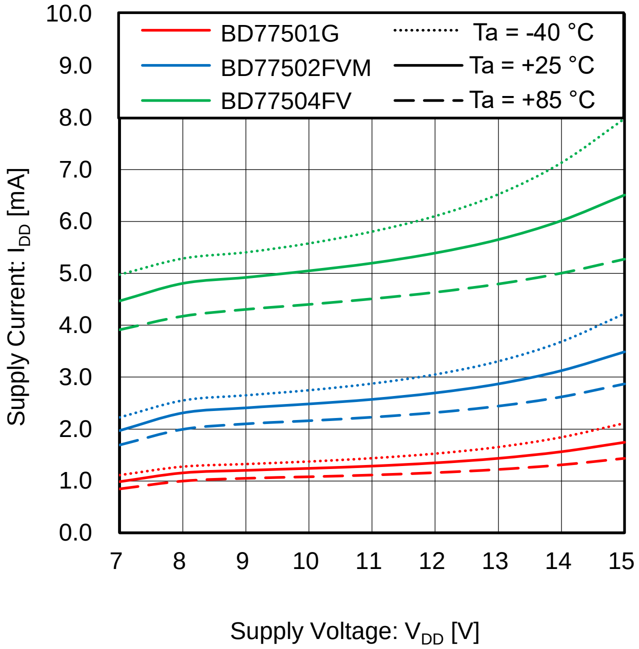


Figure 1. Supply Current vs Supply Voltage

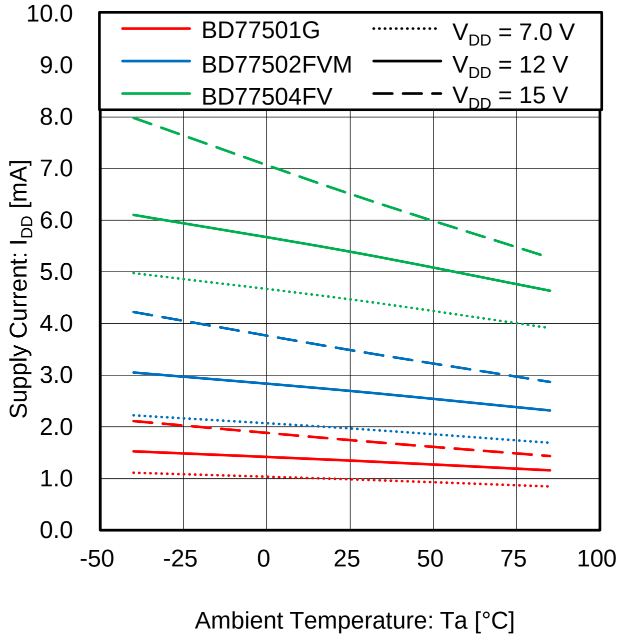


Figure 2. Supply Current vs Ambient Temperature

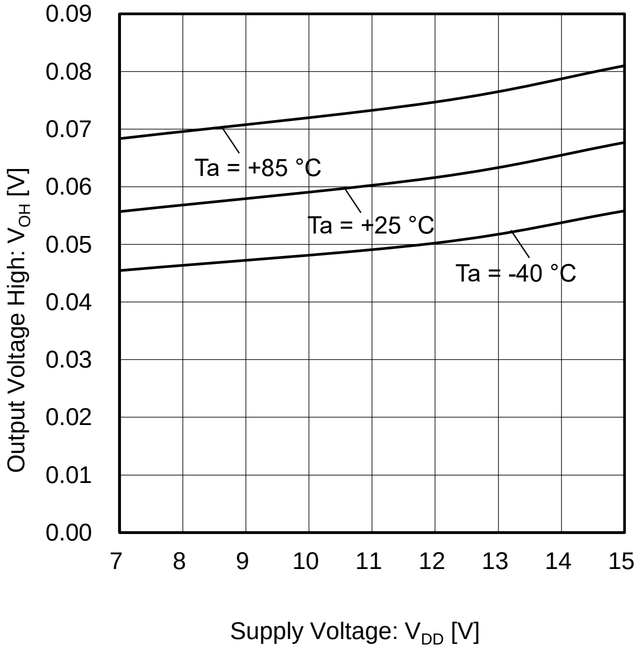


Figure 3. Output Voltage High vs Supply Voltage
(R_L = 10 kΩ, V_{OH} = V_{DD}-V_{OUT})

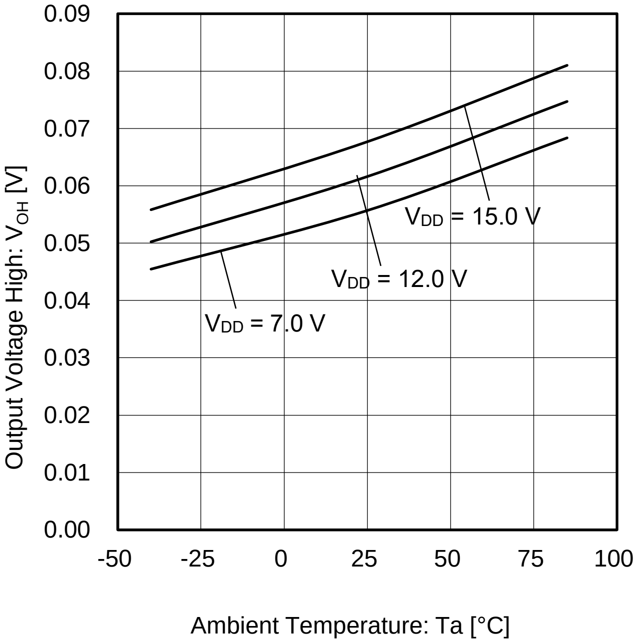


Figure 4. Output Voltage High vs Ambient Temperature
(R_L = 10 kΩ, V_{OH} = V_{DD}-V_{OUT})

(Note) The above data are measurement value of typical sample; it is not guaranteed.

Typical Performance Curves - continued

V_{SS} = 0 V

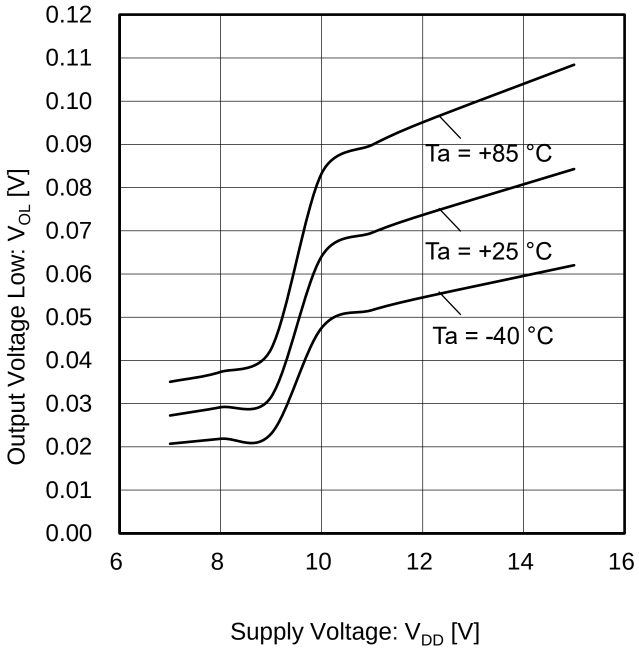


Figure 5. Output Voltage Low vs Supply Voltage
(R_L = 10 kΩ)

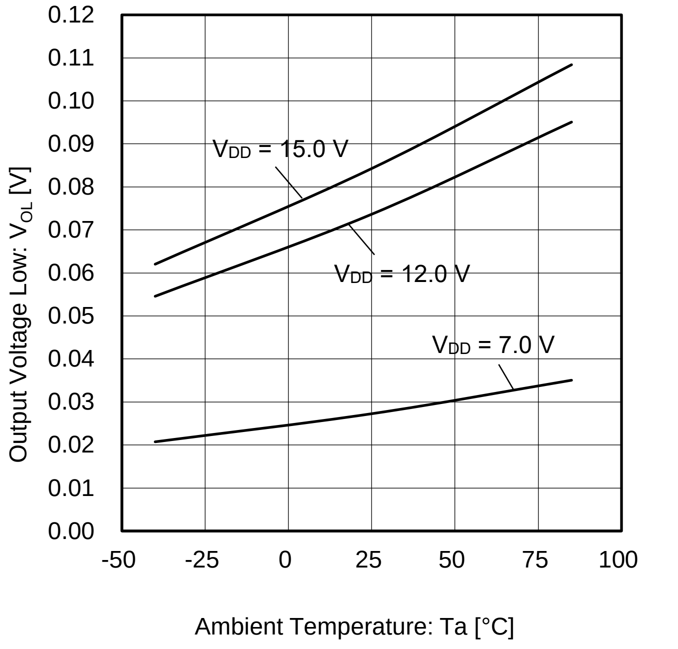


Figure 6. Output Voltage Low vs Ambient Temperature
(R_L = 10 kΩ)

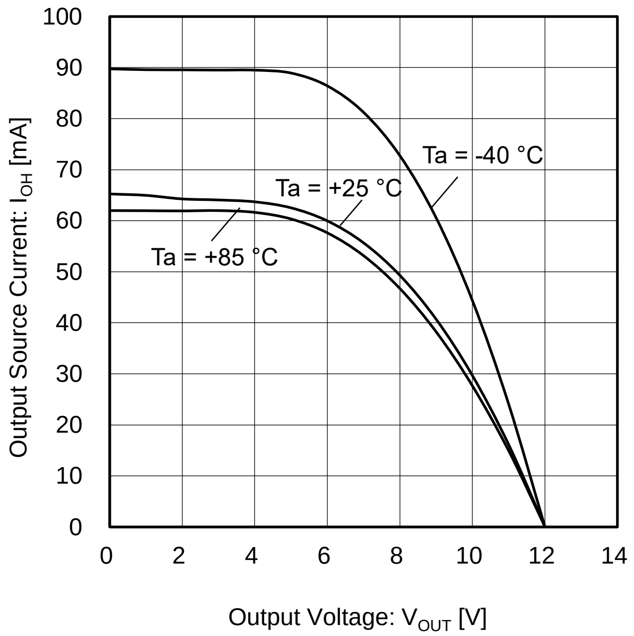


Figure 7. Output Source Current vs Output Voltage
(V_{DD} = 12 V)

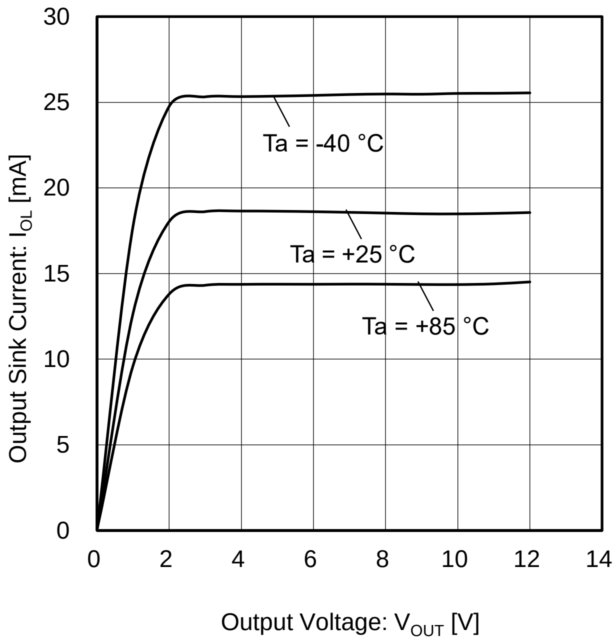


Figure 8. Output Sink Current vs Output Voltage
(V_{DD} = 12 V)

(Note) The above data are measurement value of typical sample; it is not guaranteed.

Typical Performance Curves - continued

V_{SS} = 0 V

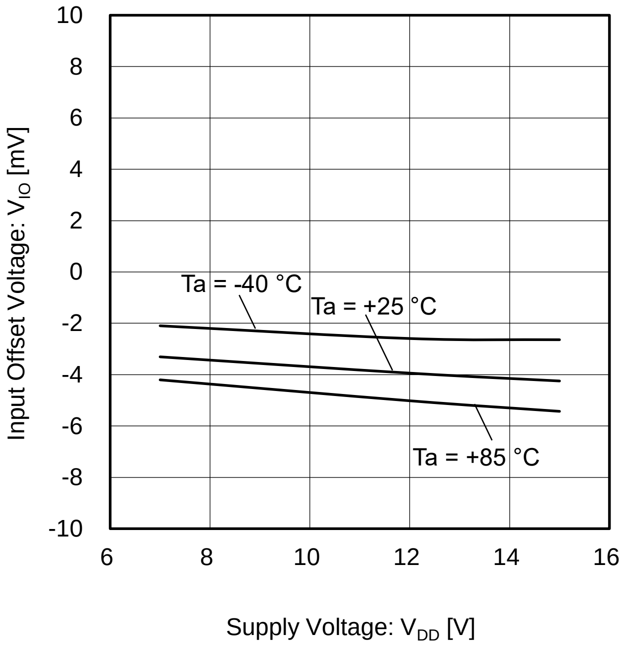


Figure 9. Input Offset Voltage vs Supply Voltage

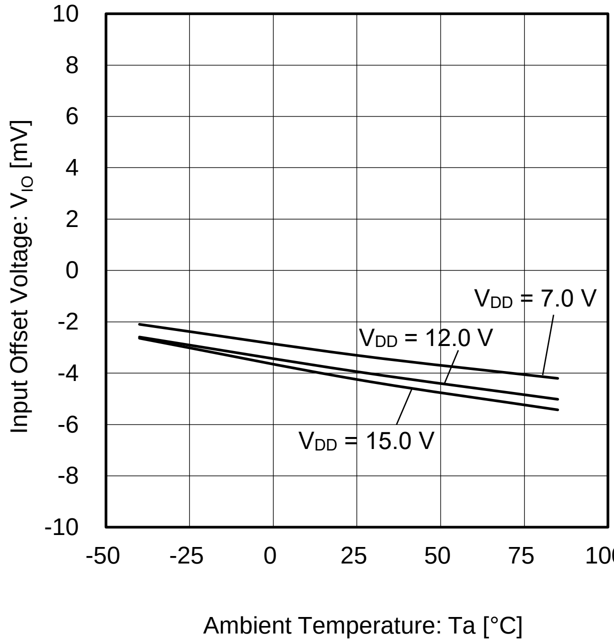


Figure 10. Input Offset Voltage vs Ambient Temperature

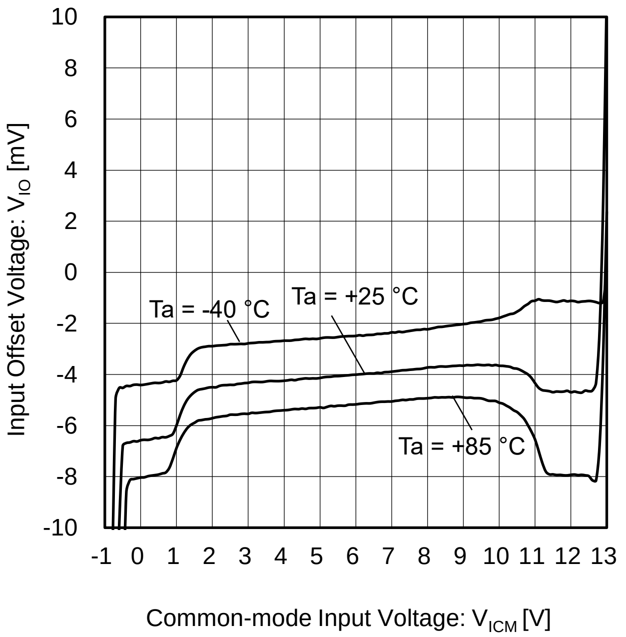


Figure 11. Input Offset Voltage vs Common-mode Input Voltage
(V_{DD} = 12 V)

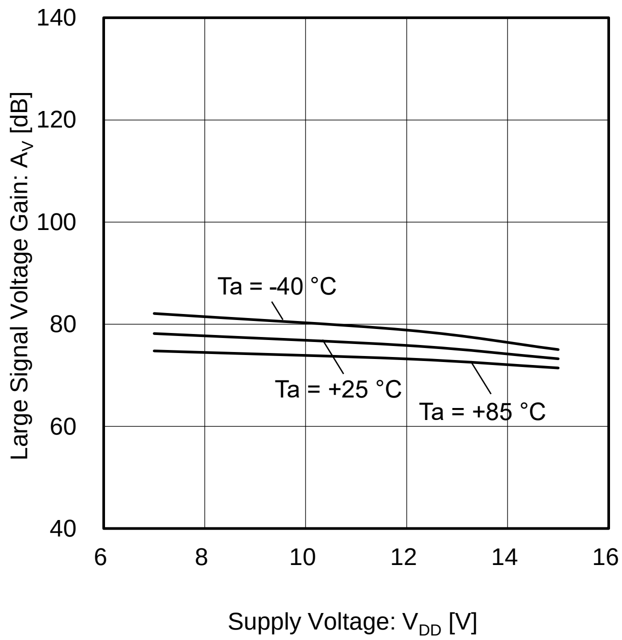


Figure 12. Large Signal Voltage Gain vs Supply Voltage
(R_L = 10 kΩ)

(Note) The above data are measurement value of typical sample; it is not guaranteed.

Typical Performance Curves - continued

V_{SS} = 0 V

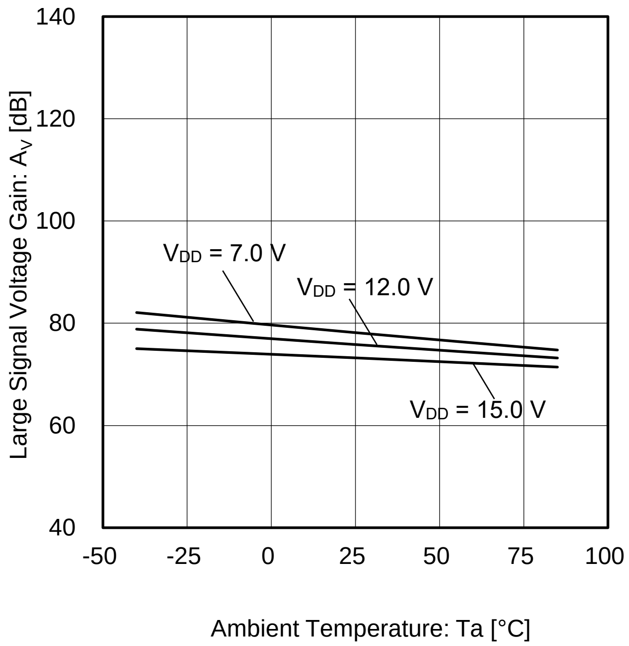


Figure 13. Large Signal Voltage Gain vs Ambient Temperature

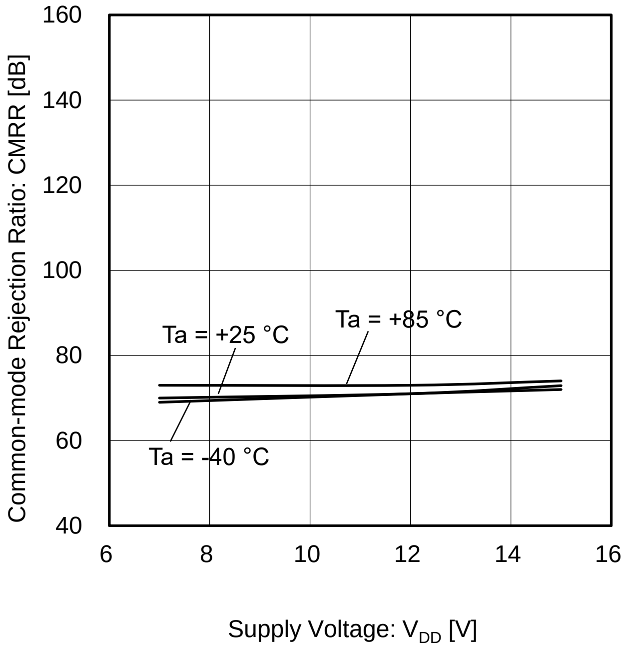


Figure 14. Common-mode Rejection Ratio vs Supply Voltage

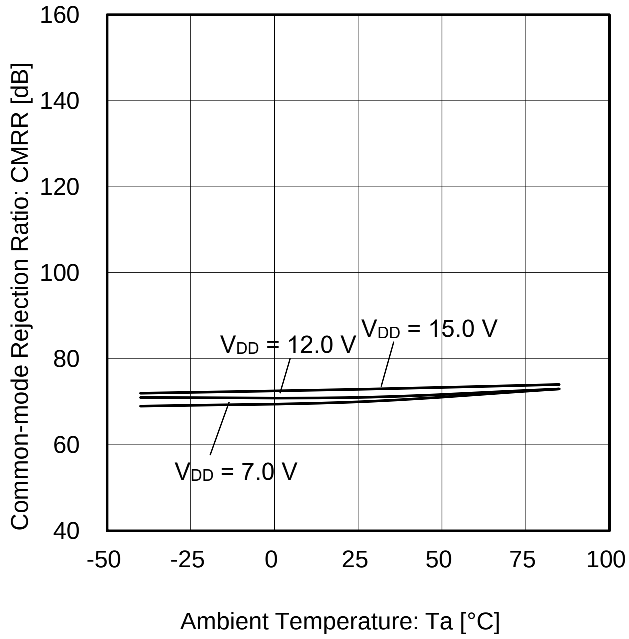


Figure 15. Common-mode Rejection Ratio vs Ambient Temperature

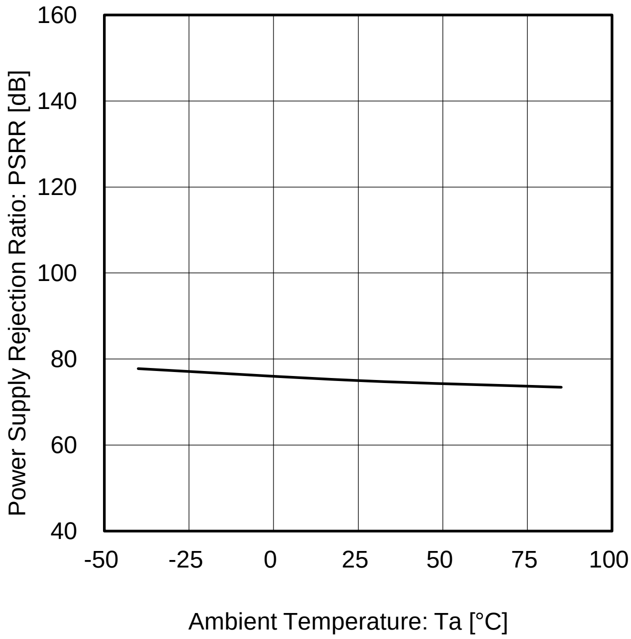


Figure 16. Power Supply Rejection Ratio vs Ambient Temperature

(Note) The above data are measurement value of typical sample; it is not guaranteed.

Typical Performance Curves – continued

V_{SS} = 0 V

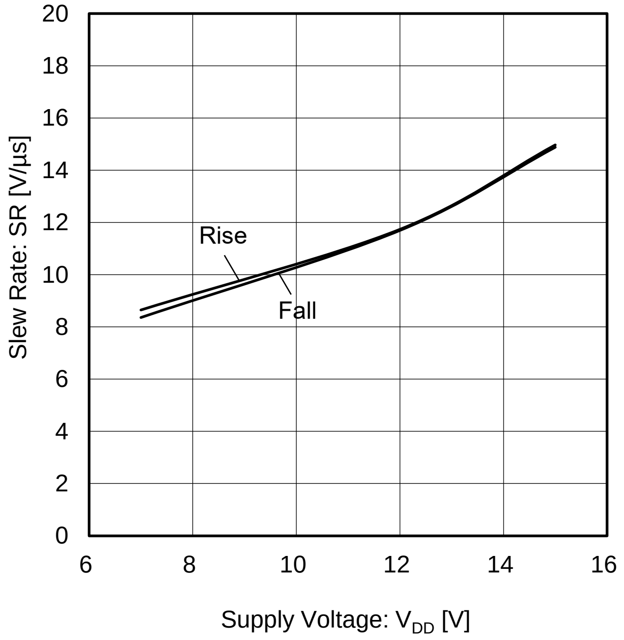


Figure 17. Slew Rate vs Supply Voltage
(Ta = 25 °C)

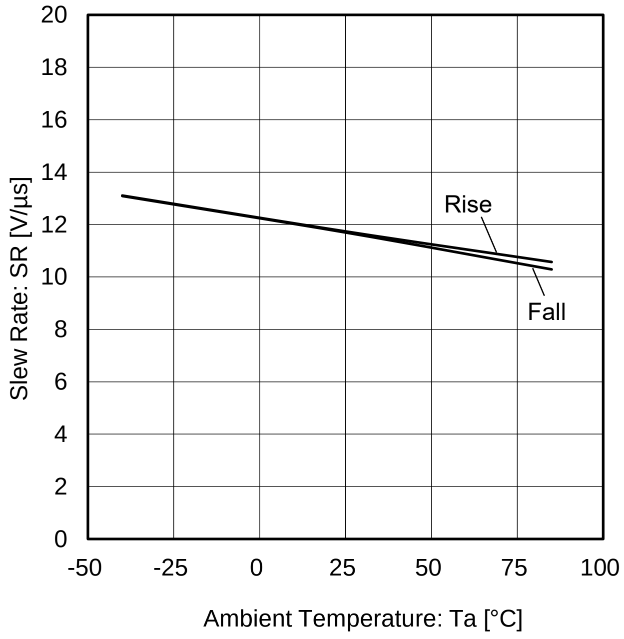


Figure 18. Slew Rate vs Ambient Temperature
(V_{DD} = 12 V)

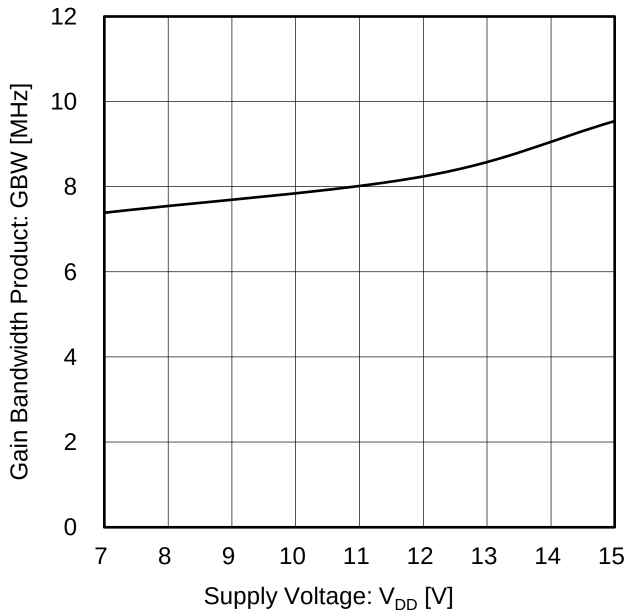


Figure 19. Gain Bandwidth Product vs Supply Voltage
(Inverting Amplifier, Ta = 25 °C)

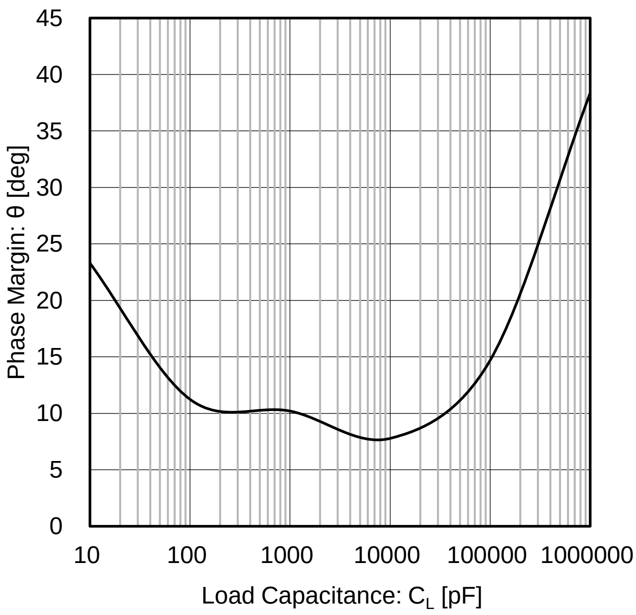


Figure 20. Phase Margin vs Load Capacitance
(R_F = 10 kΩ, G = 40 dB, Ta = 25 °C, V_{DD} = 12 V)

(Note) The above data are measurement value of typical sample; it is not guaranteed.

Application Information

EMI Immunity

BD7750xxxx series have high tolerance for electromagnetic interference from the outside because they have EMI filter, and the EMI design is simple. They are most suitable to replace from conventional products. The data of the IC simple substance on ROHM board are as follows. The test condition is based on ISO11452-2.

<Test Condition> Based on ISO11452-2
Test Circuit: Voltage Follower
V_{DD}: 12 V
V_{IN+}: 6 V
Test Method: Substituted Law
(Progressive Wave)
Field Intensity: 200 V/m
Test Wave: CW (Continuous Wave)
Frequency: 200 MHz to 1000 MHz (2 % step)

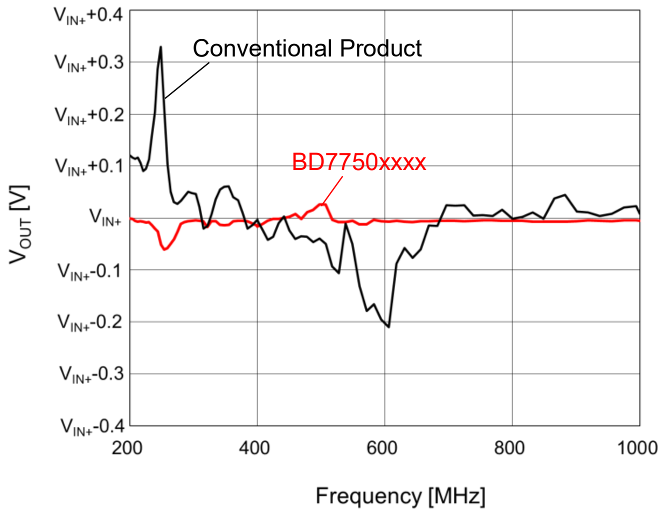
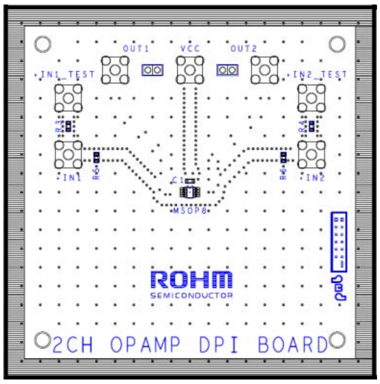
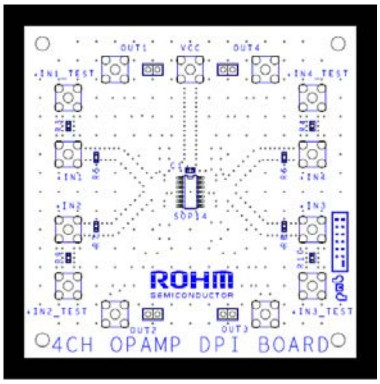


Figure 21. EMI Characteristics

EMI Evaluation Board (BD77501G)



EMI Evaluation Board (BD77502FVM)



EMI Evaluation Board (BD77504FV)

Figure 22. EMI Evaluation Board

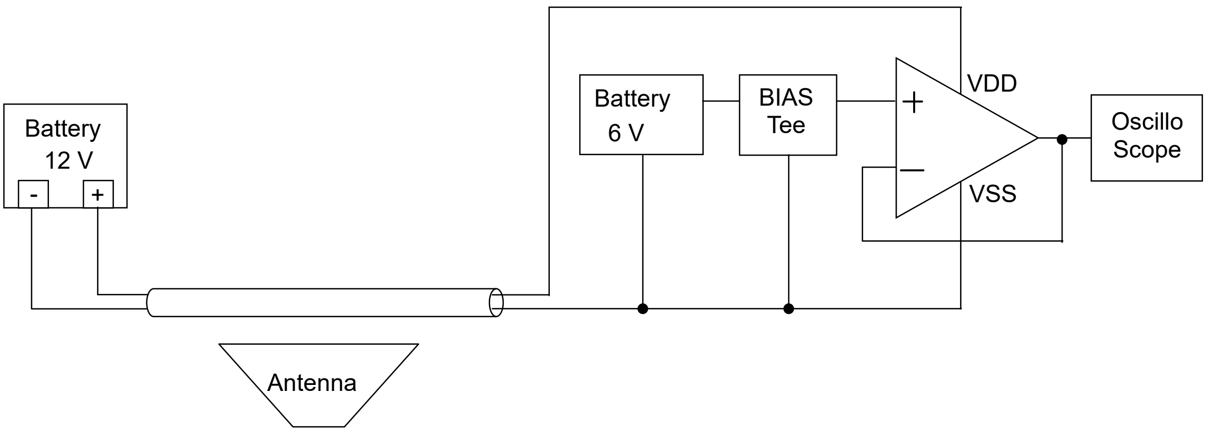


Figure 23. Measurement Circuit of EMI Evaluation

(Note) The above data is obtained using typical IC simple substance on ROHM board. These values are not guaranteed. Design and Evaluate in actual application before use.

Application Information - continued

1. Unused Circuits
   When there are unused circuits, it is recommended that they are connected as in the right figure, and set the non-inverting input pin to electric potential within the input common-mode voltage range (V_{ICM}).

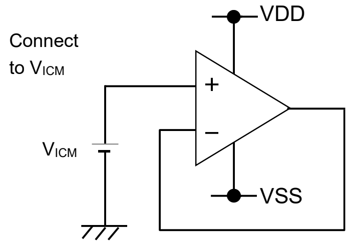


Figure 24. Example of application unused circuit processing

2. Input Voltage
   Applying V_{DD}+0.3V to the input pin is possible without causing deterioration of the electrical characteristics or destruction, regardless of the supply voltage. However, this does not ensure circuit operation. Note that the circuit operates normally only when the input voltage is within the common-mode input voltage range of the electric characteristics.

3. Power Supply (single/dual)
   The Op-Amp operates when the voltage is supplied between the VDD and VSS pin. Therefore, the single supply Op-Amp can be used as dual supply Op-Amp as well.

4. Output Capacitor
   When the VDD pin is shorted to VSS (GND) electric potential in a state where electric charge is accumulated in the external capacitor that is connected to the output pin, the accumulated electric charge flow through parasitic elements or pin protection elements inside the circuit and discharges to the VDD pin. It may cause damage to the elements inside the circuit (thermal destruction). When using this IC as an application circuit which does not constitute a negative feedback circuit and does not occur the oscillation by an output capacitive load such as a voltage comparator, connect a capacitor of 0.1 µF or less to the output pin to prevent IC damage caused by the accumulation of electric charge as mentioned above.

5. Oscillation by Output Capacitor
   Pay attention to the oscillation by capacitive load in designing an application which constitutes a negative feedback loop circuit with this IC.

6. Handling the IC
   Applying mechanical stress to the IC by deflecting or bending the board may cause fluctuations of the electrical characteristics due to the piezo resistance effects. Pay attention to defecting or bending the board.

Application Examples

○Voltage Follower

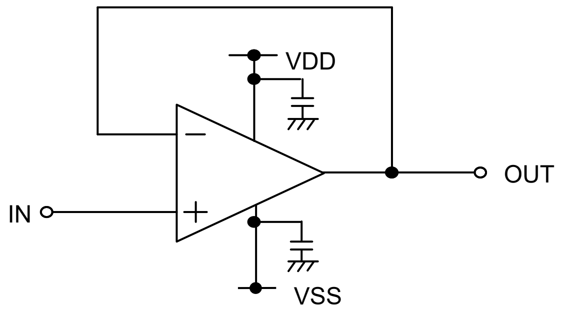


Figure 25. Voltage Follower Circuit

Using this circuit, the output voltage (V_{OUT}) is configured to be equal to the input voltage (V_{IN}). This circuit also stabilizes the output voltage (V_{OUT}) due to high input impedance and low output impedance. Computation for output voltage (V_{OUT}) is shown below.

$$V_{OUT} = V_{IN}$$

○Inverting Amplifier

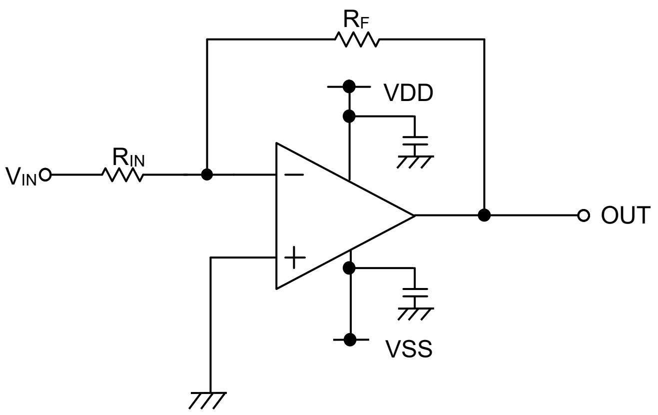


Figure 26. Inverting Amplifier Circuit

For inverting amplifier, input voltage (V_{IN}) is amplified by a voltage gain which depends on the ratio of R_{IN} and R_F, and then it outputs phase-inverted voltage. The output voltage is shown in the next expression.

$$V_{OUT} = -\frac{R_F}{R_{IN}} V_{IN}$$

This circuit has input impedance equal to R_{IN}.

○Non-inverting Amplifier

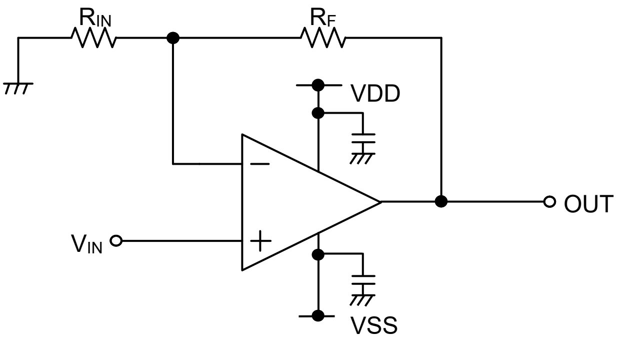


Figure 27. Non-inverting Amplifier Circuit

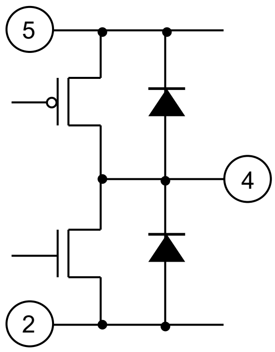
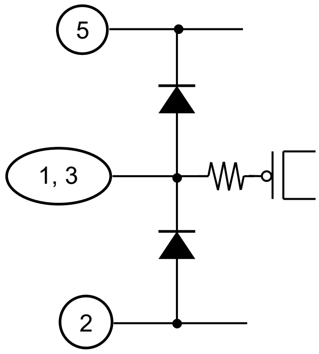
For non-inverting amplifier, input voltage (V_{IN}) is amplified by a voltage gain, which depends on the ratio of R_{IN} and R_F. The output voltage (V_{OUT}) is in-phase with the input voltage (V_{IN}) and is shown in the next expression.

$$V_{OUT} = \left(1 + \frac{R_F}{R_{IN}}\right) V_{IN}$$

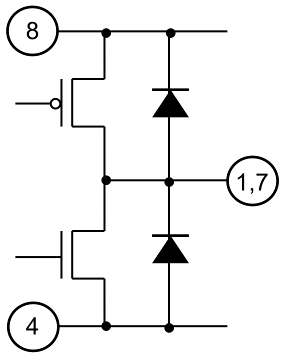
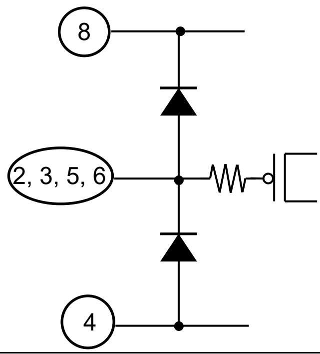
Effectively, this circuit has high input impedance since its input side is the same as that of the operational amplifier.

I/O Equivalence Circuits

○BD77501G

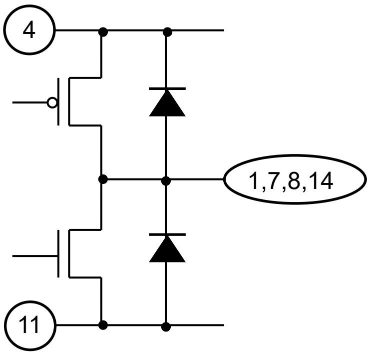
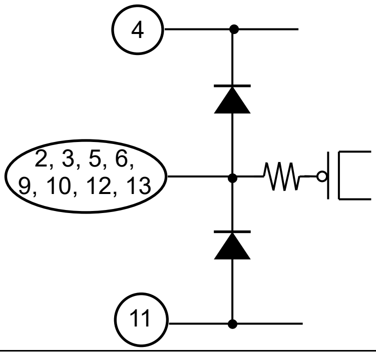
Pin No.	Pin Name	Pin Description	Equivalence Circuit
4	OUT	Output	
1 3	+IN -IN	Input	

○BD77502FVM

Pin No.	Pin Name	Pin Description	Equivalence Circuit
1 7	OUT1 OUT2	Output	
2 3 5 6	-IN1 +IN1 +IN2 -IN2	Input	

I/O Equivalence Circuits - continued

○BD77504FV

Pin No.	Pin Name	Pin Description	Equivalence Circuit
1 7 8 14	OUT1 OUT2 OUT3 OUT4	Output	
2 3 5 6 9 10 12 13	-IN1 +IN1 +IN2 -IN2 -IN3 +IN3 +IN4 -IN4	Input	

Operational Notes

1. **Reverse Connection of Power Supply**
   Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC's power supply pins.

2. **Power Supply Lines**
   Design the PCB layout pattern to provide low impedance supply lines. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

3. **Ground Voltage**
   Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

4. **Ground Wiring Pattern**
   When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

5. **Recommended Operating Conditions**
   The function and operation of the IC are guaranteed within the range specified by the recommended operating conditions. The characteristic values are guaranteed only under the conditions of each item specified by the electrical characteristics.

6. **Inrush Current**
   When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

7. **Testing on Application Boards**
   When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

8. **Inter-pin Short and Mounting Errors**
   Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

9. **Unused Input Pins**
   Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.

Operational Notes – continued

10. **Regarding the Input Pin of the IC**
This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of the P layers with the N layers of other elements, creating a parasitic diode or transistor. For example, (refer to figure below):

When GND > Pin A and GND > Pin B, the P-N junction operates as a parasitic diode.
When GND > Pin B, the P-N junction operates as a parasitic transistor.

Parasitic diodes inevitably occur in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions that cause these diodes to operate, such as applying a voltage lower than the GND voltage to an input pin (and thus to the P substrate) should be avoided.

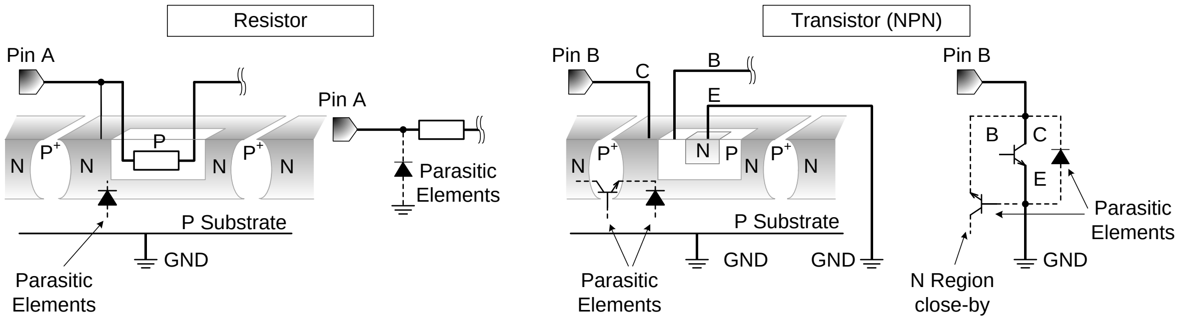


Figure 28. Example of Monolithic IC Structure

11. **Ceramic Capacitor**
When using a ceramic capacitor, determine a capacitance value considering the change of capacitance with temperature and the decrease in nominal capacitance due to DC bias and others.

Ordering Information

B D 7 7 5 0 x x x x - x x

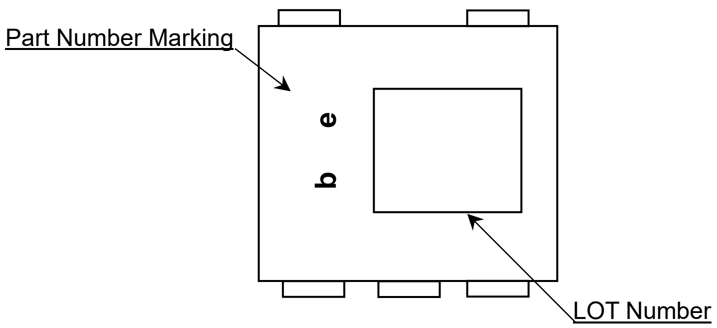
- Number of Channels
  - 1: Single
  - 2: Dual
  - 4: Quad
- Package
  - G: SSOP5
  - FVM: MSOP8
  - FV: SSOP-B14
- Product Rank
  - Packaging and forming specification
  - TR: Embossed tape and reel
  - E2: Embossed tape and reel

Lineup

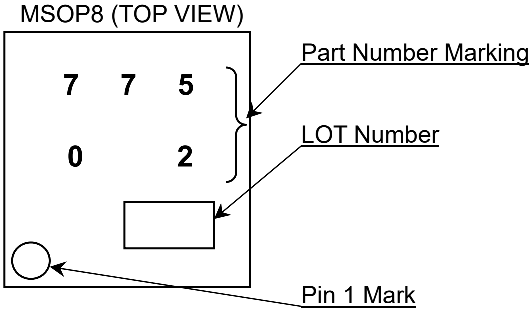
Operating Temperature Range	Operating Supply Voltage	Number of Channels	Package		Orderable Part Number
-40 °C to +85 °C	7.0 V to 15.0 V ±3.5 V to ±7.5 V	Single	SSOP5	Reel of 3000	BD77501G-TR
		Dual	MSOP8	Reel of 3000	BD77502FVM-TR
		Quad	SSOP-B14	Reel of 2500	BD77504FV-E2

Marking Diagram

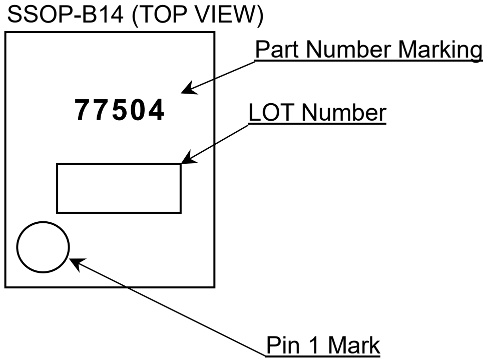
SSOP5 (TOP VIEW)



MSOP8 (TOP VIEW)

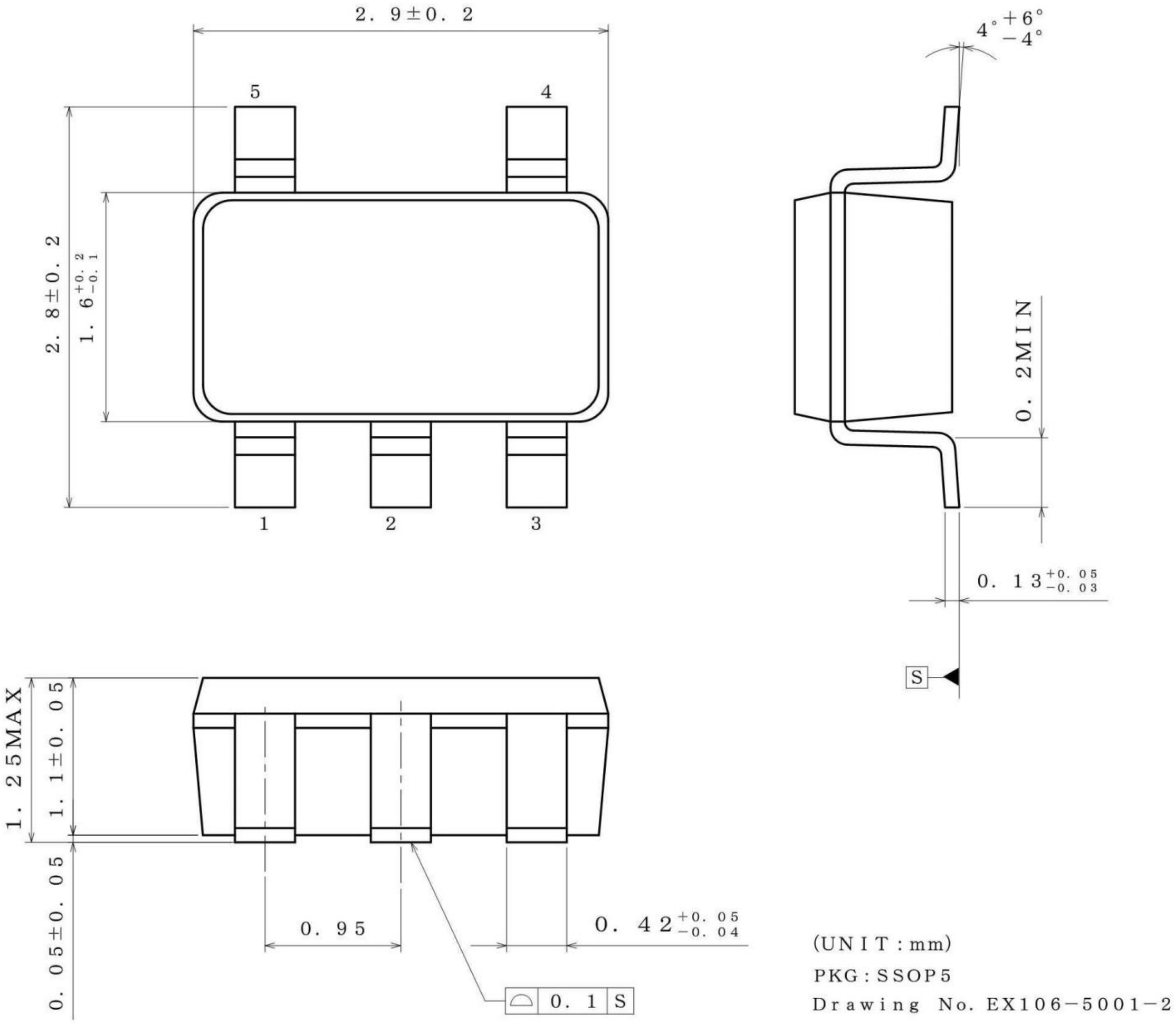


SSOP-B14 (TOP VIEW)



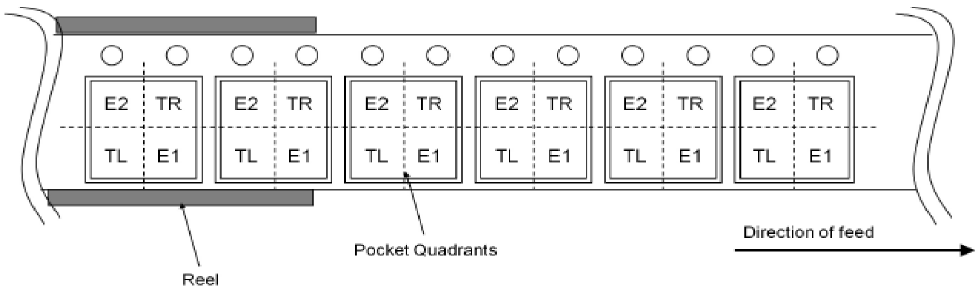
Physical Dimension and Packing Information

Package Name	SSOP5



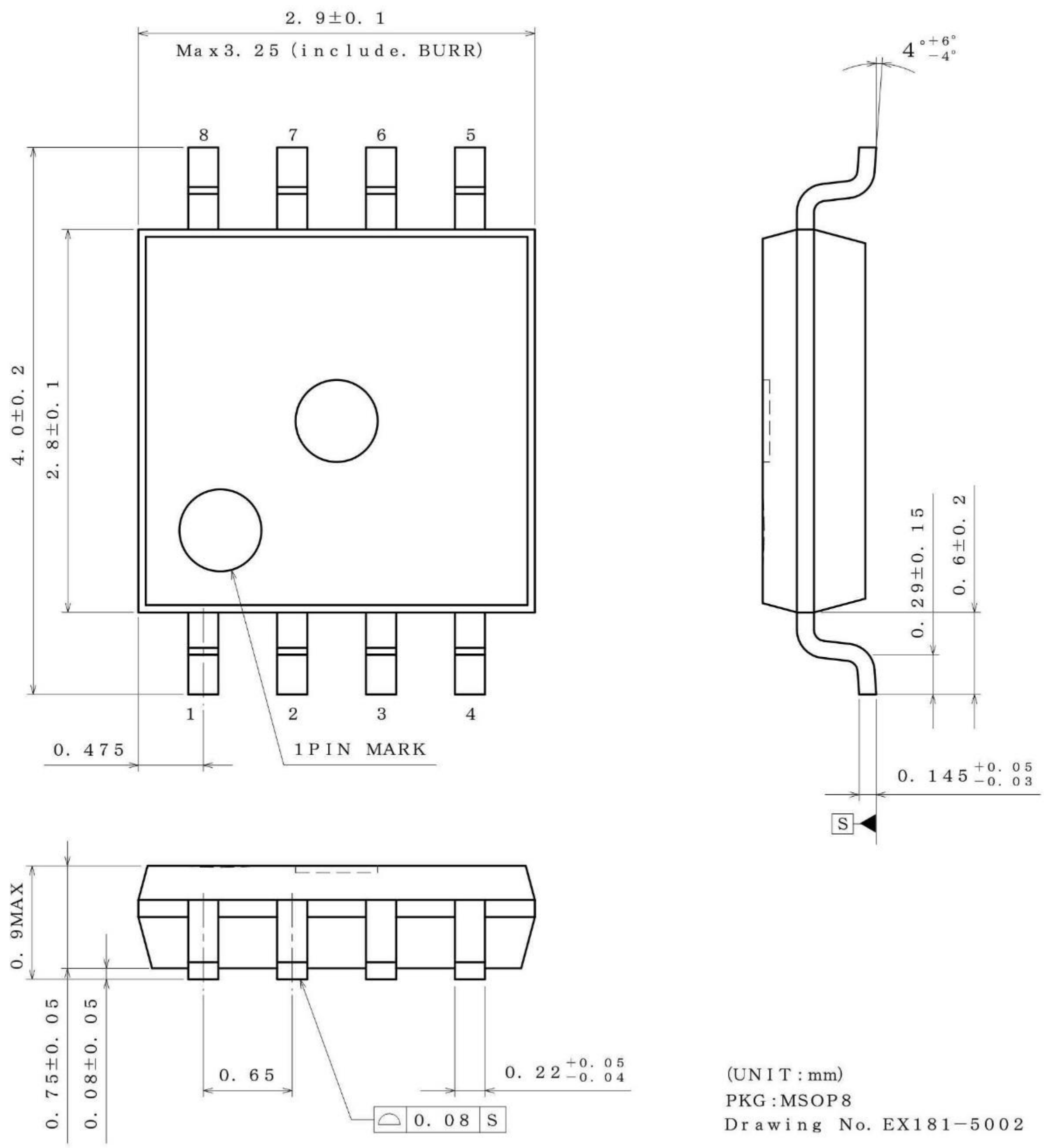
< Tape and Reel Information >

Tape	Embossed carrier tape
Quantity	3000pcs
Direction of feed	TR (The direction is the 1pin of product is at the upper right when you hold reel on the left hand and you pull out the tape on the right hand)



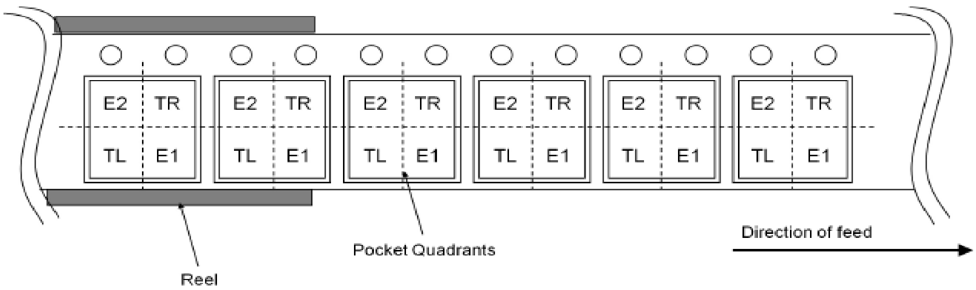
Physical Dimension and Packing Information – continued

Package Name	MSOP8



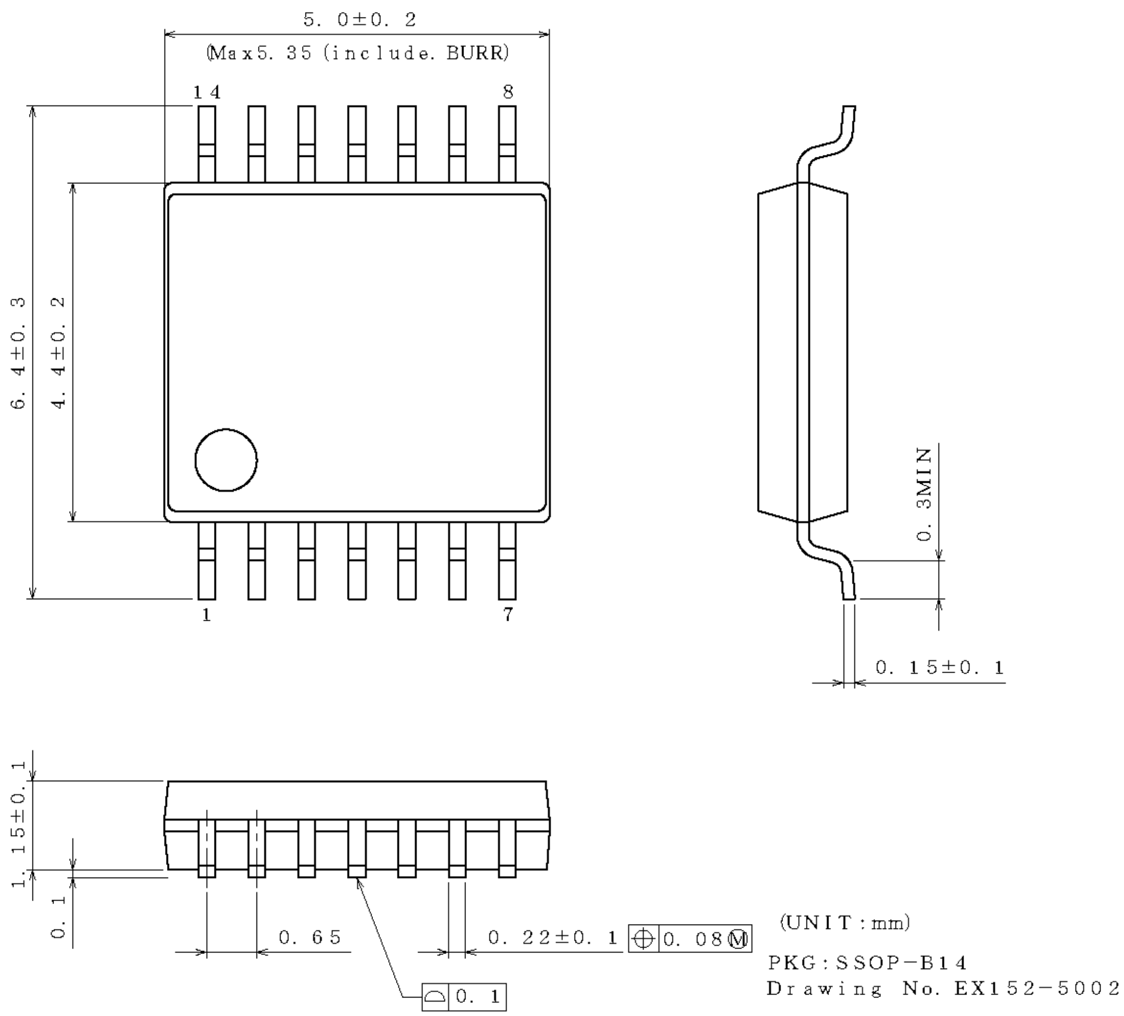
< Tape and Reel Information >

Tape	Embossed carrier tape
Quantity	3000pcs
Direction of feed	TR (The direction is the 1pin of product is at the upper right when you hold reel on the left hand and you pull out the tape on the right hand)



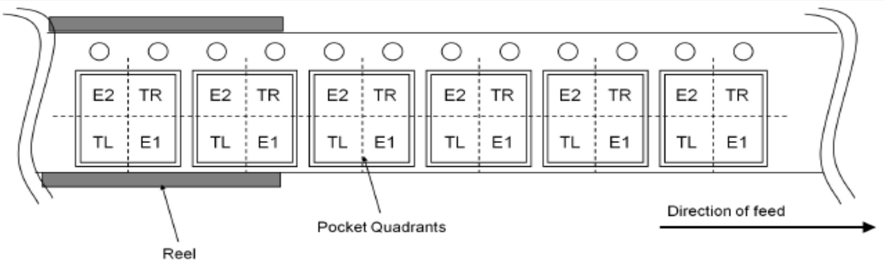
Physical Dimension and Packing Information – continued

Package Name	SSOP-B14



< Tape and Reel Information >

Tape	Embossed carrier tape
Quantity	2500pcs
Direction of feed	E2 The direction is the pin 1 of product is at the upper left when you hold reel on the left hand and you pull out the tape on the right hand



Revision History

Date	Revision	Changes
11.Nov.2019	001	New Release
06.Jul.2020	002	Add Lineup
30.Oct.2020	003	Add Lineup
01.Oct.2022	004	Modified title

Notice

Precaution on using ROHM Products

1. Our Products are designed and manufactured for application in ordinary electronic equipment (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment (Note 1), transport equipment, traffic equipment, aircraft/spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASSⅡb	CLASSⅢ
CLASSⅣ		CLASSⅢ	

2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
   [a] Installation of protection circuits or other protective devices to improve system safety
   [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure

3. Our Products are designed and manufactured for use under standard conditions and not under any special or extraordinary environments or conditions, as exemplified below. Accordingly, ROHM shall not be in any way responsible or liable for any damages, expenses or losses arising from the use of any ROHM's Products under any special or extraordinary environments or conditions. If you intend to use our Products under any special or extraordinary environments or conditions (as exemplified below), your independent verification and confirmation of product performance, reliability, etc, prior to use, must be necessary:
   [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
   [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
   [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl_2, H_2S, NH_3, SO_2, and NO_2
   [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
   [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
   [f] Sealing or coating our Products with resin or other coating materials
   [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.) ; or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
   [h] Use of the Products in places subject to dew condensation

4. The Products are not subject to radiation-proof design.

5. Please verify and confirm characteristics of the final or mounted products in using the Products.

6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.

8. Confirm that operation temperature is within the specified range described in the product specification.

9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.

2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.

2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
   [a] the Products are exposed to sea winds or corrosive gases, including Cl_2, H_2S, NH_3, SO_2, and NO_2
   [b] the temperature or humidity exceeds those recommended by ROHM
   [c] the Products are exposed to direct sunshine or condensation
   [d] the Products are exposed to high Electrostatic

2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.

3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.

4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

Precaution for Product Label

A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

Precaution for Disposition

When disposing Products please dispose them properly using an authorized industry waste company.

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