

## **Sound Processors for Home Theater Systems**

# 5.1ch Sound Processors



BD3813KS,BD3815KS

No.10081EAT04

#### Description

The BD3813KS and BD3815KS sound processors integrate a gain amp and volume/bass/treble controls on a single chip, making them optimally suited for use in audio applications, such as AV receivers, home theater systems and mini-component systems. Used in combination with the BD3812F, a 2ch volume IC, the units enable 6.1ch and 7.1ch operation. In addition, utilization of a BiCMOS process ensures a wide dynamic range (129dB).

#### Features

- 1) Wide dynamic range: 129dB (Tone Bypass, VOL=MUTE, IHF-A)
- 2) Independent 6 channels for Master Volume (0 to -95 dB, MUTE 1dB/Step) Implementation of a resistance ladder type circuit reduces residual noise and shock sounds during switching.
- 3) Low current consumption achieved through utilization of BiCMOS processes
- 4) Maximum output voltage: 4.2Vrms (Vcc=7V, VEE=-7V, RL=10kΩ)
- 5) Built-in 5ch independent input gain amplifier useful for amplifying input signals
- 6) Built-in 2ch output port
- 7) 2-wire serial control (for both 3.3V and 5V)

#### Applications

AV receivers, home theater systems and mini-component systems

#### Line up matrix

| Parameter       | BD3813KS            | BD3815KS            |
|-----------------|---------------------|---------------------|
| Mode Selector   | Yes                 | Yes                 |
| Input Gain      | 0, 6, 12dB          | 0, 6, 18dB          |
| Volume          | 0 to -95dB 1dB/Step | 0 to -95dB 1dB/Step |
| Bass, Treble    | ±14dB 2dB/Step      | ±14dB 2dB/Step      |
| Number of Ports | 2                   | 2                   |
| Package         | SQFP56              | SQFP56              |

● Absolute maximum ratings (Ta=25°C)

| Parameter                   | Symbol          | Ratings                         | Unit |
|-----------------------------|-----------------|---------------------------------|------|
| Dower Cumply Voltage        | V <sub>CC</sub> | 7.5 <sup>*1</sup>               | V    |
| Power Supply Voltage        | V <sub>EE</sub> | -7.5                            | V    |
| Input Signal Voltage        | V <sub>IN</sub> | V <sub>CC</sub> +0.3 to VEE-0.3 | V    |
| Power Dissipation           | Pd              | 1000 <sup>*2</sup>              | mW   |
| Operating Temperature Range | Topr            | 20 to 75                        | °C   |
| Storage Temperature Range   | Tastg           | 55 to 125                       | ô    |

Applying voltage only to the  $V_{CC}$  side, even if within the specified power supply voltage range, may cause excessive current to flow, resulting in permanent damage to the IC.  $\,$  Therefore, when starting up the power supplies, VEE and  $\,$  V<sub>CC</sub> should either be powered ON simultaneously, or VEE first, followed by V<sub>CC</sub>. Please note the derating characteristics above Ta=25°C: 10mW/°C (Mounted on a 70mmx70mmx1.6mm sized board).

## Operating conditions

(Normal function at Ta=25°C)

| Doromotor                | Cymphol         |      | Linit |      |      |  |
|--------------------------|-----------------|------|-------|------|------|--|
| Parameter                | Symbol          | Min. | Тур.  | Max. | Unit |  |
| Operating Supply Voltage | V <sub>CC</sub> | 5    | 7     | 7.3  | V    |  |
| Operating Supply Voltage | V <sub>EE</sub> | -7.3 | -7    | -5   | V    |  |

## **●**Electrical Characteristics

Ta=25°C, VCC=7V, VEE=-7V, f=1kHz, VIN=1Vrms, RL=10k $\Omega$ , Rg=600 $\Omega$ 

Input Gain=0dB, Master Volume=0dB, Bass bnd Treble=0dB, Unless otherwise noted.

|              | Paramete                           | r                                  | Symbol |      | Limits |      | Unit  | Conditions  |    |   |
|--------------|------------------------------------|------------------------------------|--------|------|--------|------|-------|---|----|---|
|              | i aramete                          |                                    | Symbol | Min. | Тур.   | Max. | Offic | Conditions  |    |   |
|              | Cinavit Comment                    | VCC                                | 10     | _    | 10     | 20   | A     | No simpl  |    |   |
|              | Circuit Current                    | VEE                                | IQ     | -20  | -10    | _    | mA    | No signal   |    |   |
|              | Output Voltage Gai                 | n 1                                | Gv1    | -2   | 0      | 2    | dB    | Measure : Pin31, 29, 27, 25, 23, 21   |    |   |
|              | Output Voltage Gai                 | n 2                                | Gv2    | -2   | 0      | 2    | dB    | Measure : Pin19   |    |   |
|              | Total Harmonic Disratio 1          | tortion                            | THD1   | _    | 0.004  | 0.05 | %     | Measure : Pin31, 29, 27, 25, 23, 21<br>BW=400∼30kHz                         |    |   |
|              | Total Harmonic Disratio 2          | tortion                            | THD2   | _    | 0.004  | 0.05 | %     | Measure : Pin19<br>BW=400~30kHz   |    |   |
|              | Maximum Output V                   | oltage 1                           | Vomax1 | 3.4  | 4.2    | l    | Vrms  | Measure : Pin31, 29, 27, 25, 23, 21<br>THD=1%                               |    |   |
| Total Output | Maximum Output V                   | oltage 2                           | Vomax2 | 3.4  | 4.2    | _    | Vrms  | Measure : Pin19<br>THD=1%   |    |   |
| <u>a</u> 0   |                                    |                                    |        | _    | 2.0    | 12   | μVrms | Measure : Pin 31,29<br>Rg=0Ω, Tone: ON, BW=IHF-A                            |    |   |
| Ϊ́ο          | Output Noise Voltage               | ge 1                               | Vno1   | _    | 1.5    | 8.0  | μVrms | Measure : Pin 31,29<br>Rg=0Ω, Tone: By-pass, BW=IHF-A                       |    |   |
|              | Output Noise Voltage               | ge 2                               | Vno2   | _    | 1.5    | 8.0  | μVrms | Measure : Pin 27,25,23,21<br>Rg=0Ω, BW=IHF-A                                |    |   |
|              | Output Noise Voltage               | ge 3                               | Vno3   | _    | 1.0    | 5.0  | μVrms | Measure : Pin 19<br>Rg=0Ω, BW=IHF-A   |    |   |
|              | Crosstalk between<br>Rch→Lch       | Channels                           | CTCRL  | _    | -95    | -80  | dB    | Measure : Pin29(OUTFL) Rg=0Ω, BW=IHF-A Reference : Pin31(OUTFR)=1Vrms       |    |   |
|              | Crosstalk between Channels Lch→Rch |                                    | CTCLR  | _    | -95    | -80  | dB    | Measure : Pin31(OUTFR)<br>Rg=0Ω, BW=IHF-A<br>Reference : Pin29(OUTFL)=1Vrms |    |   |
|              | Crosstalk between DVD              | Crosstalk between Selectors<br>DVD |        |      |        | _    | -95   | -80   | dB | Measure : Pin 31,29,27,25,<br>23,21,19<br>Rg=0Ω, BW=IHF-A |
|              | Crosstalk between D/A              | Selectors                          | CTSB   | _    | -95    | -80  | dB    | Measure : Pin 31,29,27,25,<br>23,21,19<br>Rg=0Ω, BW=IHF-A                   |    |   |

|               |  |                         |      | Limits |  |  |  |
|---------------|--|-------------------------|------|--------|--|--|--|
|               | Parameter                              | Symbol                  | Min. | Тур.   | Max.   | Unit   | Conditions   |
|               | Volume Control Range                   | GVR                     | -98  | -95    | -92  | dB   | Measure : Pin31, 29, 27, 25, 23, 21<br>Vin=3Vrms                 |
| put           | Volume Setting Error 1                 | VE1                     | -2   | 0      | 2  | dB   | 0 to -53dB<br>Measure : Pin31, 29, 27, 25, 23, 21<br>Vin=3Vrms   |
| Volume Output | Volume Setting Error 2                 | VE2                     | -3   | 0      | 3  | dB   | -54 to -95dB<br>Measure : Pin31, 29, 27, 25, 23, 21<br>Vin=3Vrms |
| %             | Channel Balance                        | VCB                     | -0.5 | 0      | 0.5  | dB   | Measure : Pin31, 29, 27, 25, 23, 21<br>Vin=3Vrms, Volume=0dB     |
|               | Maximum Attenuation                    | Vmin                    | _    | -115   | -105   | dB   | BW=IHF-A<br>Measure : Pin31, 29, 27, 25, 23, 21<br>Vin=3Vrms     |
|               | Input Gain Control Range (BD3813KS)    | GIG                     | 10   | 12     | 14   | dB   | Measure : Pin31, 29, 27, 25, 23, 21, 19<br>Vin=0.4Vrms           |
| nput Gain     | Input Gain Control Range<br>(BD3815KS) |                         | 16   | 18     | 20   | dB   | Measure : Pin31, 29, 27, 25, 23, 21, 19<br>Vin=0.4Vrms           |
| Input         | Input Gain Setting Error (BD3813KS)    |                         |      | 2      | dB   | Measure : Pin31, 29, 27, 25, 23, 21, 19<br>Vin=0.4Vrms |  |
|               | Input Gain Setting Error (BD3815KS)    |                         | -2   | 0      | 2  | dB   | Measure : Pin31, 29, 27, 25, 23, 21, 19<br>Vin=0.4Vrms           |
|               | Treble Maximum Boost Gain              | GTB                     | 12   | 14     | 16   | dB   | Measure : Pin 31, 29<br>f=15kHz, VIN=0.4Vrms                     |
| Treble        | Treble Maximum Cut Gain                | GTC                     | -16  | -14    | -12  | dB   | Measure : Pin 31, 29<br>f=15kHz, VIN=0.4Vrms                     |
| Tre           | Treble Step Resolution                 | TR                      | _    | 2      | _  | dB   | Measure : Pin 31, 29<br>f=15kHz, VIN=0.4Vrms                     |
|               | Treble Gain Setting Error              | TE                      | -2   | 0      | 2  | dB   | Measure : Pin 31, 29<br>f=15kHz, VIN=0.4Vrms                     |
|               | Bass Maximum Boost Gain                | GBB                     | 12   | 14     | 16   | dB   | Measure : Pin 31, 29<br>f=100Hz, Vi=0.4Vrms                      |
| Bass          | Bass Maximum Cut Gain                  | GBC                     | -16  | -14    | -12  | dB   | Measure : Pin 31, 29<br>f=100Hz, VIN=0.4Vrms                     |
| Ba            | Bass Step Resolution                   | BR                      | _    | 2      | _  | dB   | Measure : Pin 31, 29<br>f=100Hz, VIN=0.4Vrms                     |
|               | Bass Gain Setting Error                | ting Error BE -2 0 2 dB |      | dB     | Measure : Pin 31, 29<br>f=100Hz, VIN=0.4Vrms |  |  |
| Port          | Port H Output                          | PH                      | 4.5  | 4.9    |  | V  | Measure : Pin11,12<br>VDD=5V, RL=47kΩ                            |

<sup>\*</sup> This product is not designed to be resistant against radiation

## **Timing Chart**

- 1) Signal Timing Conditions
  - Data is read on the rising edge of the clock.
  - · Latch is read on the falling edge of the clock.
  - The latch signal must terminate with the Low state.
  - \*To avoid malfunction, the clock and data signals must terminate with the Low state.

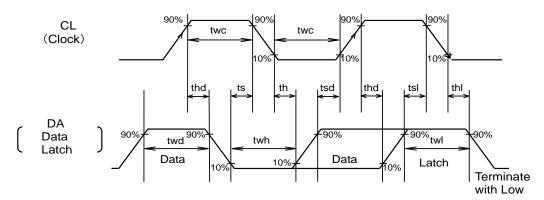


Fig.1

| Parameter                         | Symbol |      | Limits |      | Unit  |
|-----------------------------------|--------|------|--------|------|-------|
| Farameter                         | Symbol | Min. | Тур.   | Max. | Offic |
| Minimum Clock Width               | twc    | 2.0  |        |      | μs    |
| Minimum Data Width                | twd    | 2.0  | _      | _    | μs    |
| Minimum Latch Width               | twl    | 2.0  | _      | _    | μs    |
| Low Hold Width                    | twh    | 2.0  | _      | _    | μs    |
| Data Set-up Time (Data→Clock)     | tsd    | 1.0  | _      | _    | μs    |
| Data Hold Time ( Clock → Data )   | thd    | 1.0  | _      | _    | μs    |
| Latch Set-up Time ( Clock →Latch) | tsl    | 1.0  | _      | _    | μs    |
| Latch Hold Time ( Data → Latch )  | thl    | 1.0  | _      | _    | μs    |
| Latch Low Set-up Time             | ts     | 1.0  | _      | _    | μs    |
| Latch Low Hold Time               | th     | 1.0  | _      | _    | μs    |

2) Control Signal Voltage Conditions

| Parameter         | Condition                 |      |      | Unit       |       |
|-------------------|---------------------------|------|------|------------|-------|
| Parameter         | Condition                 | Min. | Тур. | Max.(≤Vcc) | Offic |
| "H" Input Voltage | Vcc=5~7.3V                | 2.2  | _    | 5.5        | V     |
| "L" Input Voltage | V <sub>EE</sub> =-5~-7.3V | 0    | _    | 1.0        | V     |

3) Control Data Format - Basic Configuration

| <u>←</u> | Input | directi | on  |     |     |     |     |     |    |    |    |    |    |    |      |        |      |
|----------|-------|---------|-----|-----|-----|-----|-----|-----|----|----|----|----|----|----|------|--------|------|
|          | MSB   |         |     |     |     |     |     |     |    |    |    |    |    |    |      |        | LSB  |
|          | D16   | D15     | D14 | D13 | D12 | D11 | D10 | D9  | D8 | D7 | D6 | D5 | D4 | D3 | D2   | D1     | D0   |
| Data     |       |         |     |     |     |     | Da  | ata |    |    |    |    |    |    | Sele | ct Add | ress |

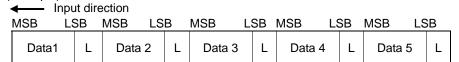
| • Cont    |             | a Form<br>directio |             |         |        |     |             |                    |          |                      |                      |                   |                   |    | Select Address |    | ress |
|-----------|-------------|--------------------|-------------|---------|--------|-----|-------------|--------------------|----------|----------------------|----------------------|-------------------|-------------------|----|----------------|----|------|
|           | D16         | D15                | D14         | D13     | D12    | D11 | D10         | D9                 | D8       | D7                   | D6                   | D5                | D4                | D3 | D2             | D1 | D0   |
| Data<br>1 | Input<br>FR | Gain<br>/FL        | Input<br>SR |         | Input  |     | Input<br>S' |                    |          | Gain<br>B            | SW1<br>0:A<br>1:B    | SW2<br>0:A<br>1:B | SW3<br>0:A<br>1:B | 0  | 0              | 0  | 0    |
|           | D16         | D15                | D14         | D13     | D12    | D11 | D10         | D9                 | D8       | D7                   | D6                   | D5                | D4                | D3 | D2             | D1 | D0   |
| Data<br>2 |             | Tre                | ble         |         |        | Ва  | ISS         |                    | TON<br>E | Port A<br>0:L<br>1:H | Port B<br>0:L<br>1:H | *                 | *                 | 1  | 0              | 0  | 0    |
|           | D16         | D15                | D14         | D13     | D12    | D11 | D10         | D9                 | D8       | D7                   | D6                   | D5                | D4                | D3 | D2             | D1 | D0   |
| Data<br>3 |             |                    | Master      | Volum   | e FRch | l   |             | Master Volume FLch |          |                      |                      |                   |                   |    | 0              | 0  | 1    |
|           | D16         | D15                | D14         | D13     | D12    | D11 | D10         | D9                 | D8       | D7                   | D6                   | D5                | D4                | D3 | D2             | D1 | D0   |
| Data<br>4 |             |                    | Master      | Volum   | e SRch | ı   |             | Master Volume SLch |          |                      |                      |                   | 0                 | 1  | 0              |    |      |
|           | D16         | D15                | D14         | D13     | D12    | D11 | D10         | D9                 | D8       | D7                   | D6                   | D5                | D4                | D3 | D2             | D1 | D0   |
| Data<br>5 |             |                    | Maste       | r Volum | ne Cch |     |             |                    |          | Master               | Volume               | e SWcł            | 1                 |    | 0              | 1  | 1    |

Changing the Select Address settings allows selection of four different control formats.

For Select Address, values except those shown above must not be specified.

The address data must be initialized after every power ON.

#### (Example)



<sup>&</sup>quot;L" means latch.

After power ON, only the desired data can be set for the second and subsequent times,.

(Example) When changing the bass,

Input direction



<sup>\*</sup> Indicates 0 or 1.

## Application Circuit

## 1) BD3813KS / BD3815KS

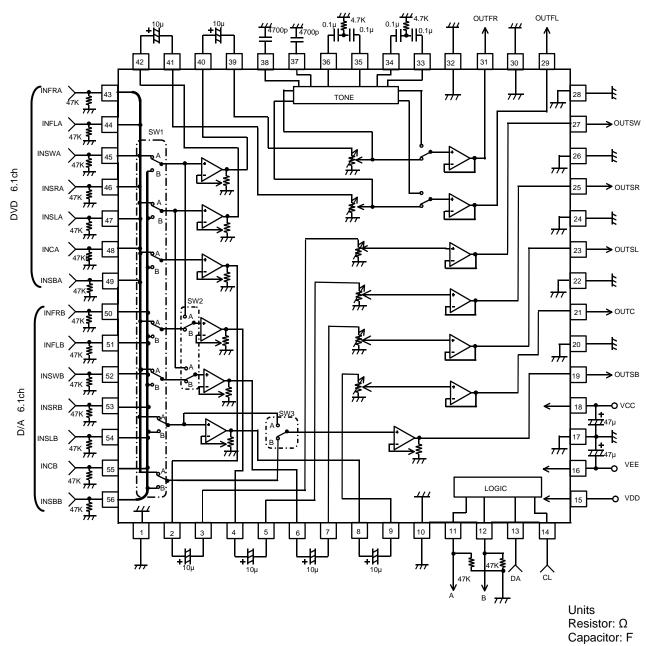


Fig. 2

## **●**Pin Description

| in Desc    | ription     |  |            |             |                           |
|------------|-------------|--|------------|-------------|---------------------------|
| Pin<br>No. | Pin<br>Name | Description                              | Pin<br>No. | Pin<br>Name | Description               |
| 1          | GND1        | Ground pin                               | 29         | OUTFL       | Lch Output pin            |
| 2          | GOUTSW      | Input Gain output for subwoofer pin      | 30         | GND9        | Ground pin                |
| 3          | VINSW       | Volume Input for subwoofer pin           | 31         | OUTFR       | Rch Output pin            |
| 4          | GOUTSR      | Input Gain output for surround Rch pin   | 32         | GND10       | Ground pin                |
| 5          | VINSR       | Volume Input for surround Rch pin        | 33         | BNF2L       | Lch Bass filter pin 2     |
| 6          | GOUTSL      | Input Gain output for surround Lch pin   | 34         | BNF1L       | Lch Bass filter pin 1     |
| 7          | VINSL       | Volume Input for surround Lch pin        | 35         | BNF2R       | Rch Bass filter pin 2     |
| 8          | GOUTC       | Input Gain output for center speaker pin | 36         | BNF1R       | Lch Bass filter pin 1     |
| 9          | VINC        | Volume Input for center speaker pin      | 37         | TNFL        | Lch Treble filter pin     |
| 10         | GND2        | Ground pin                               | 38         | TNFR        | Rch Treble filter pin     |
| 11         | PORTA       | Output for port pin                      | 39         | VINFR       | Rch Volume input pin      |
| 12         | PORTB       | Output for port pin                      | 40         | GOUTFR      | Rch Input gain output pin |
| 13         | DA          | Serial data, latch input pin             | 41         | VINFL       | Lch Volume input pin      |
| 14         | CL          | Serial clock input pin                   | 42         | GOUTFL      | Lch Input gain output pin |
| 15         | VDD         | Power supply for port pin                | 43         | INFRA       | Input for Rch DVD pin     |
| 16         | VEE         | (-) Power supply pin                     | 44         | INFLA       | Input for Lch DVD pin     |
| 17         | GND3        | Ground pin                               | 45         | INSWA       | Input for SWch DVD pin    |
| 18         | VCC         | (+) Power supply pin                     | 46         | INSRA       | Input for SRch DVD pin    |
| 19         | OUTSB       | Surround back output pin                 | 47         | INSLA       | Input for SLch DVD pin    |
| 20         | GND4        | Ground pin                               | 48         | INCA        | Input for Cch DVD pin     |
| 21         | OUTC        | Center speaker output pin                | 49         | INSBA       | Input for SBch DVD pin    |
| 22         | GND5        | Ground pin                               | 50         | INFRB       | Input for Rch DSP pin     |
| 23         | OUTSL       | Output for surround Lch pin              | 51         | INFLB       | Input for Lch DSP pin     |
| 24         | GND6        | Ground pin                               | 52         | INSWB       | Input for SWch DSP pin    |
| 25         | OUTSR       | Output for surround Rch pin              | 53         | INSRB       | Input for SRch DSP pin    |
| 26         | GND7        | Ground pin                               | 54         | INSLB       | Input for SLch DSP pin    |
| 27         | OUTSW       | Subwoofer output pin                     | 55         | INCB        | Input for Cch DSP pin     |
| 28         | GND8        | Ground pin                               | 56         | INSBB       | Input for SBch DSP pin    |

## **Equivalent Circuits**

| <u>iquivale</u>              | nt Circuits   |             |                    |   |
|------------------------------|---|-------------|--------------------|---|
| Pin No                       | p. Pin Name   | Pin Voltage | Equivalent Circuit | Description   |
| 2<br>4<br>6<br>8<br>40<br>42 | GOUTSW<br>GOUTSR<br>GOUTSL<br>GOUTC<br>GOUTFR<br>GOUTFL | 0           | VCC<br>VEE         | Sound signal output pins from input gain                                      |
| 3<br>5<br>7<br>9<br>39<br>41 | VINSW<br>VINSR<br>VINSL<br>VINC<br>VINFR<br>VINFL       | 0           | VCC<br>VEE         | Sound signal input pins to master volume, Input Impedance: $20k\Omega$ (typ.) |
| 11<br>12                     | PORTA<br>PORTB  | _           | VCC<br>VEE         | Open drain output pins  |
| 13                           | DA  | _           | VCC<br>VEE         | Serial data input pin   |
| 14                           | CL  | _           | VCC VEE            | Serial clock input pin  |

| Pin No.  | Pin Name  | Pin Voltage | Equivalent Circuit   | Description  |
|--|---|-------------|--|--|
| 19<br>21<br>23<br>25<br>27<br>29<br>31                                     | OUTSB<br>OUTC<br>OUTSL<br>OUTSR<br>OUTSW<br>OUTFL<br>OUTFR                              | 0           | VEE OF   | Sound signal output pins   |
| 33<br>35   | BNF2L<br>BNF2R  | 0           | VCC<br>VEE   | Bass frequency characteristic/gain setting pins                                    |
| 34<br>36   | BNF1L<br>BNF1R  | 0           | VCC OVER THE PROPERTY OF THE P | Bass frequency characteristic/gain setting pins                                    |
| 37<br>38   | TNFL<br>TNFR  | 0           | VCC OVER OVER OVER OVER OVER OVER OVER OVER  | Treble frequency characteristic/gain setting pins                                  |
| 43<br>44<br>45<br>46<br>47<br>48<br>49<br>50<br>51<br>52<br>53<br>54<br>55 | INFRA INFLA INSWA INSRA INSLA INCA INSBA INFRB INFLB INSWB INSRB INSLB INSLB INCB INSBB | 0           | VCC VEE VEE  | Sound signal input pins;<br>Input impedances determined by external<br>resistances |

**OSwitch Description** 

| <u> </u> | F                    |                |                |  |
|----------|----------------------|----------------|----------------|--|
| Output   | Input<br>(SW1=A)     |                |                |  |
| Output   | SW2,3=B<br>(Default) | SW2=A<br>SW3=B | SW2=B<br>SW3=A |  |
| FR       | FR                   | FR             | FR             |  |
| FL       | FL                   | FL             | FL             |  |
| sw       | SW                   | SW             | SW             |  |
| SR       | SR                   | FR             | SR             |  |
| SL       | SL                   | FL             | SL             |  |
| С        | С                    | С              | С              |  |
| SB       | SB                   | SB             | С              |  |

SW1 A: Select input line A

B: Select input line B

SW2 A: Output the signals of the FR and FL inputs onto the Surround Outputs (SR, SL) Used when the source is stereo.

B: Output the signals of the SR and SL inputs onto the Surround Outputs (SR, SL) Used when the source is 5.1ch or 6.1ch.

SW3 A: Output the C input signal onto SB output

Used when the source is 5.1ch with the 6.1ch speaker system installed.

B: Output the SB input signal onto the SB output Used when the source is 6.1ch with the 6.1ch speaker system installed.

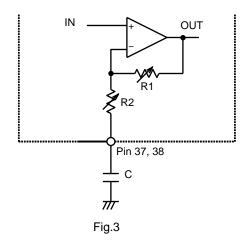
## Setting constants for tone control filters

## 1) Treble filter

 $fc=1/2\pi(R2)C$  (Hz)

G=20log(R1+R2+Zc)/(R2+Zc) (dB)

 $Zc=1/j\omega C$  ( $\Omega$ )

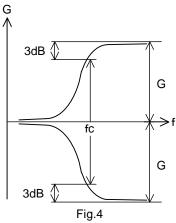


Standard Values of R1, R2 (Reference)

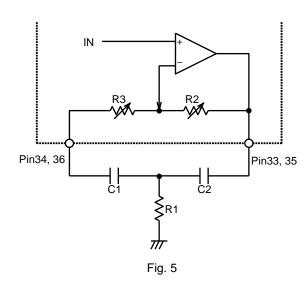
the standard values to some degree.

| Treble Boost Amount Cut Amount | Resistance (kΩ)<br>*Typ. |      |
|--------------------------------|--------------------------|------|
|                                | R1                       | R2   |
| 0dB                            | 0                        | 20   |
| ±2dB                           | 4.1                      | 15.9 |
| ±4dB                           | 7.3                      | 12.7 |
| ±6dB                           | 10.3                     | 9.7  |
| ±8dB                           | 12.3                     | 7.7  |
| ±10dB                          | 14.0                     | 6.0  |
| ±12dB                          | 15.4                     | 4.6  |
| ±14dB                          | 16.5                     | 3.5  |





## 2) Bass Filter



$$f_0 = \frac{1}{2\pi \sqrt{R1(R2 + R3)C1C2}}$$
 (Hz)

$$Q = \frac{1}{C1+C2} \sqrt{\frac{C1C2R2}{R1}}$$

when C1=C2

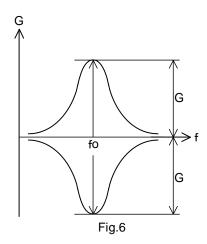
$$G = 20log - \frac{\frac{R2+R3}{R1} + 2}{\frac{R3}{R1} + 2}$$
 (dB)

Standard values of R2, R3 (Reference)

 $(R1=4.7k\Omega, C1=C2=0.1\mu F)$ 

| (R 1=4.7 KΩ, C 1=C2=0.1μΓ | ,                        |      |
|---------------------------|--------------------------|------|
| Boost Amount              | Resistance (kΩ)<br>Typ.* |      |
| Cut Amount                | R2                       | R3   |
| 0dB                       | 0                        | 41.0 |
| ±2dB                      | 10.8                     | 30.2 |
| ±4dB                      | 19.3                     | 21.7 |
| ±6dB                      | 26.0                     | 15.0 |
| ±8dB                      | 31.2                     | 9.8  |
| ±10dB                     | 35.4                     | 5.6  |
| ±12dB                     | 38.4                     | 2.6  |
| ±14dB                     | 41.0                     | 0    |

<sup>\*</sup>The actual boost/cut amounts may deviate from the standard values in some degree.



\* Bass Filter Feature

To set the f0 and Q values of the Bass characteristics, refer to the external components of the Bass Filter shown in the upper-left figure.

#### ● Reference Data

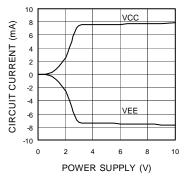


Fig.7 Circuit Current - Power Supply

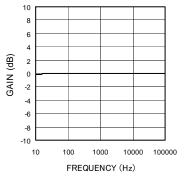


Fig.8 Voltage Gain - Frequency

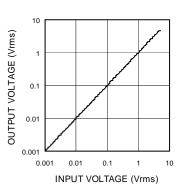


Fig.9 Output Voltage - Input Voltage

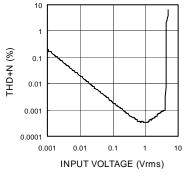


Fig.10 THD+N -Input Voltage

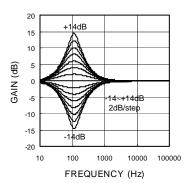


Fig.11 Bass Gain - Frequency

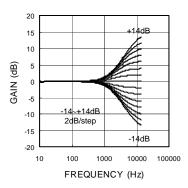


Fig.12 Treble Gain - Frequency

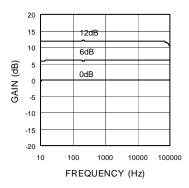


Fig.13 Input Gain - Frequency (BD3813KS)

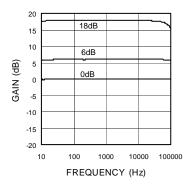


Fig.14 Input Gain - Frequency (BD3815KS)

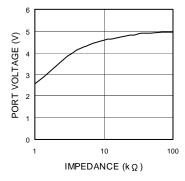


Fig.15 Port H Voltage – Load Resistance

#### Notes for use

- (1) The numbers and data shown above are representative design values and are not guaranteed.
- (2) Although the sample application circuits are guaranteed, further verification of the electrical characteristics are recommended. When modifying external components before use, ensure that sufficient margins are in place by taking into account variations in the external components themselves as well as the LSI regarding both static and transient characteristics.
- (3) Absolute maximum ratings

Operating or testing the IC over the maximum ratings may damage the part itself as well as peripheral components. Therefore, please ensure that the specifications are never exceeded. In addition, implementation of fuses or other physical safety measures is recommended.

- (4) V<sub>EE</sub> potential
  - Ensure that the  $V_{EE}$  pin voltage is at the lowest potential and that no other pin is at a lower voltage, including transient phenomena.
- (5) Thermal design

Implement thermal designs that take into account the power dissipation under actual operating conditions.

- (6) Shorts between pins and erroneous installation
  - Incorrect mounting may damage the IC. In addition, the presence of foreign particles between the pins, a pin and the power supply, or a pin and GND may result in destruction.
- (7) Operation in a strong magnetic field

Operation in a strong magnetic field may cause malfunction.

(8) Serial control

The wiring pattern of the CL and DA terminals should be routed so as not to cause interference with the analog signal related lines.

- (9) Power ON/OFF
  - (a) During power ON/OFF a shock sound will be generated. Therefore, use the MUTE function.
  - (b) When turning ON the power supplies, V<sub>EE</sub> and V<sub>CC</sub> should either be powered on simultaneously, or V<sub>EE</sub> first, followed by V<sub>CC</sub>, since if V<sub>CC</sub> is started up first, an excessive current may pass V<sub>CC</sub> through to V<sub>EE</sub>.
- (10) Function switching

For functions except the Master Volume, Treble and Bass controls, use of the MUTE function is recommended.

(11) Port power supply

The port power supply should be turned ON following  $V_{CC}$  and  $V_{EE}$ . If the port is not used, the port power supply must be connected to  $V_{EE}$ .

## ●Thermal Derating Curve

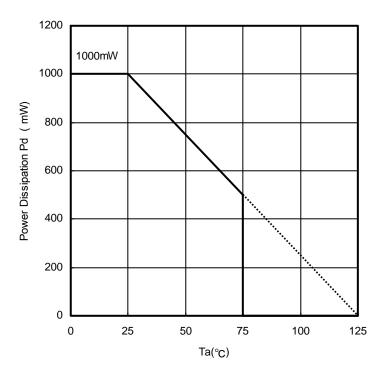
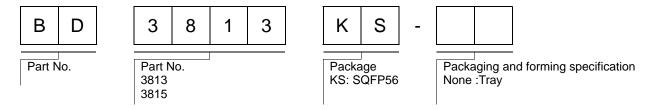


Fig. 16

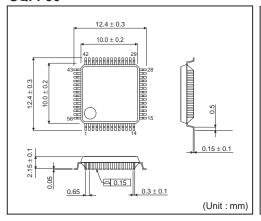
BD3813KS, BD3815KS

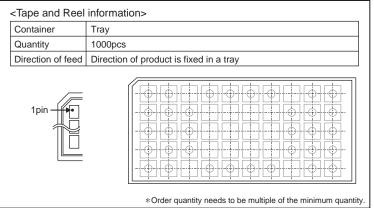
Board size: 70mm x 70mm x 1.6mm Raw material: FR4 glass epoxy board (copper area less than 3%)

## Ordering part number



## SQFP56





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|---------|----------|------------|----------|
| CLASSⅢ  | CLASSIII | CLASS II b | CLASSIII |
| CLASSIV |          | CLASSⅢ     |          |

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  - [h] Use of the Products in places subject to dew condensation
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
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- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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