

# 1.5A Variable/Fixed Output LDO Regulators



## BAxxJC5 Series(Fixed) BA00JC5WT(Variable)

#### General Description

The BAxxJC5 are low-saturation regulators with an output current of 1.5A and a voltage accuracy of  $\pm$ 1%. A broad output voltage range is offered, from 1.5V to 12V, and built-in overcurrent protection and thermal shutdown (TSD) circuits prevent damage due to short-circuiting and overloading, respectively.

#### Features

- Output voltage accuracy: ±1%
- Broad output voltage range available: 1.5V -12V (BAxxJC5 series)
- Low saturation-voltage type with PNP output
- Built-in overcurrent protection circuit
- Built-in thermal shutdown circuit
- Integrated shutdown switch (BA00JC5WT)

#### •Key Specifications

- Input Power Supply Voltage: 16.0V(Max.)
- Output voltage type: BAxxJC5 Series Fixed
- BA00JC5WT Variable
- Output current: 1.5A(Max.)
  Shutdown current: (BA00JC5WT) 0μA(Typ.)
- Operating temperature range: -40°C to +105°C

#### Applications

All electronic devices that use microcontrollers and logic circuits



W (Typ.) x D (Typ.) x H (Max.) 10.00mm x 30.50mm x 4.60mm 10.00mm x 30.50mm x 4.60mm



#### Lineup Matrix

Dent Neurale en	Output Voltage (V)												
Part Number	1.5	1.8	2.5	3.0	3.3	5.0	6.0	6.3	8.0	9.0	12.0	Variable	Package
BAxxJC5T	0	0	0	0	0	0	0	0	0	0	0	-	TO220FP-3
BAxxJC5WT	-	-	-	-	-	-	-	-	-	-	-	0	TO220FP-5

#### Ordering Information

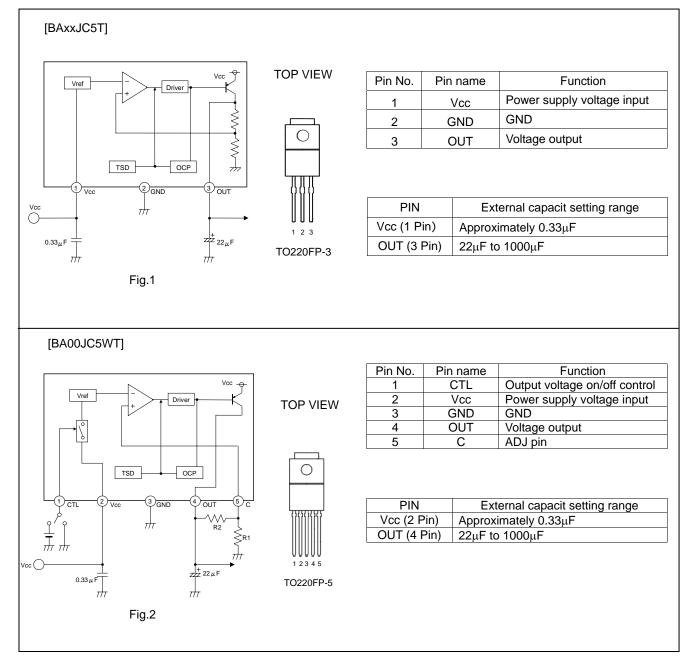
В	A	Х	х	J	С	5	Х	х	-	
Part number	Outpu 00:Va Other		9	Currer capac JC5:1	ity	Shutde switch W : Inc None:v	lude	тс	e O220FP-3 O220FP-5	Packaging and forming specification None:Tube Contener

OProduct structure : Silicon monolithic integrated circuit OThis product is not designed protection against radioactive rays.

## ●Lineup

Maximum output current (Max.)	Shutdown Switch	Pac	kage	Output voltage(Typ.)	Orderable Part Number
				1.5 V	BA15JC5T
				1.8 V	BA18JC5T
				2.5 V	BA25JC5T
		TO220FP-3		3.0 V	BA30JC5T
				3.3 V	BA33JC5T
1.5A	No Switch		Tube of 500	5.0 V	BA50JC5T
I.5A			Tube of 500	6.0 V	BA60JC5T
				6.3 V	BA63JC5T
				8.0 V	BA80JC5T
				9.0 V	BA90JC5T
				12.0 V	BAJ2JC5T
	With Switch	TO220FP-5		Variable	BA00JC5WT

## Block Diagrams / Standard Example Application Circuits / Pin Configurations / Pin Descriptions



### ●Absolute Maximum Ratings (Ta = 25°C)

Parameter Power supply voltage		Symbol	Ratings	Unit	
		Vcc	18 <sup>*1</sup>	V	
Dower dissinction	TO220FP-3	<b>D</b> 1	2000 <sup>*2</sup>	mW	
Power dissipation	TO220FP-5	- Pd	2000 <sup>*2</sup>	IIIVV	
Operating temperatu	ire range	Topr	−40 to +105	°C	
Ambient storage temperature		Tstg	−55 to +150	°C	
Maximum junction te	mperature	Tjmax	150	°C	

Must not exceed Pd \*1

Derated at 16mW/°C at Ta>25°C \*2

#### Recommended Operating Ratings

Parameter	Sympol	Ra	Unit		
Parameter	Symbol	Min.	Max.	Unit	
Input power supply voltage	V <sub>CC</sub> *3	3.0	16.0	V	
Input power supply voltage	Vcc*4	Vo + 1.0	16.0	V	
Output current	lo	-	1.5	Α	
Variable output voltage setting value	Vo	1.5	12	V	

When output voltage is 1.5 V, 1.8 V, or 2.5 V. When output voltage is 3.0 V or higher. \*3 \*4

#### Electrical Characteristics

BAxxJC5T

Unless otherwise specified, Ta=25°C, Vcc=3.3V(Vo=1.5V,1.8V,2.5V), Vcc=5.0V(Vo=3.0V,3.3V), Vcc=Vo+3.0V(Vo≥5.0V)

Parameter	Symbol		Limit		Unit	Conditions	
Farameter	Symbol	Min	Тур	Max	Unit		
Bias Current	lb	-	0.5	0.9	mA	lo=0mA	
Output Voltage	Vo	Vo(T) × 0.99	Vo(T)	Vo(T) × 1.01	V	lo=200mA	
Dropout Voltage *5	ΔVd	—	0.3	0.5	V	Io=500mA, Vcc=Vo×0.95V	
Peak Output Current	lo	1.5	_	—	Α		
		44	55	_	dB	f=120Hz, ein <sup>*8</sup> =-20dBV Io=100mA, Vo≦6.3V	
Dinnle Dejection	R.R.	43	53	—		f=120Hz, ein <sup>*8</sup> =-20dBV Io=100mA, Vo=8.0V	
Ripple Rejection		42	52	—		f=120Hz, ein <sup>*8</sup> =-20dBV Io=100mA, Vo=9.0V	
		40	50	_		f=120Hz, ein <sup>*8</sup> =-20dBV Io=100mA, Vo=12.0V	
Line Regulation *6	Reg.I	—	5	60	mV	Vcc=Vo+1.0V→16V, Io=200mA	
Load Regulation	Reg.L	—	5	60	mV	lo=5mA→1.5A	
Temperature Coefficient of Output Voltage *7	Тсvо	_	±0.02	_	%/°C	lo=5mA,Tj=0 to 125°C	
Output Short Current	los	—	0.7	—	Α	Vcc=16V(Vo≦3.3V)	
	105	_	0.8	_	A	Vcc=16V(Vo≧5.0V)	

Vo(T): Output Voltage

\*5 Vo≧3.3V

\*6 Vcc=3.0V->16V (Vo=1.5V, 1.8V, 2.5V)

\*7 Not 100% tested

\*8 ein=Input Voltage Ripple

## Electrical Characteristics - continued

## BA00JC5WT

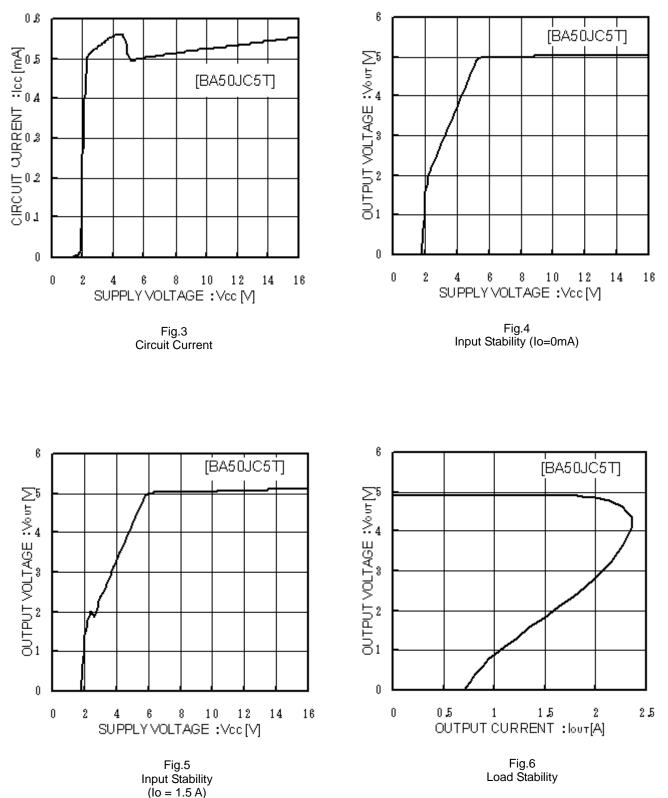
Unless otherwise specified, Ta =  $25^{\circ}$ C, Vcc = 3.3 V, VcTL = 3 V, Vo=2.5V

Parameter	Symbol L		Limit	imit		Conditions	
Falameter	Symbol	Min.	Тур. Ма		Unit	Conditions	
Shut Down Current	Isd	-	0	10	μA	Vctl=0V, Io=0mA (OFF MODE)	
Bias Current	lb	-	0.5	0.9	mA	Io=0mA	
C pin Voltage	Vc	1.237	1.250	1.263	V	Io=50mA	
Dropout Voltage1	ΔVd	-	0.3	0.5	V	Io=500mA, Vcc=2.5V	
Peak Output	lo	1.5	-	-	Α		
Ripple Rejection	R.R.	45	55	_	dB	f=120Hz, ein <sup>*9</sup> =-20dBV lo=100mA	
Line Regulation	Reg.I	-	5	60	mV	Vcc=4.5V→16V, Io=200mA	
Load Regulation	Reg.L	-	5	60	mV	Io=5mA→1.5A	
Temperature Coefficient of Output Voltage *10	Тсvо		±0.02	_	%/°C	lo=5mA,Tj=0~125°C	
Output Short Current	los	Ι	0.6	_	А	Vcc=16V	
CTL ON Mode Voltage	Vth1	2.0	-	_	V	ACTIVE MODE, Io=0mA	
CTL OFF Mode Voltage	Vth2	-	-	0.8	V	OFF MODE, Io=0mA	
CTL Input Current	lin	40	80	130	μA	Io=0mA	

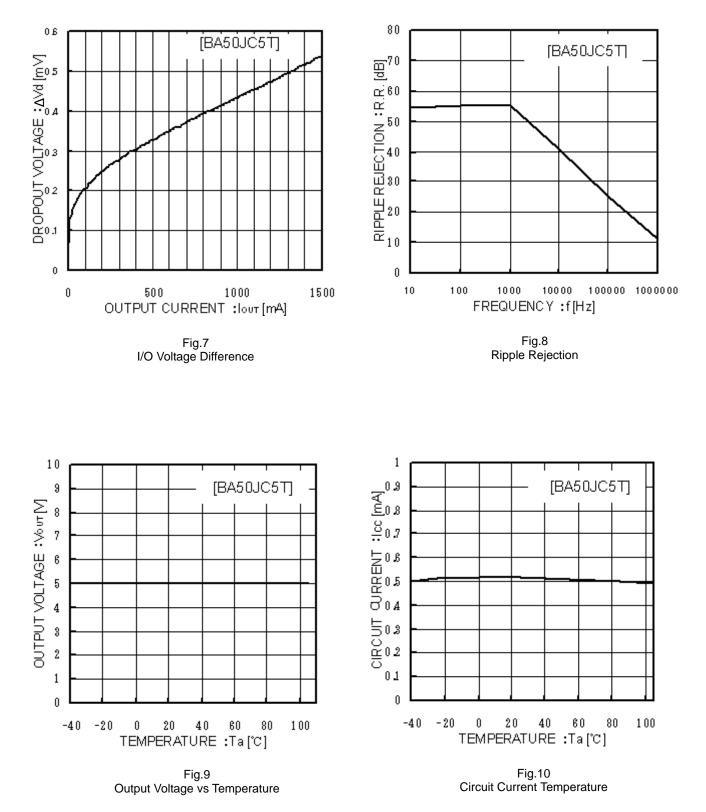
\*9 ein=Input Voltage Ripple \*10 Not 100% tested

### •Typical Performance Curves

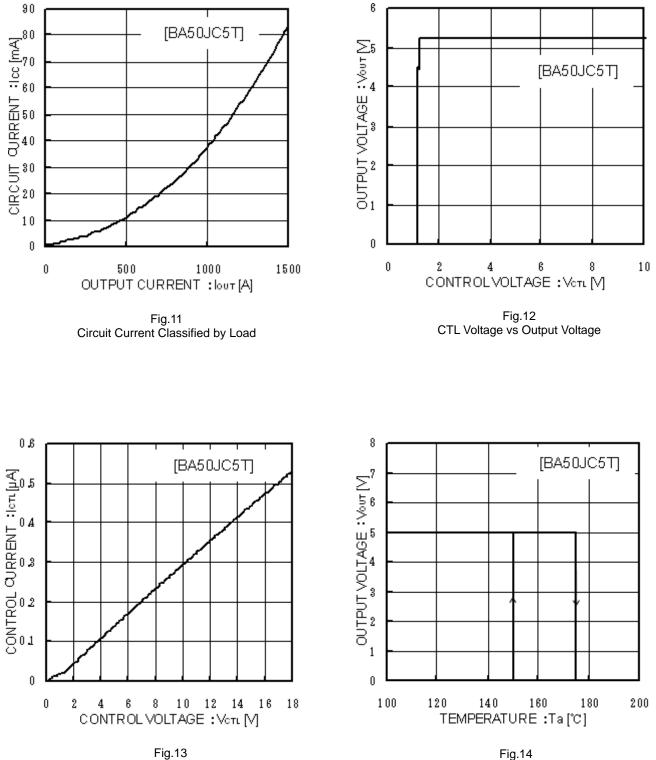
(Unless otherwise specified, Ta = 25°C; Vcc = 8V; VcTL = 3V; Io = 0mA)

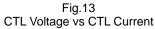


## Typical Performance Curves - continued



## Typical Performance Curves - continued





Thermal Shutdown Circuit (Io = 5 mA)

### I/O equivalence circuit

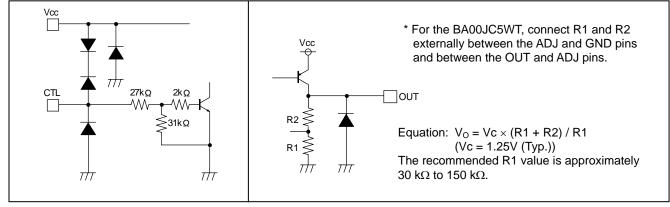
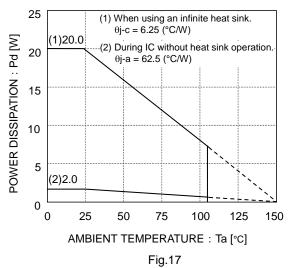




Fig.16

## Power Dissipation

• TO220FP-3/TO220FP-5



The characteristics of the IC are greatly influenced by the operating temperature. If the temperature exceeds the maximum junction temperature  $T_{jmax}$ , deterioration or damage may occur. Implement proper thermal designs to ensure that power dissipation is within the permissible range in order to prevent instantaneous damage resulting from heat and maintain the reliability of the IC for long-term operation.

The following method is used to calculate the power consumption Pc (W):

 $Pc = (Vcc - Vo) \times Io + Vcc \times Icca$ Power dissipation  $Pd \ge Pc$ 

The load current lo is calculated:

$$lo \le \frac{Pd - Vcc \times lcca}{Vcc - Vo}$$

Calculation Example:

Vcc = 6.0V and Vo = 5.0V at Ta =  $85^{\circ}$ C lo ≤  $\frac{1.040 - 6.0 \times lcca}{6.0 - 5.0}$  ( $\theta$ ja =  $62.5^{\circ}$ C/W → -16.0mW/°C  $25^{\circ}$ C = 2000mW →  $85^{\circ}$ C = 1040mW

Vcc : Input voltage Vo : Output current Io : Load current Icca : Circuit current Refer to the above and implement proper thermal designs so that the IC will not be used under excessive power dissipation conditions under the entire operating temperature range.

The power consumption Pc of the IC in the event of shorting (i.e. the Vo and GND pins are shorted) can be obtained from the following equation:  $Pc = Vcc \times (Icca + Ishort)$  (Ishort: short current)

#### Peripheral Circuit Considerations

Vcc pin

Insert a capacitor (0.33  $\mu F$  approx.) between V<sub>CC</sub> and GND. The capacitance will vary depending on the application. Use a suitable capacitance and implement designs with sufficient margins.

• GND pin

Verify that there is no potential difference between the ground of the application board and the IC. If there is a potential difference, the set voltage will not be output accurately, resulting in unstable IC operation. Therefore, lower the impedance by designing the ground pattern as wide and as short as possible.

#### CTL terminal

The CTL terminal turns on at an operating power supply voltage of 2.0 V or higher and turns off at 0.8 V or lower. There is no particular order when turning the power supply and CTL terminals on or off.

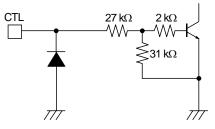


Fig.18 Input Equivalent Circuit

## •Vo Terminal

Insert a capacitor between the Vo and GND pins in order to prevent output oscillation.

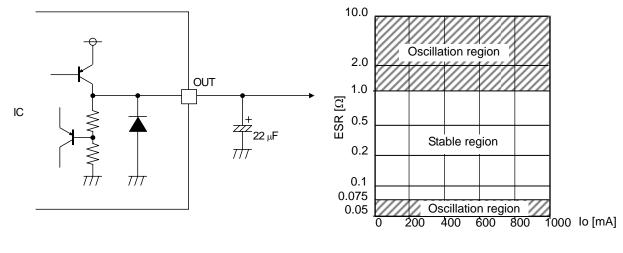


Fig.19 Output Equivalent Circuit

Fig.20 IO vs. ESR

The capacitance may vary greatly with temperature changes, thus making it impossible to completely prevent oscillation. Therefore, use a tantalum aluminum electrolytic capacitor with a low ESR (Equivalent Serial Resistance). The output will oscillate if the ESR is too high or too low, so refer to the ESR characteristics in Fig.20 and operate the IC within the stable region. Use a capacitor within a capacitance between  $22\mu$ F and  $1,000\mu$ F.

#### Operational Notes

#### 1. Absolute maximum ratings

An excess in the absolute maximum ratings, such as supply voltage, temperature range of operating conditions, etc., can break down the devices, thus making impossible to identify breaking mode, such as a short circuit or an open circuit. If any over rated values will expect to exceed the absolute maximum ratings, consider adding circuit protection devices, such as fuses.

#### 2. GND voltage

The potential of GND pin must be minimum potential in all operating conditions.

#### 3. Thermal design

Use a thermal design that allows for a sufficient margin in light of the power dissipation (Pd) in actual operating conditions.

#### 4. Inter-pin shorts and mounting errors

Use caution when positioning the IC for mounting on printed circuit boards. The IC may be damaged if there is any connection error or if pins are shorted together.

#### 5. Actions in strong electromagnetic field

Use caution when using the IC in the presence of a strong electromagnetic field as doing so may cause the IC to malfunction.

#### 6. Testing on application boards

When testing the IC on an application board, connecting a capacitor to a pin with low impedance subjects the IC to stress. Always discharge capacitors after each process or step. Always turn the IC's power supply off before connecting it to or removing it from a jig or fixture during the inspection process. Ground the IC during assembly steps as an antistatic measure. Use similar precaution when transporting or storing the IC.

#### 7. Regarding input pin of the IC

This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated.P-N junctions are formed at the intersection of these P layers with the N layers of other elements, creating a parasitic diode or transistor. For example, the relation between each potential is as follows:

When GND > Pin A and GND > Pin B, the P-N junction operates as a parasitic diode.

When GND > Pin B, the P-N junction operates as a parasitic transistor.

Parasitic diodes can occur inevitable in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Accordingly, methods by which parasitic diodes operate, such as applying a voltage that is lower than the GND (P substrate) voltage to an input pin, should

#### 8. Ground Wiring Pattern

When using both small signal and large current GND patterns, it is recommended to isolate the two ground patterns, placing a single ground point at the ground potential of application so that the pattern wiring resistance and voltage variations caused by large currents do not cause variations in the small signal ground voltage. Be careful not to change the GND wiring pattern of any external components, either.

#### 9. Thermal shutdown circuit

The IC incorporates a built-in thermal shutdown circuit (TSD circuit). The thermal shutdown circuit (TSD circuit) is designed only to shut the IC off to prevent thermal runaway. It is not designed to protect the IC or guarantee its operation. Do not continue to use the IC after operating this circuit or use the IC in an environment where the operation of this circuit is assumed.

#### **10. Overcurrent Protection Circuit**

An overcurrent protection circuit is incorporated in order to prevention destruction due to short-time overload currents. Continued use of the protection circuits should be avoided. Please note that the current increases negatively impact the temperature.

**11.** Damage to the internal circuit or element may occur when the polarity of the Vcc pin is opposite to that of the other pins in applications. (I.e. Vcc is shorted with the GND pin while an external capacitor is charged.) Use a maximum capacitance of 1000µF for the output pins. Inserting a diode to prevent back-current flow in series with Vcc or bypass diodes between Vcc and each pin is recommended.

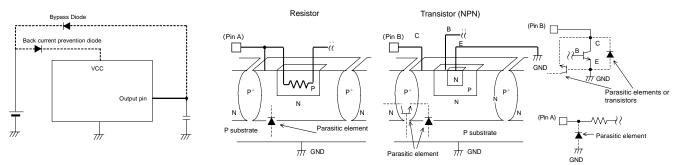


Fig.21 Bypass Diode

Fig.22 Example of Simple Bipolar IC Architecture

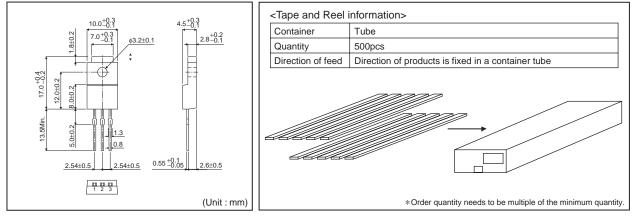
Status of this document

The Japanese version of this document is formal specification. A customer may use this translation version only for a reference to help reading the formal version.

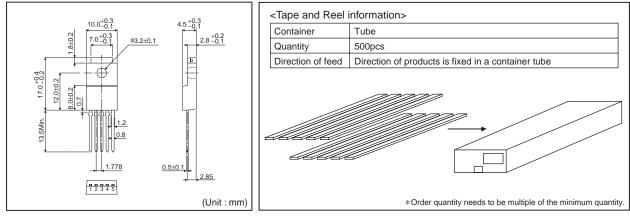
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## Physical Dimension Tape and Reel Information

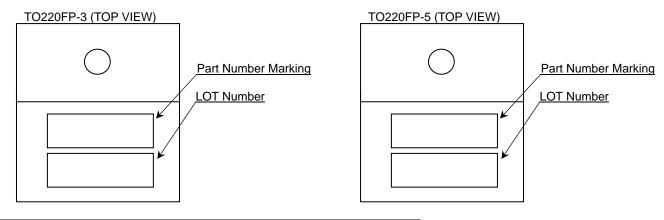
## TO220FP-3



### TO220FP-5



## Marking Diagrams



Orderable Part Number	Package	Part Number Marking	
BA15JC5T		BA15JC5	
BA18JC5T		BA18JC5	
BA25JC5T		BA25JC5	
BA30JC5T		BA30JC5	
BA33JC5T		BA33JC5	
BA50JC5T		BA50JC5	
BA60JC5T	TO220FP-3	BA60JC5	
BA63JC5T		BA63JC5	
BA80JC5T		BA80JC5	
BA90JC5T		BA90JC5	
BAJ2JC5T		BAJ2JC5	
BA00JC5WT	TO220FP-5	00JC5W	

#### Revision History

	Date	Revision	Changes					
	26.Jun.2012	001	New Release					

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  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [C] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### **Precautions Regarding Application Examples and External Circuits**

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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