

RSA 6.1J4

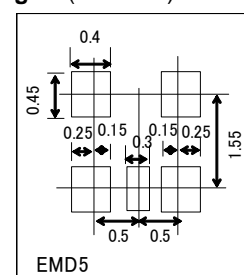
ESD protection

- 1) Ultra small mold type. (EMD5)
- 2) High reliability.

Silicon epitaxial planar

The drawing shows the JE1TA package with the following dimensions:

- Top View:**
 - Overall width: 1.6 ± 0.1
 - Distance from left edge to center of pin (1): 0.22 ± 0.05
 - Pin pitch (distance between pins 1 and 2): 0.5
 - Distance from center of pin (2) to right edge: 0.5
 - Overall width including pins: 1.0 ± 0.1
- Side View:**
 - Overall height: 1.2 ± 0.1
 - Distance from top of package to top of pin (4): 1.6 ± 0.1
 - Pin diameter: 0.13 ± 0.05
 - Pin length (from package body to tip): $0 - 0.1$
 - Distance from bottom of package to bottom of pin (3): 0.5 ± 0.05

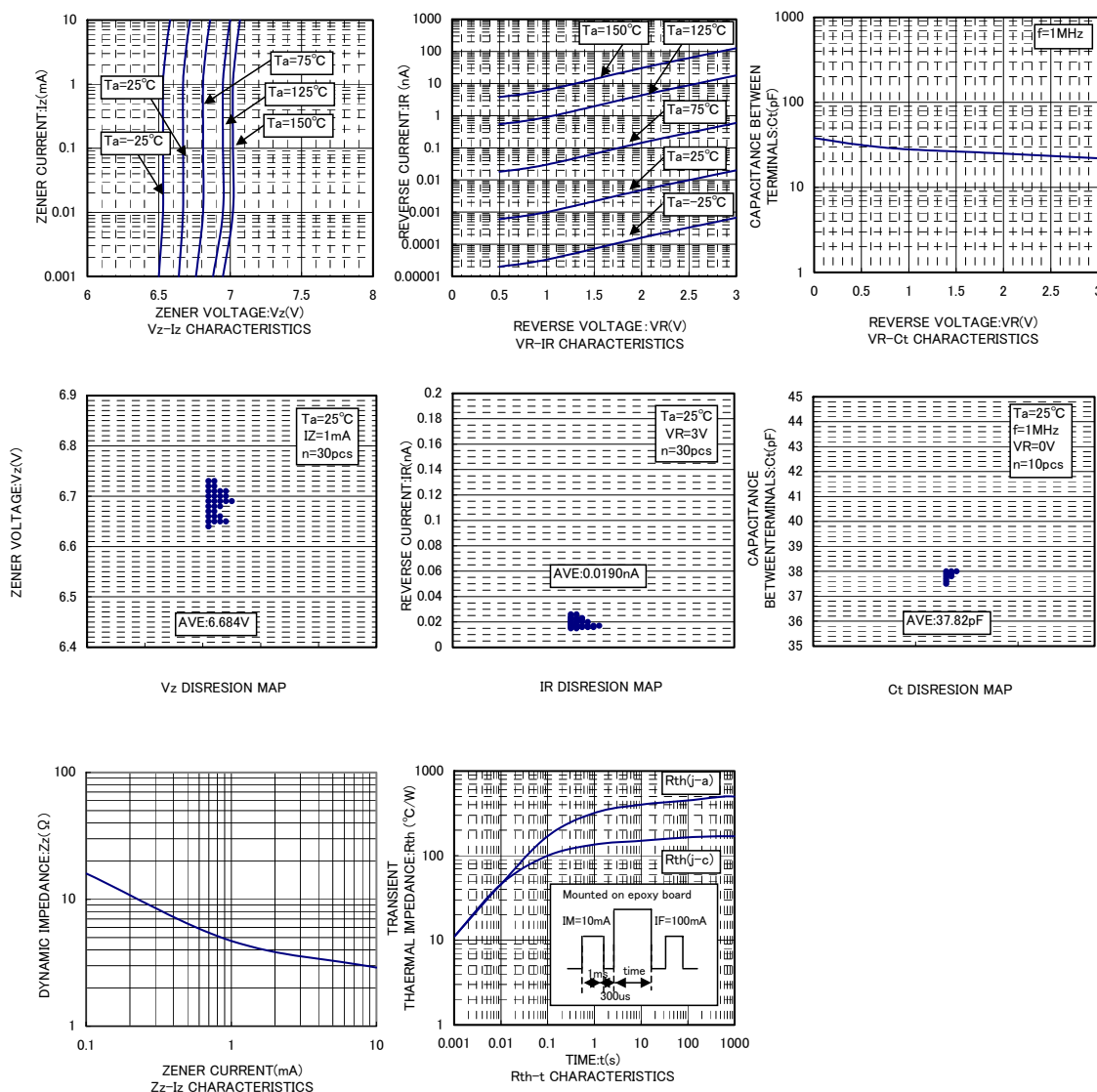


Parameter	Symbol	Limits	Unit
Peak pulse power-1 (tp=10x1000us)	P _{pk}	10	W
Peak pulse power-2 (tp=8x20us)	P _{pk}	80	W
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Zener voltage	V_Z	6.1	-	7.20	V	$I_Z=1\text{ mA}$
Reverse current	I_R	-	-	1.0	μA	$V_R=3.0\text{ V}$
Forward voltage	V_F	-	-	1.0	V	$I_F=100\text{ mA}$
Capacitance between terminals	C_t	-	35	-	pF	$f=1\text{ MHz}, V_R=0\text{ V}$

Diodes

●Electrical characteristic curves (Ta=25°C)



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