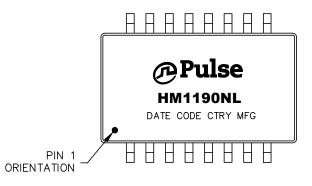


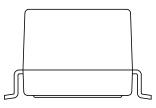


## NOTES:

- 1. ROHS COMPLIANT
- 2. STORAGE TEMPERATURE: -50°C TO +125°C
- 3. COMPLIANCE TO J-STD:
  - A. J-STD-002: SOLDERABILITY AT 245°C REFLOW PROFILE
  - B. J-STD-020: LEVEL 1, NO MOISTURE SENSITIVE
  - C. J-STD-075: R7, 245°C MAXIMUM THROUGH REFLOW SOLDER
- TO ORDER TAPE & REEL PACKAGING ADD A "T" SUFFIX
  TO THE PART NUMBER(i.e HXXXXXNL BECOMES HXXXXXNLT).







FINAL OUTLINE

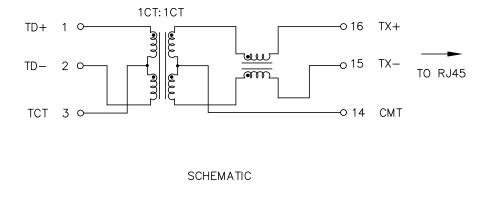
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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,SIN,100D,1:1,SM,TU	HM1190NL-11	PS-2005.001-G	1 OF 3	HM1190NL	G

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SIDE

MEDIA

\$Tx

LEGEND

ALL CHANNELS IN PHASE INPUT TO

CHIP SIDE

Td 💳

OUTPUT

## ELECTRICAL CHARACTERISTICS AT +25°C UNLESS OTHER SPECIFIED MEETS IEEE 802.3 SPECIFICATION

PARAMETER	SPECIFICATIONS						
QUALIFICATION	AEC-Q200(PENDING)						
OPERATING TEMP	-40°C TO +125°C						
TURNS RATIO	1 : 1 ±2%						
POLARITY	PER SCHEMATIC						
INDUCTANCE (OCL) (MEDIA SIDE,-40°C~+125°C)	350 uH MIN	(MEASURED AT 100 KHz, 100 mVRMS) AND WITH 8 mA DC BIAS					
	1-100 MHz	125 MHz					
INSERTION LOSS	-1.0 dB MAX	-3.0 dB MAX					
RETURN LOSS	5 MHz	30-80 MHz					
$Z_{OUT} = 100 \text{ OHMS}$	-18 dB MIN	-16 + 20LOG <sub>10</sub> (f/30 MHz) dB MIN					
DIFFERENTIAL TO	5-200 MHz						
COMMON MODE REJECTION	-50 +	17LOG <sub>10</sub> (f/5 MHz) dB MIN					
INPUT – OUTPUT ISOLATION	2500	VRMS FOR 60 SECONDS					

NOTE: f IS FREQUENCY IN MHz.

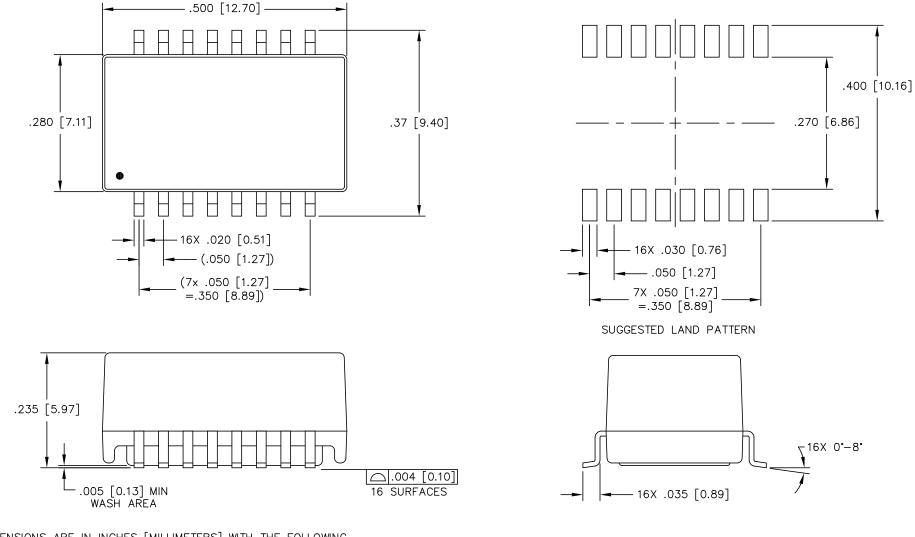
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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,SIN,100D,1:1,SM,TU	HM1190NL-11	PS-2005.001-G	2 OF 3	HM1190NL	G

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DIMENSIONS ARE IN INCHES [MILLIMETERS] WITH THE FOLLOWING TOLERANCES: [MILLIMETERS] ARE FOR REFERENCE ONLY. .XX=  $\pm$ .01 [ $\pm$ 0.25] .XXX=  $\pm$ .005 [ $\pm$ 0.13]

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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,SIN,100D,1:1,SM,TU	HM1190NL-11	PS-2005.001-G	3 OF 3	HM1190NL	G

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