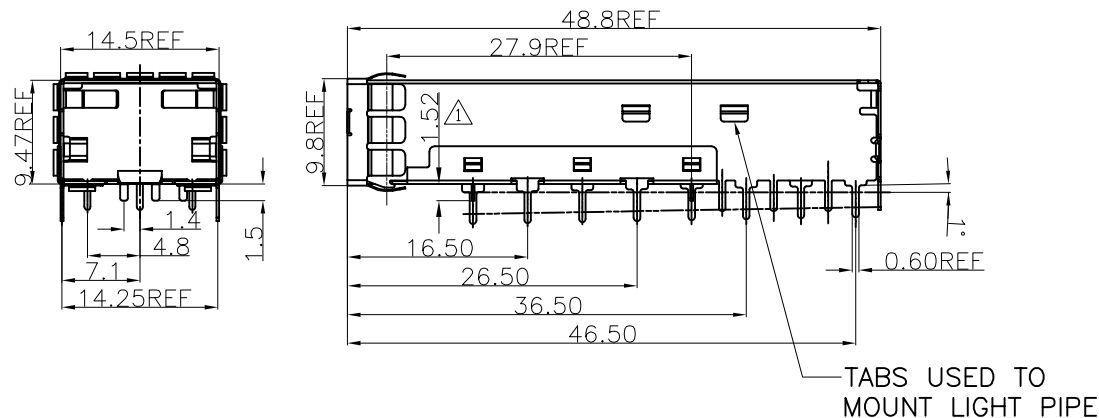
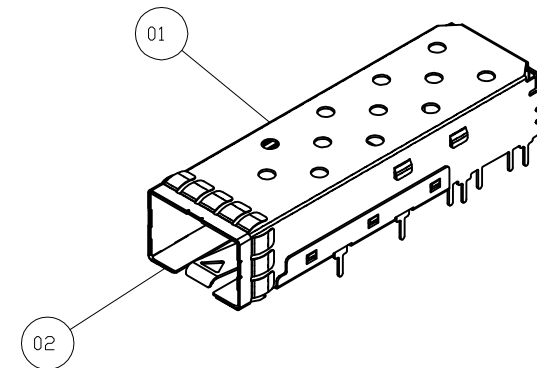
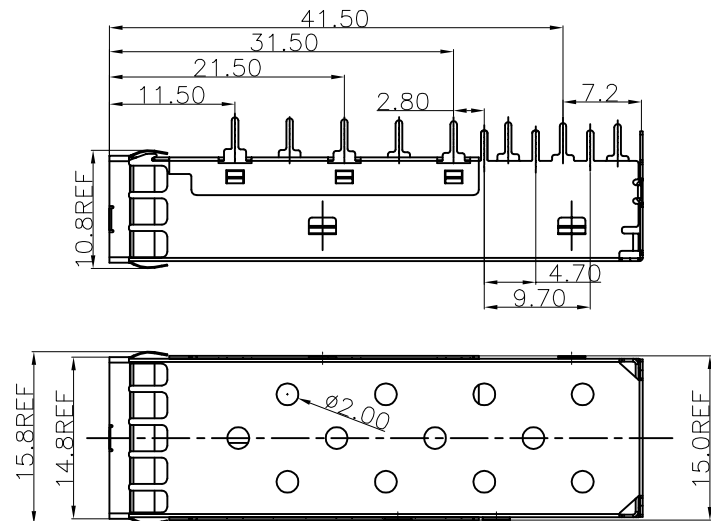


REVISION RECORD		
REV	DESCRIPTION	DRFT
1	UPDATE DIM	Jack 8/05/11'
2	MODIFY NI PLATING SPEC.	Jack 11/11/11'
3	ADD DIMENSION 1.50	Roy 7/03/13'



NOTE:

- DESCRIPTION

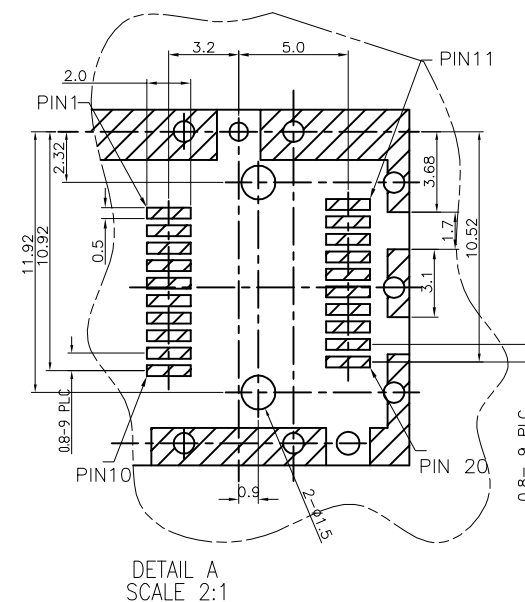
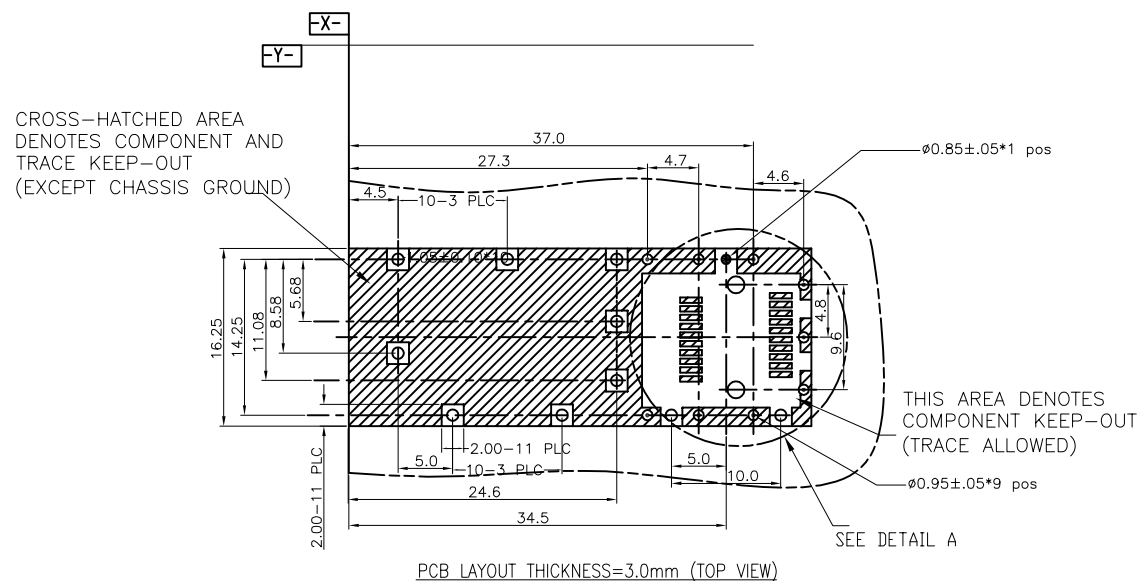
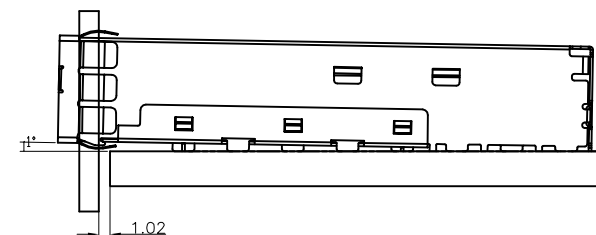
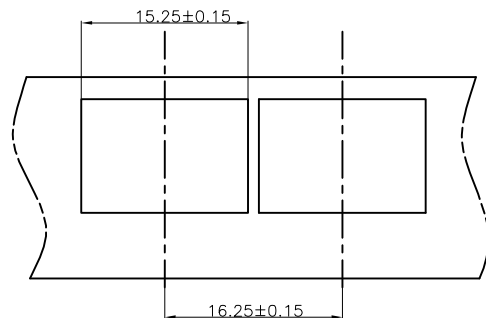
01 SFP TOP CAGE (Non-Logo)	Q'ty 1PCS	MATERIAL COPPER	PLATING MATT NICKLE
02 SFP BOTTOM CAGE (Non-Logo)	1PCS	COPPER	MATT NICKLE
- PLATED: PLATED NICKEL 50u"-350u" Max
- TEMPERATURE:

OPERATION: -40°C~ +85°C
STORAGE: -40°C~ +85°C
- RoHS COMPLIANT

PART NUMBER
SFPFCAGE001-02-L

DETACHED LISTS	<div><div>MM</div><div>(INCH)</div><div></div></div>		RoHS Compliant		<div><div><div></div></div><div>Pulse</div><div>Electronics</div></div>		FULL RISE ELECTRONIC CO., LTD	
	TOLERANCES EXCEPT AS NOTED		DRFTD PETER	DATE 6/11/08'	TITLE THROUGH HOLE			
	MM		CHKD	DATE	EMI SFP CAGE ENHANCED FOR PCI			
	.0 ± 0.25	±	APPLV	DATE	DRAWING NO. CSFPPCAGE001-02-L		SIZE A3	REV 3
	.00 ± 0.20	±	SCALE : 1:1		/PART NO. SFPPCAGE001-02-L		DO NOT SCALE DRAWING	
	.000 ± 0.10	±			SHEET 1		OF 2	
<div><div><div></div></div><div></div></div>								
THIRD ANGLE PROJECTION								

REVISION RECORD		
REV	DESCRIPTION	DRFT
1	UPDATE DIM	Jack 8/05/11'
2	MODIFY NI PLATING SPEC.	Jack 11/11/11'
3	ADD DIMENSION 1.50	Roy 7/03/13'



- $\phi 1.05 * 10$
- $\phi 0.95 * 9$
- $\phi 0.85 * 1$

Suggestion: Through hole is $\phi 0.85 +0.05/-0$

DETACHED LISTS		RoHS Compliant		Pulse Electronics		FULL RISE ELECTRONIC CO., LTD	
TOLERANCES EXCEPT AS NOTED		DFTD PETER		DATE 6/11/08'		TITLE THROUGH HOLE	
MM		CHKD		DATE		DRAWING NO. CSFPFCAGE001-02-L	
.0 ± 0.25		APPL		DATE		/PART NO. SFPPCAGE001-02-L	
.00 ± 0.20		SCALE 1:1		DO NOT SCALE DRAWING		SHEET 2 OF 2	
.000 ± 0.10		SIZE A3		REV 3			
ANGLES ± 0.5°							
THIRD ANGLE PROJECTION							

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Pulse:

[SFPPCAGE001-02-L](#)