

Product Datasheet

P1110B – 915 MHz RF Powerharvester® Receiver



DESCRIPTION

The Powercast P1110B Powerharvester® receiver is an RF energy harvesting device that converts RF to DC. Housed in a compact SMD package, the P1110B receiver provides RF energy harvesting and power management for battery and capacitor recharging. The P1110B converts RF energy to DC and provides the energy to the attached storage element. When an adjustable voltage threshold on the storage element is achieved, the P1110B automatically disables charging. A microprocessor can be used to obtain data from the component for improving overall system operation.

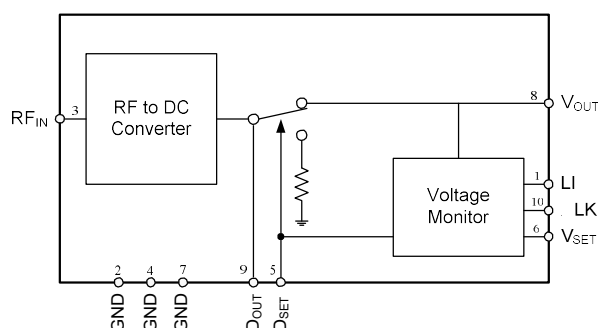
FEATURES

- High conversion efficiency, >70%
- Low power consumption
- Configurable voltage output to support Li-ion and Alkaline battery recharging
- Operation from 0V to support capacitor charging
- Received signal strength indicator
- No external RF components required - Internally matched to 50 ohms
- Wide operating range
- Operation down to -5 dBm input power
- Industrial temperature range
- RoHS Compliant

APPLICATIONS

- Wireless sensors
 - Industrial Monitoring
 - Smart Grid
 - Structural Health Monitoring
 - Defense
 - Building automation
 - Agriculture
 - Oil & Gas
 - Location-aware services
- Wireless trigger
- Low power electronics

FUNCTIONAL BLOCK DIAGRAM



PIN CONFIGURATION

TOP VIEW



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ABSOLUTE MAXIMUM RATINGS

T_A = 25°C, unless otherwise noted.

Parameter	Rating	Unit
RF Input Power	23	dBm
RF _{IN} to GND	0	V
D _{SET} to GND	6	V
V _{OUT} to GND	4.3	V
V _{OUT} Current	100	mA
Operating Temperature Range	-40 to 85	°C
Storage Temperature Range	-40 to 85	°C

Exceeding the absolute maximum ratings may cause permanent damage to the device.

ESD CAUTION

This is an ESD (electrostatic discharge) sensitive device. Proper ESD precautions should be taken to avoid degradation or damage to the component.



PIN FUNCTIONAL DESCRIPTION

Pin	Label	Function
1	LI	Li-ion/LiPo recharging pin. Connect directly to the analog ground plane for 4.2V maximum recharging. NC when using ALK or V _{SET} pin.
2	GND	RF Ground. Connect to analog ground plane.
3	RF _{IN}	RF Input. Connect to 50Ω antenna through a 50Ω transmission line. Add a DC block if antenna is a DC short.
4	GND	RF Ground. Connect to analog ground plane.
5	D _{SET}	Digital Input. Set to enable measurement of harvested power. If this function is not desired leave NC.
6	V _{SET}	Maximum Output Voltage Adjustment. Sets the maximum output voltage on the V _{OUT} pin. Connect to an external resistor. NC when using LI or ALK pin.
7	GND	DC Ground. Connect to analog ground plane.
8	V _{OUT}	DC Output. Connect to external storage device. Maximum output voltage set by V _{SET} , LI, or ALK pin.
9	D _{OUT}	Analog Output. Provides an analog voltage level corresponding to the harvested power.
10	ALK	Alkaline recharging pin. Connect directly to the analog ground plane for 3.3V maximum recharging. NC when using LI or V _{SET} pin.

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SPECIFICATIONS

$T_A = 25^{\circ}\text{C}$, $V_{OUT} = 3.0\text{V}$, unless otherwise noted.

Parameter	Symbol	Condition	Min	Typ	Max	Unit
RF Characteristics						
Input Power	RF_{IN}		0		20	dBm
Frequency			902		928	MHz
DC Characteristics						
Output Voltage	V_{OUT}		0		4.2	V
Output Current	I_{OUT}				50	mA
Output Current	I_{OUT}	No RF_{IN}		-1.5		μA
V_{SET} Range	V_{SET}		1.8		4.2	V
Signal Strength	D_{OUT}	$RF_{IN} = 0\text{dBm}$		61		mV
Digital Characteristics						
D_{SET} Input High				1		V
Timing Characteristics						
D_{SET} Delay				20		μs

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FUNCTIONAL DESCRIPTION

RF INPUT (RF_{IN})

The RF input is an unbalanced input from the antenna. Any standard or custom 50Ω antenna may be used with the receiver. The P1110B has been optimized for operation in the 902-928MHz band but will operate outside this band with reduced efficiency. Contact Powercast for custom frequency requirements.

The RF input must be isolated from ground. For antennas that are a DC short, a high-Q DC blocking capacitor should be added in series with the antenna.

STORAGE SELECTION (V_{OUT})

The P1110B is designed to charge an external storage element including batteries and capacitors. The output voltage from the P1110B will be set by the voltage of the storage element with a maximum set by the V_{SET}, LI, or ALK pin. The P1110B will produce a charging current that will be dependent on the RF input power. The voltage on this pin can vary from 0V to 4.2V. The charging current for a fixed input RF power will decrease as the voltage on the V_{OUT} pin increase due to the fixed amount of power available.

The P1110B monitors the voltage on the storage element and turns off V_{OUT} when the element is fully charged. The P1110B does not monitor the charging current because it is typically much less than the maximum charge current of the storage element.

When selecting a storage element, the leakage current must be strongly considered. Certain battery chemistries have higher leakage currents than others. It is recommended that the leakage current of the storage element be less than 1% per month. Higher leakage currents will result in using more of the harvested energy to replace the capacity lost due to leakage rather than replenishing the capacity.

When no load is attached to the P1110B, a minimum of 10uF is required on the V_{OUT} pin.

RSSI OPERATION (D_{OUT}, D_{SET})

The RSSI functionality allows the sampling of the received signal to provide an indication of the amount of energy being harvested. When D_{SET} is driven high the harvested DC power will be directed to an internal sense resistor, and the corresponding voltage will be provided to the D_{OUT} pin. The voltage on the D_{OUT} pin can be read after a 20μs settling time.

When the RSSI functionality is being used, the harvested DC power is not being stored.

If the RSSI functionality is not used, the D_{OUT} and D_{SET} pins should be left as no connects. The D_{SET} pin has an internal pull down.

SETTING THE OUTPUT VOLTAGE (V_{OUT})

The maximum voltage from the P1110B is set using the V_{SET}, LI, or ALK pin. The LI pin can be directly connected to ground to set the maximum voltage to 4.2V, or the ALK

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pin can be directly connected to ground to set the maximum voltage to 3.3V. For custom voltage settings, the V_{SET} pin can be used. Placing a resistor from V_{SET} to ground will adjust the maximum output voltage. The resistor can be calculated using the following equation.

$$R = \frac{12.35M}{V_{OUT-MAX} - 1.235}$$

LAYOUT CONSIDERATIONS

When setting the output voltage, the resistor connected to the V_{SET} pin should be as close as possible to the pin. No external capacitance should be added to this pin.

The RF_{IN} feed line should be designed as a 50 Ω trace and should be as short as possible to minimize feed line losses. The following table provides recommended dimensions for 50 Ω feed lines (CPWG) for different circuit board configurations.

PCB Side View			
Material	Thickness (H)	Trace Width (S)	Spacing (W)
FR4 (ε _r = 4.2)	62	50	9
FR4 (ε _r = 4.2)	31	50	20

*All dimensions are in mils.

The GND pins on each side of the RF_{IN} pin should be connected to the PCB ground plane through a via located next to the pads under the receiver.

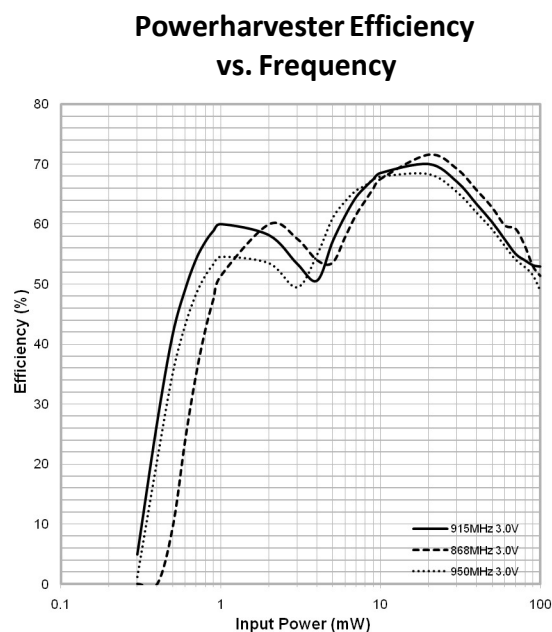
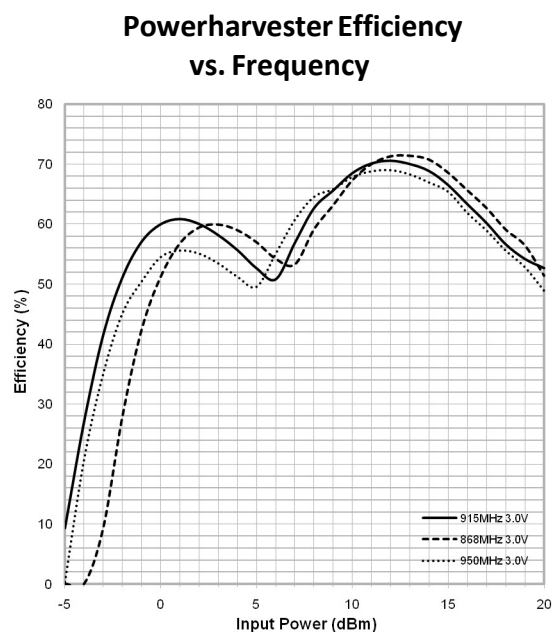
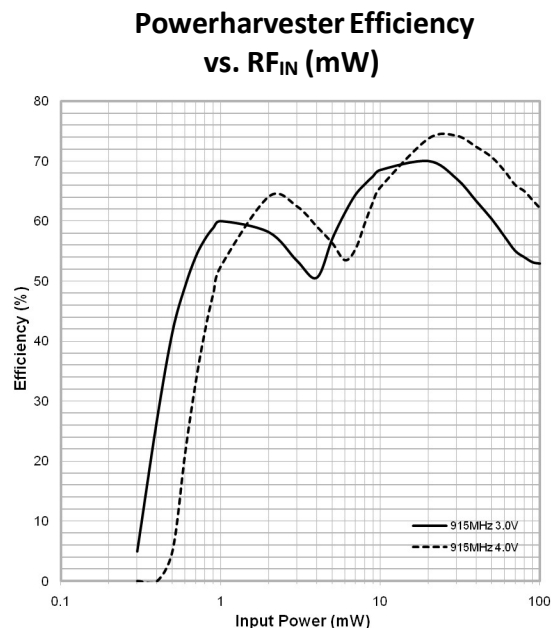
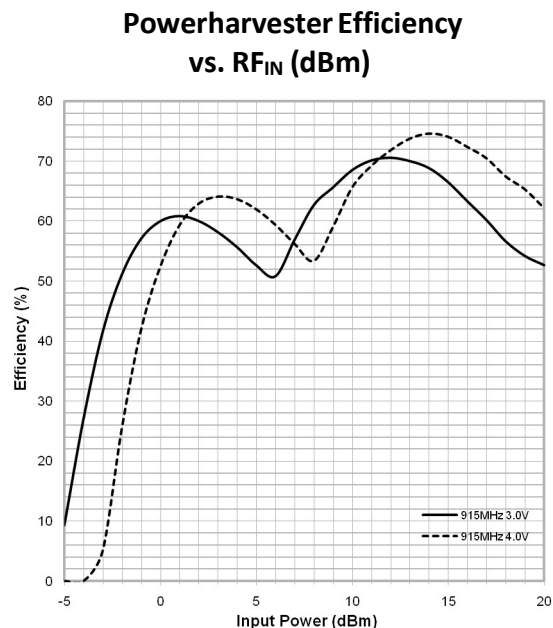
The D_{OUT} pin can contain low-level analog voltage signals. If a long trace is connected to this pin, additional filtering capacitance next to the A/D converter may be required. Additional capacitance on this pin will increase the D_{SET} delay time.

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TYPICAL PERFORMANCE GRAPHS $T_A = 25^\circ\text{C}$, unless otherwise noted.



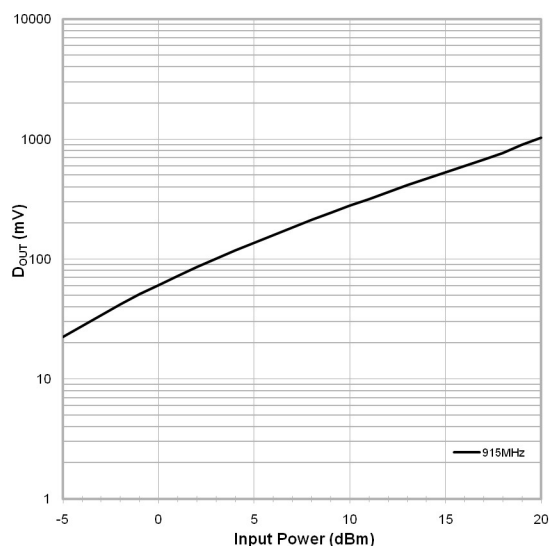
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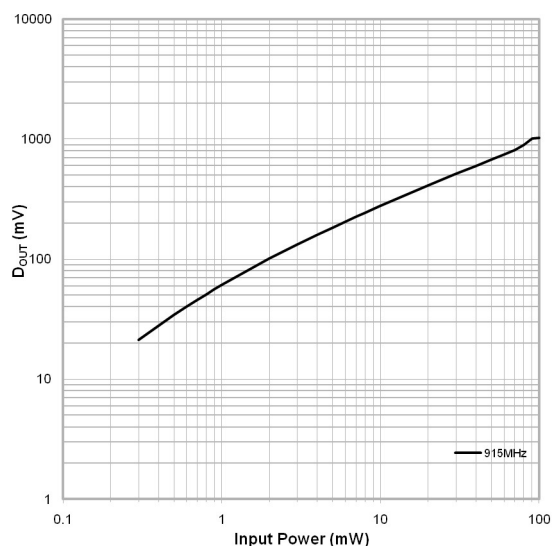


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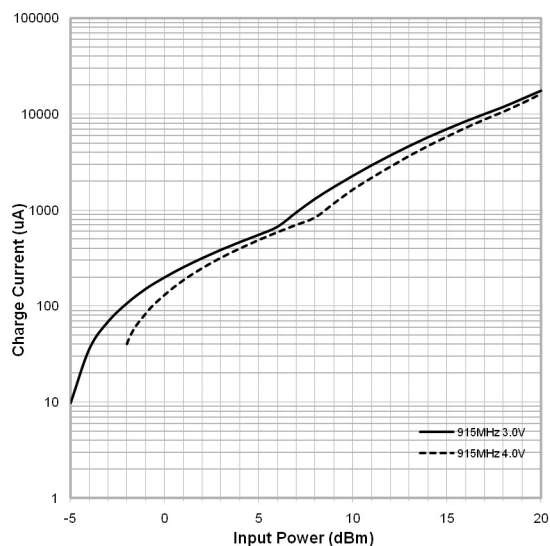
Received Signal Strength Indicator
vs. RF_{IN} (dBm)



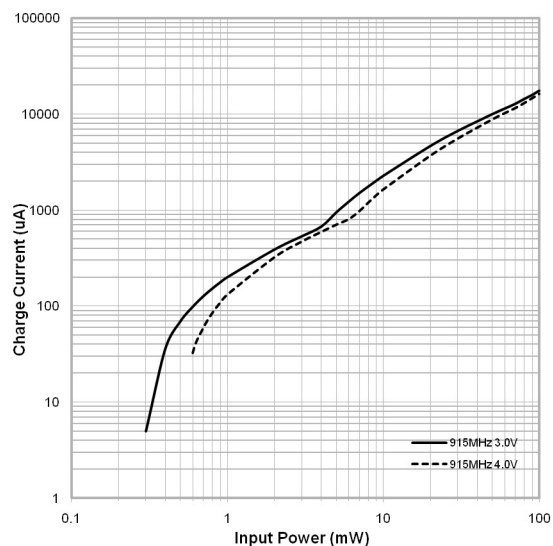
Received Signal Strength Indicator
vs. RF_{IN} (mW)



Charge Current vs. RF_{IN} (dBm)



Charge Current vs. RF_{IN} (mW)

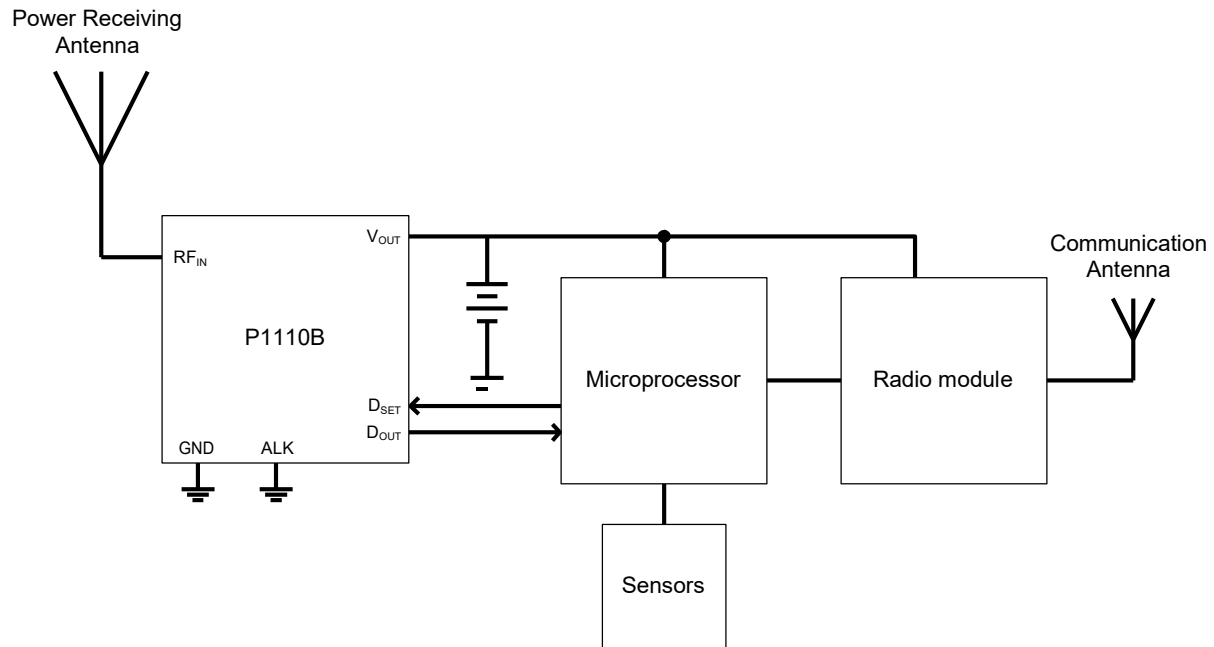


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TYPICAL APPLICATION CIRCUIT

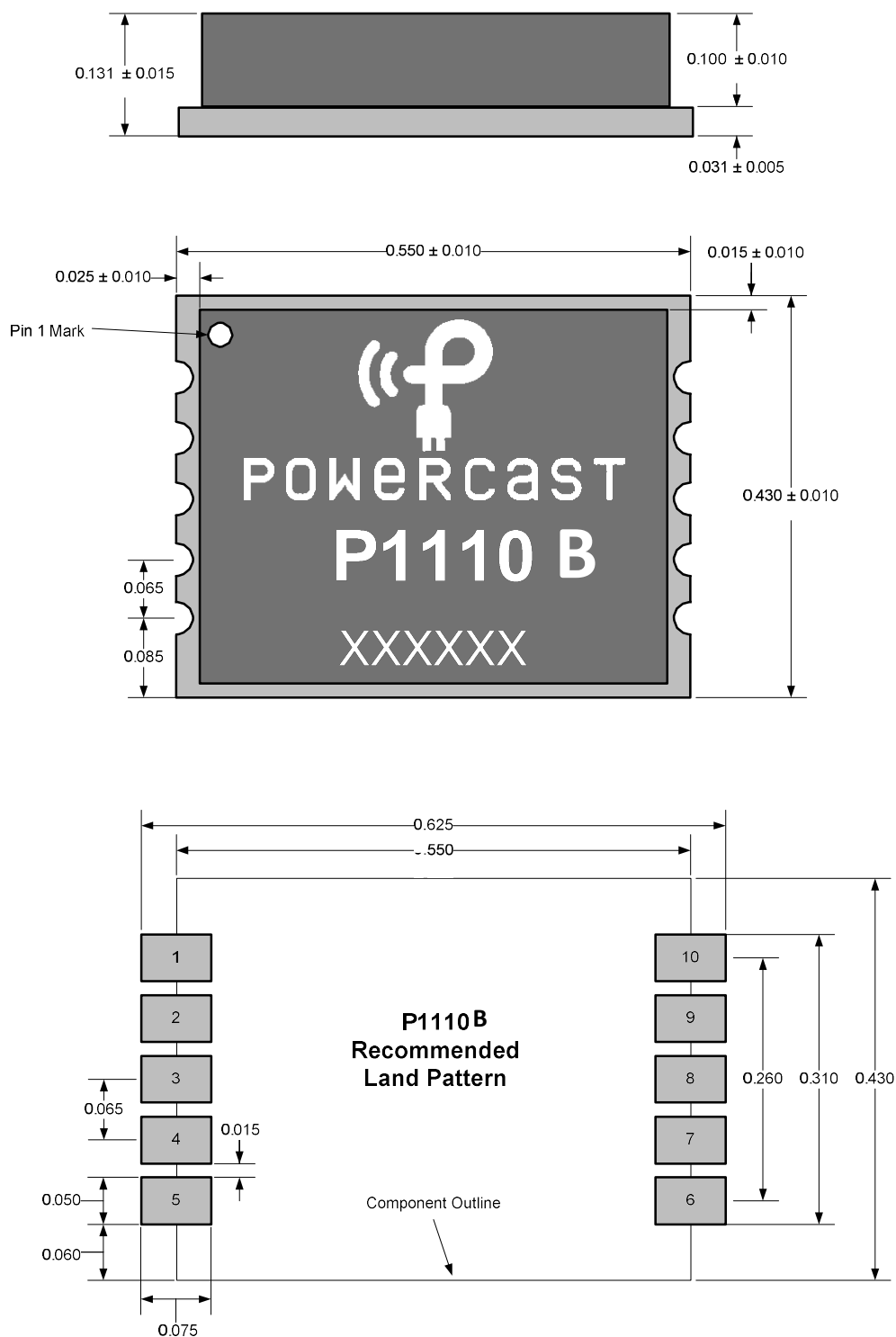


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MECHANICAL SPECIFICATIONS



*Dimensions in inches

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