



LV44F / LV44G Series 3.3 V LVDS Clock Oscillators

November 2018

Lead Free 

- Pletronics' LV44F and LV44G Series is a quartz crystal controlled precision square wave generator with a fast rise and fall time LVDS output.
- The package is designed for high density surface mount designs.
- Tape and Reel or cut tape packaging is available.
- 3.2 x 2.5 mm LCC Ceramic Package
- Enable/Disable Function on pad 1
- Disable function includes low standby power mode
- LV44F use Fundamental Mode Crystals
13.5MHz to 110MHz
- LV44G use 3rd Overtone Crystals
35MHz to 170MHz
- Low Jitter

Pletronics Inc. certifies this device is in accordance with the
RoHS 6/6 (2011/65/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following:

Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's

Weight of the Device: 0.16 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020D.1

Second Level Interconnect code: e4

Absolute Maximum Ratings:

Parameter	Unit
V _{CC} Supply Voltage	-0.5V to +5.0V
V _i Input Voltage	-0.5V to V _{CC} + 0.5V
V _o Output Voltage	-0.5V to V _{CC} + 0.5V
Junction Temperature (T _j)	-55°C to +150°C

Thermal Characteristics

The maximum die or junction temperature is 150°C

The thermal resistance junction to board is 30 to 50°C/Watt depending on the solder pads, ground plane and construction of the PCB.

Part Number:

LV44	45	G	E	V	-125.0M	-XX	
							Packaging code or blank T250 = 250 per Tape and Reel T500 = 500 per Tape and Reel T1K = 1000 per Tape and Reel
							Frequency in MHz
							Supply Voltage V_{cc} V = 3.3V ± 10%
							Optional Enhanced OTR Blank = Temp. range -10 to +70°C C = Temp. range -20 to +70°C E = Temp. range -40 to +85°C
							Series Model F = Fundamental mode crystal G = 3 rd Overtone mode crystal
							Frequency Stability 45 = ± 50 ppm 44 = ± 25 ppm 20 = ± 20 ppm
							Series Model

Marking Legend:

P ff.ff Lt
• YMDXXX

- PLE = Pletronics
 ff.fff M = Frequency in MHz
 L = LVDS
 t = Mode of operation 'F' or 'G'
 YMD = Date of Manufacture (year and week, or year-month-day)
 All other marking is internal factory codes

Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from the marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD

Code	6	7	8	9	0	Code	A	B	C	D	E	F	G	H	J	K	L	M
Year	2016	2017	2018	2019	2020	Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Code	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	G
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Code	H	J	K	L	M	N	P	R	T	U	V	W	X	Y	Z	
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	

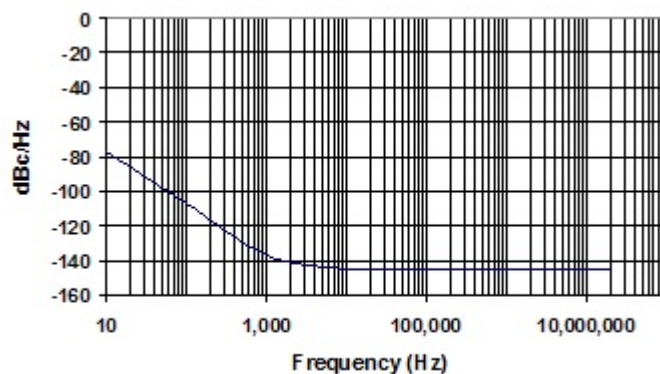
Electrical Specification for 3.30V $\pm 10\%$ over the specified temperature range

Item	Min	Typ	Max	Unit	Condition	
Frequency Range	13.5	-	110	MHz	For “F” series devices	
	35	-	170	MHz	For “G” series devices	
Frequency Accuracy “45”	-50	-	+50	ppm	For all supply voltages, load changes, aging for 1 year, shock, vibration and temperatures	
“44”	-25	-	+25			
“20”	-20	-	+20			
Supply Voltage Sensitivity	-2	-	2	ppm	For V _{cc} change of \pm 10%	
Output Waveform	LVDS					
Output High Level (V _{OH})	-	1.43	1.60	V	See load circuit	
Output Low Level (V _{OL})	0.90	1.10	-	V	See load circuit	
Output Offset Voltage	1.125	-	1.375	V		
Output Symmetry	45	-	55	%	output crossing point	See load circuit
Output Swing	250	350	450	mV	See load circuit	
Jitter*	-	-	0.6	ps RMS	12 KHz to 20 MHz from the output frequency	
	-	-	2.8	ps RMS	10 Hz to 1 MHz from the output frequency	
Output T _{RISE} and T _{FALL}	-	150	400	ps	V _{th} is 20% and 80% of waveform	
V _{cc} Supply Current (I _{cc})	-	12 16	20 27	mA	< 80MHz ≥ 80MHz	“F” series devices
	-	12 16 20 24	20 27 34 40	mA	< 90MHz ≥ 90 MHz to 125MHz ≥ 125MHz to 160MHz ≥ 160MHz	“G” series devices
Disable current	-	-10	-	uA	Pad 1 = 0.0 volts	
V disable	-	-	0.9	V	Referenced to pad 3	
V enable	2.1	-	-	V	Referenced to pad 3	
Output leakage V _{OUT} = V _{cc}	-10	-	+10	uA	Pad 1 low, device disabled	
V _{OUT} = 0V	-10	-	+10	uA		
Enable time	-	-	2	ms		
Disable time	-	-	200	ns	Time for output to reach a high Z state	
Start up time	-	-	2	ms	Time for output to reach specified frequency	
Operating Temperature	-10	-	+70	°C	Standard Temperature Range	
	- 20	-	+70	°C	Extended Temperature Range “C” Option	
	- 40	-	+85	°C	Extended Temperature Range “E” Option	
Storage Temperature	-55	-	+125	°C		
Standby Current I _{cc}	-	-	10	uA	Pad 1 low, device disabled	

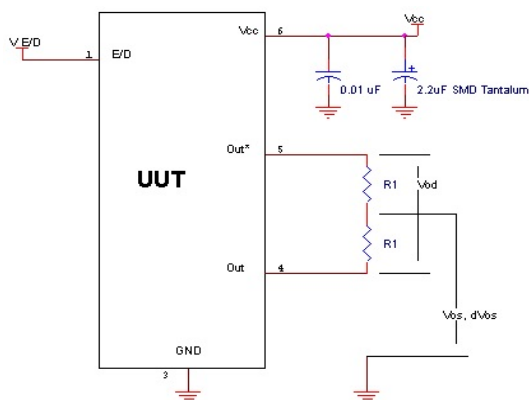
* Measured at 125.0 MHz

Specifications with Pad 1 E/D open circuit unless stated otherwise

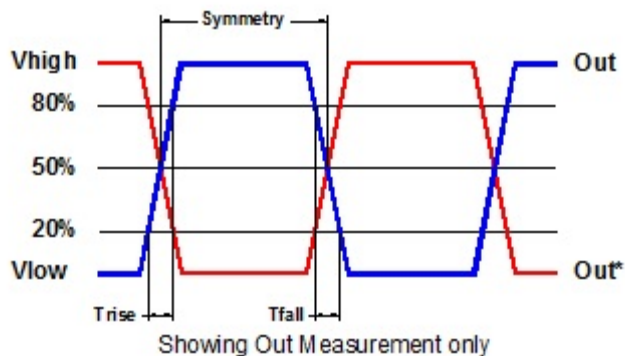
Typical Phase-Noise Response



Load Circuit



Test Waveform



Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A





ESD Rating

Model	Minimum Voltage	Conditions
Human Body Model	1500	MIL-STD-883 Method 3115
Charged Device Model	1000	JESD 22-C101

Package Labeling

Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Courier New
Bar code is 39-Full ASCII

Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Arial

P/N:	
	LV4445FEV-75.0M
Customer P/N:	
	12345678
Qty:	
	1000
D/C	
	0JX-MTG
MSL:	1

RoHS Compliant

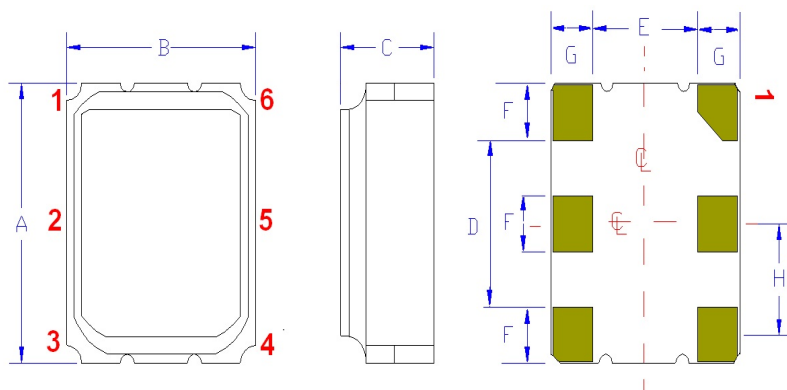
2nd LvL Interconnect

Category=e4

Max Safe Temp=260C for 10s 2X Max

P/N:	
	LV4445GEV-125.0M
Customer P/N:	
	12345678
Qty:	
	1000
D/C	
	0JX-MTG
MSL:	1

Mechanical:



	Inches	mm
A	0.125 \pm 0.004	3.20 \pm 0.10
B	0.098 \pm 0.004	2.50 \pm 0.10
C	0.040 max	1.00 max
D ¹	0.063	1.60
E ¹	0.051	1.30
F ¹	0.031	0.80
G ¹	0.024	0.60
H ¹	0.047	1.20

Contacts:

Gold 11.8 to 39.4 μ inches (0.3 to 1.0 μ m)
over
Nickel 50 to 350 μ inches (1.27 to 8.89 μ m)

¹ Typical dimensions

Not to Scale

Pad	Function	Note
1	Output Enable/Disable	When this pad is not connected the oscillator shall operate. When this pad is < 'V disable', the output will be inhibited (high impedance state.) Recommend connecting this pad to V _{cc} if the oscillator is to be always on.
2	No connect	There is no internal connection to this pad
3	Ground (GND)	
4	Output	The outputs must be terminated, 100 ohms between the outputs is the ideal termination.
5	Output*	
6	Supply Voltage (V _{cc})	Recommend connecting appropriate power supply bypass capacitors as close as possible.



Layout and application information

Recommend connecting Pad 1 and Pad 2 together to permit the design to accept Enable/Disable input on either pad

For Optimum Jitter Performance, Pletronics recommends:

- a ground plane under the device
- no large transient signals (both current and voltage) should be routed under the device
- do not layout near a large magnetic field such as a high frequency switching power supply
- do not place near piezoelectric buzzers or mechanical fans.

IMPORTANT NOTICE

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