

PLETRONICS PETTJ Series 3.3V PECL Clock Oscillator





5.0 x 7.0 x 1.70 mm LCC Ceramic Package

Features

- Pletronics' PE77J Series is a Quartz crystal controlled Precision Square Wave Oscillator
- PECL Differential Output
- Enable/Disable Function on pad 1
- Low Jitter
- 3.3V nominal Supply Voltage
- 25-175 MHz Frequency Range

Applications

Driving A/Ds, D/As, FPGAs

Fibre Channel

Ethernet, GbE, SynchE

Medical

Storage Area Networking

COTS

Telecom

PON

		PON					
Electrical Characteristics							
Parameter	Min	Тур	Max	Unit	Condition		
Frequency Range ²	25	-	175	MHz	Consult factory for other options		
Frequency Stability vs. Temperature 2 $\pm 20 = 20, \pm 25 = 44, \pm 50 = 45$	-20	-	+50	ppm	For all supply voltages, load changes, aging for 1 year at 25°C ± 2°C, shock, vibration and temperatures		
Operating Temperature Range ²	-10 -20 -40	-	+70 +70 +85	°C	Standard range Extended range C option Extended range E option		
Supply Voltage ^{1, 2} V _{CC}	3.135	3.30	3.465	Volts			
Supply Current I _{CC}	-	-	60	mA			
Output Waveform		PEC	L / ECL				
Output High Level V _{OH}	2.275	2.350	2.420	Volts	Referenced to Ground		
Output Low Level V _{OL}	1.490	1.600	1.680	Volts	Referenced to Ground		
Output T _{RISE} and T _{FALL}	-	-	0.5	ns	Vth is 20% and 80% of waveform		
Start Up Time	-	-	10	ms	Time for output to reach specified frequency		
Duty Cycle	45	-	55	%	50% of V _{CC} (See Load Circuit)		
V _{DISABLE}	-	-	0.99	V	Deferenced to ground		
V _{ENABLE}	2.31	-		V	Referenced to ground		
Enable Time	-	-	2	ms	Time for output to reach a logic high state		
Disable Time	-	-	200	ns	Time for output to reach a high Z state		
Enable/Disable Internal Pull-up	50	-	-	Kohm	To V _{CC} , measured with pad 1 = 0.0 volts		
Output Leakage $V_{OUT} = V_{CC}$ $V_{OUT} = 0V$	-10 -10	-	+10 +10	μA	Pad 1 low, device disabled		
Standby Current	-	-	30	μΑ			
littor	-	0.1	-	20	12 kHz to 20 MHz from the output frequency at 156.25 MHz		
Jitter	-	1.25	-	ps	10 Hz to 1 MHz from the output frequency		
Storage Temperature Range	-55	-	+125	°C			
Phase Noise 10 Hz 100 Hz 1 kHz 10 kHz 100 kHz	-	-64 -98 -127 -142 -152	-	dBc/Hz	25°C ± 2°C at 156.25 MHz		

Notes: Specifications with Pad 1 E/D open circuit

² Specified by part number

¹ Place an appropriate power supply bypass capacitor next to device for correct operation



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Part Number

Series Model	Frequency Stability		Operating Temperature Range	Supply Voltage V _{cc}	Frequency in MHz	Optional T&R Packaging code	
PE77	45	J	E	V	- 125.0M	-XX	
	45 = ± 50 ppm (STD) 44 = ± 25 ppm 20 = ± 20 ppm		Blank = -10 to +70°C (STD) C = -20 to +70°C E = -40 to +85°C	V = 3.3V ±10%	25– 175 MHz	T250 = 250 per Reel T500 = 500 per Reel T1K = 1000 per Reel (Std for 1K pcs)	

Device Marking

PLE PE77
FFF.FF M
• YMDxx

PE7xYWWxx FFF.FF M • PLExx

PLE = Pletronics
FFF.FF = Frequency in MHz
YMD or YWW = Date Code, All other marking is internal codes

Note: Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD (Year Month Day)

Code	7	8		9	0	1	Code	е	A	В	С	D	Е	F	•	G	Н	J	K	L	М
Year	2017	201	8	2019	2020	2021	Mont	: h J/	AN	FEB	MAR	APR	MA	/ JU	N	JUL	AUG	SEP	OCT	NOV	DEC
Code	1	2	3	4	5	6	7	8	9	Α	В	С	D	E	F	G	i				
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	6				
Code	Н	J	K	L	М	N	Р	R	Т	U	V	w	Х	Υ	Z						
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31						

Package Labeling

Tape and Reel available for quantities of 250 to 1000 per reel, cut tape for < 250. 16mm tape, 8mm pitch.

P/N Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII RoHs Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant

2nd LvL Interconnect

Category=e4

Max Safe Temp=260C for 10s 2X Max

Pletronics Inc. certifies this device is in accordance with the RoHS 3 (2015/863) and WEEE 2 (2012/19/EU) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's Weight of the Device: 0.16 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020D

Second Level Interconnect code: e4



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	Inches	mm	→ B →
	0.276 ± 0.006	7.00 ± 0.15	•
В	0.197 ± 0.006	5.00 ± 0.15	
С	0.067 max	1.70 max	*CANTO!
D ¹	0.038	0.96	Â
E ¹	0.200	5.08	
F ¹	0.004	0.10	
G ¹	0.050	1.27	
H ¹	0.055	1.40	
I ¹	0.024	0.60	Pad Layout mm shown
J ¹	0.006R	0.15R	Disclaimer: Recommended layout shown.
K ¹	0.008R	0.20R	Adjust layout as needed for individual process requirements.
¹ Тур	oical dimensions		
			(Not to Scale)

Layou	ıt	
Pad	Function	Note
1	Output Enable/Disable	The oscillator shall operate when this pad is not connected. The output will be inhibited (high impedance state) when this pad is logic low. Recommend connecting this pad to $V_{\rm CC}$ if the oscillator is to be always on.
2	No connect	There is no internal connection to this pad. Recommend connecting to pad 1 to permit E/D input on either pad for layout.
3	Ground (GND)	
4	Output	Both outputs must be terminated and biased for proper operation. The ideal termination is 50 ohms connected to 2.0V
5	Output*	below supply voltage
6	V _{CC} Supply Voltage	Connect an appropriate power supply bypass capacitor as close as possible to pad 4

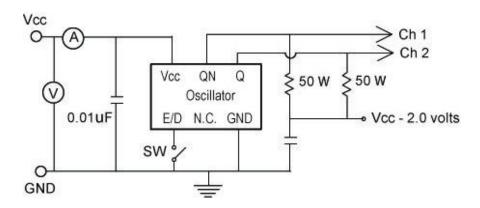
For Optimum Jitter Performance, Pletronics recommends:

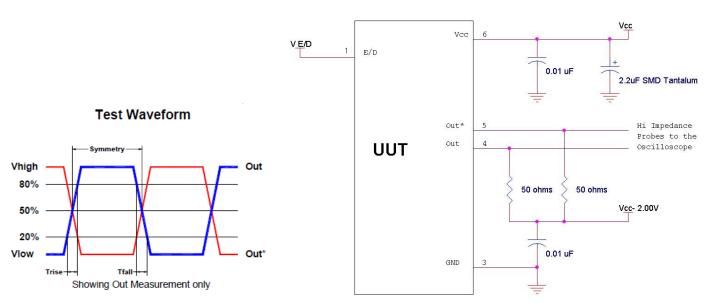
- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply Do not place near piezoelectric buzzers or mechanical fans



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Electrical Test / Load Circuit





Environmental / ESD Ratings

Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	JESD22-B104
Vibration	JESD22-B103
Solderability	IPC J-STD-002
Thermal Shock	MIL-STD-883 Method 1011, Condition A

ESD Rating

Model	Min. Voltage	Condition
Human Body Model	2000V	JESD22-A114
Charged Device Model	500V	JESD 22-C101
Machine Model	200V	JESD22-A115

Thermal Characteristics:

The maximum die or junction temperature is 155°C

The thermal resistance junction to board is 30 to 50°C/Watt depending on the solder pads, ground plane and construction of the PCB.

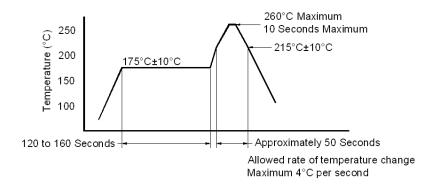
Absolute Maximum Ratings

Parameter	Unit
V _{CC} Supply Voltage	-0.5V to +5.0V
Vi Input Voltage	-0.5V to V _{CC} + 0.5V
Vo Output Voltage	-0.5V to V _{CC} + 0.5V



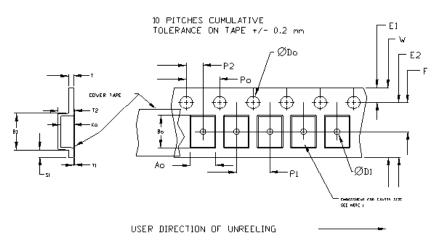
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Reflow Cycle



The part may be reflowed 2 times without degradation (typical for lead free processing).

Tape and Reel

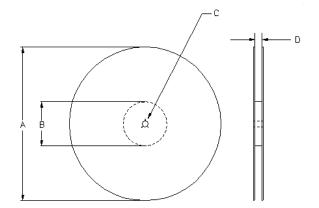


	Tape Constant Dimensions Table 1											
Tape Size	Do	D1 min	E1	Ро	P2	S1 min	T max	T1 max				
8mm		1.0			2.0							
12mm	1.5	1.5	1.75	4.0	±0.05	0.0	0.0	0.4				
16mm	+0.1 -0.0	1.5	±0.1	±0.1	2.0	0.6	0.6	0.1				
24mm	-0.0	1.5			±0.1							

	Tape Variable Dimensions Table 2											
Tape Size	B1 max	E2 min	F	P1	T2 max	W max	Ao, Bo & Ko					
16mm	12.1	14.25	7.5 ±0.1	8.0 ±0.1	8.0	16.3	Note 1					

Dimensions in mm Drawing Not to scale

Note 1: Embossed cavity to conform to EIA- 481-B



	Reel Dimensions (may vary) Table 3											
		АВ				D						
Reel Size	Inches	mm	Inches	mm	mm	mm						
7	7.0	177.8	2.50	63.5	13.0	Tape size +0.4						
10	10.0	254.0	4.00	101.6	+0.5	+2.0						
13	13.0	330.2	3.75	95.3	-0.2	-0.0						



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