



DB2J41100L  
Silicon epitaxial planar type

For rectification

- Features
- Low forward voltage and low reverse leakage current
  - Short reverse recovery time trr
  - Halogen-free / RoHS compliant  
(EU RoHS / UL-94 V-0 / MSL:Level 1 compliant)

■ Marking Symbol: 4R

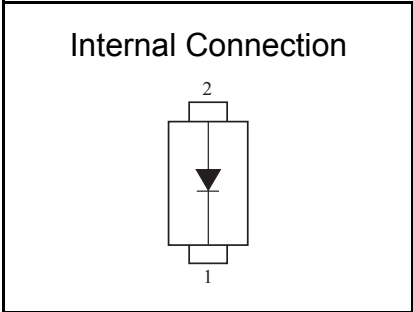
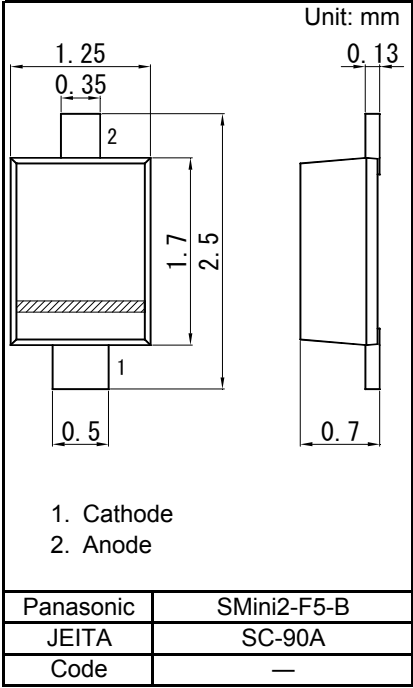
■ Packaging  
Embossed type (Thermo-compression sealing) : 3 000 pcs / reel (standard)

■ Absolute Maximum Ratings Ta = 25 °C

Parameter	Symbol	Rating	Unit
Reverse voltage	VR	40	V
Forward current (average) <sup>*1</sup>	IF(AV)	1	A
Non-repetitive peak forward surge current <sup>*2</sup>	IFSM	3	A
Junction temperature	Tj	125	°C
Operating ambient temperature	Topr	-40 to +85	°C
Storage temperature	Tstg	-55 to +125	°C

Note: <sup>\*1</sup> For embedded alumina substrate (substrate size: 5 cm× 5 cm)

<sup>\*2</sup> 50 Hz sine wave 1 cycle (Non-repetitive peak current)

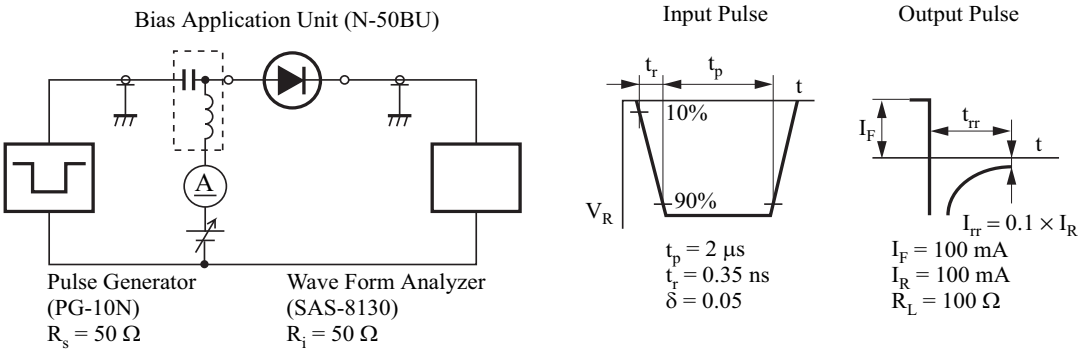




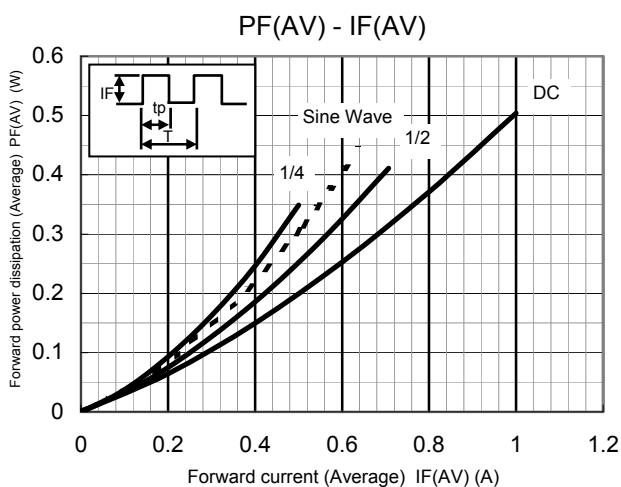
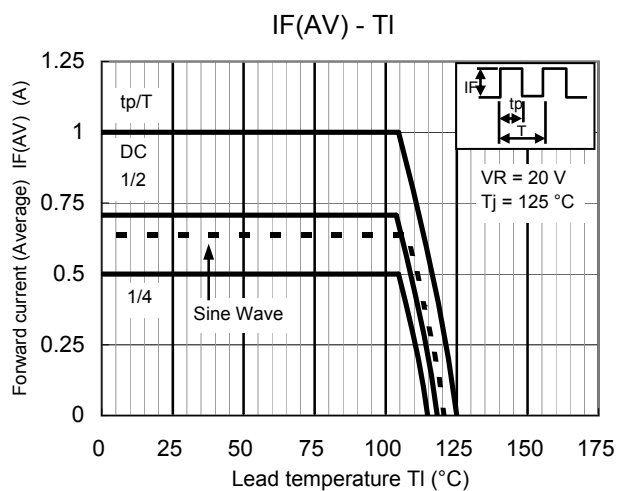
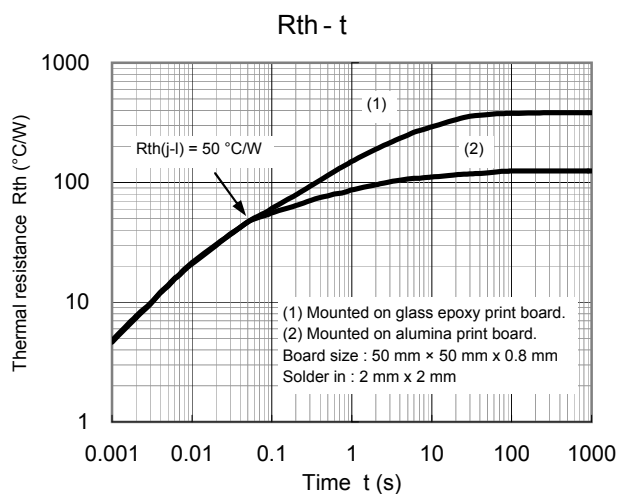
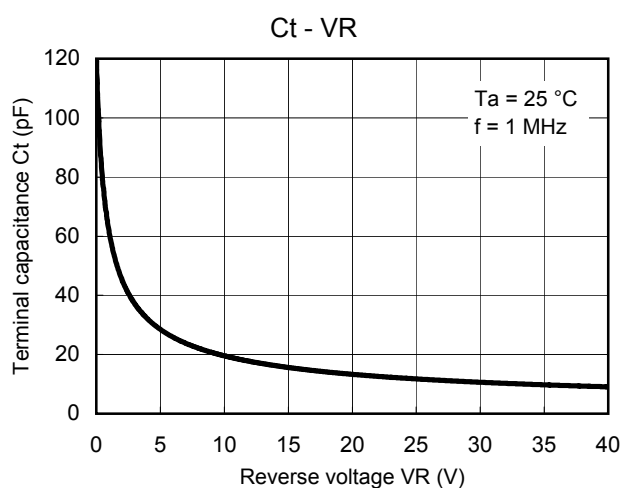
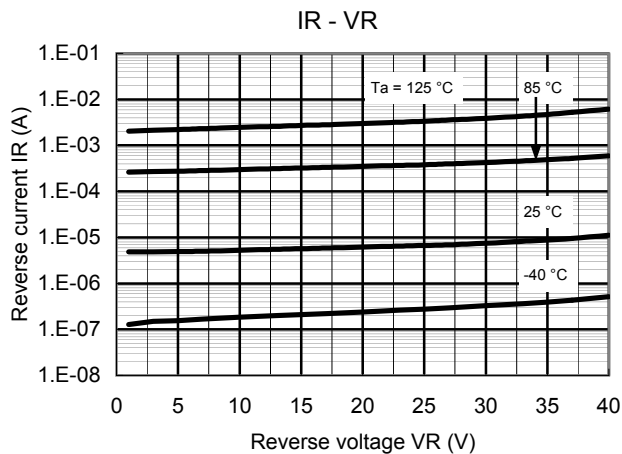
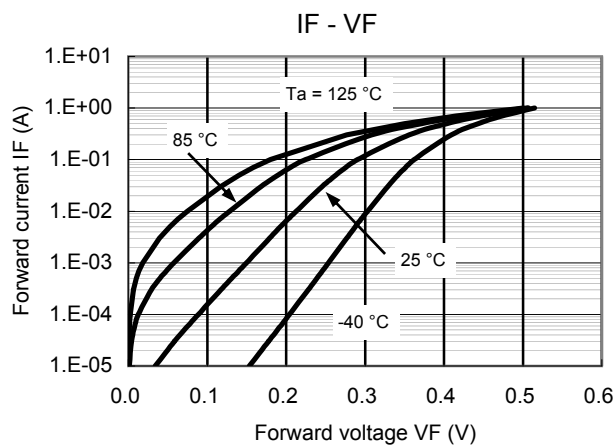
■ Electrical Characteristics Ta = 25 °C ± 3 °C

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Forward voltage	VF	IF = 1 A		0.50	0.58	V
Reverse current	IR	VR = 40 V		15	100	μA
Terminal capacitance	Ct	VR = 10 V, f = 1 MHz		21		pF
Reverse recovery time *1	trr	IF = 100 mA, Irr = 0.1×IR, RL = 100 Ω		6.8		ns

- Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 Measuring methods for Diodes.  
2. This product is sensitive to electric shock (static electricity, etc.). Due attention must be paid on the charge of a human body and the leakage of current from the operating equipment.  
3. \*1 trr test circuit



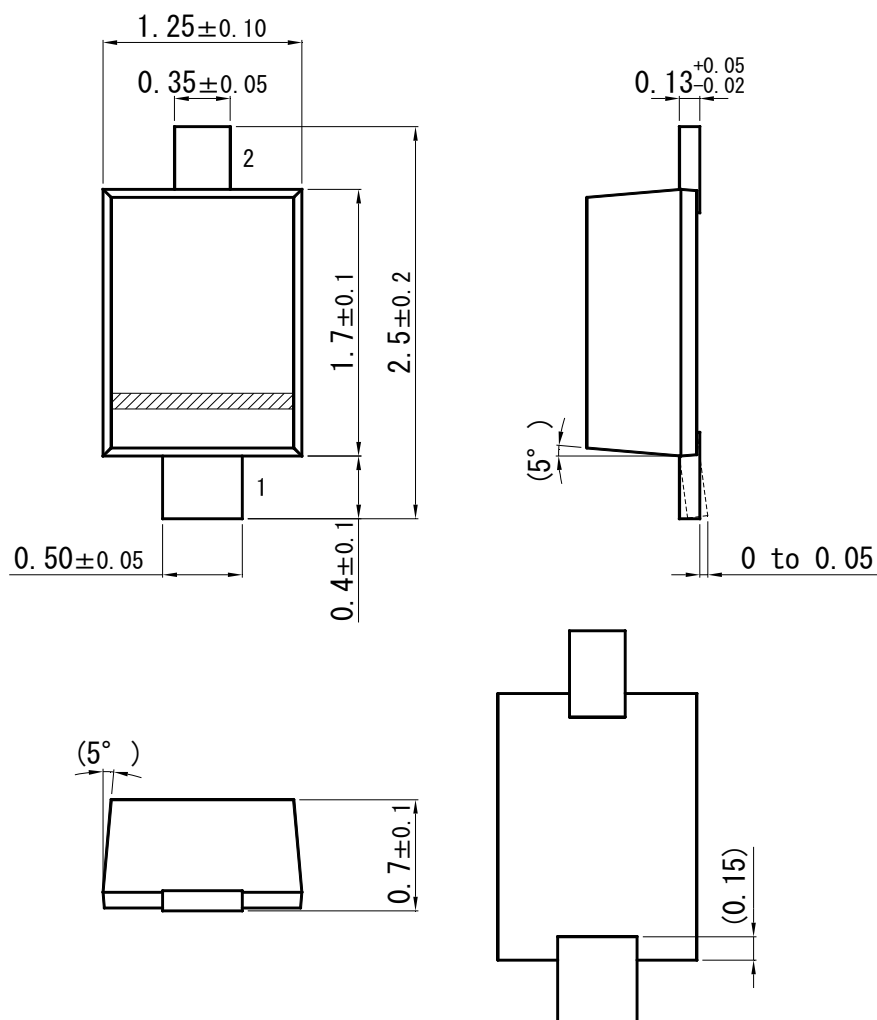
Technical Data ( reference )



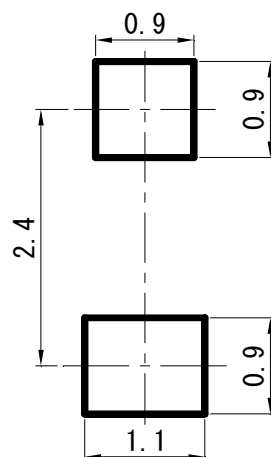
**Panasonic**

SMini2-F5-B

Unit: mm



■ Land Pattern (Reference) (Unit: mm)



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