

FJPF13009 NPN Silicon Transistor

Features

- High-Voltage Capability
- High Switching Speed
- Suitable for Electronic Ballast and Switched Mode Power Supply



1.Base 2.Collector 3.Emitter

Ordering Information

Part Number ⁽¹⁾	Marking	Package	Packing Method
FJPF13009H1TU	J13009-1	TO-220F 3L	Rail
FJPF13009H2TU	J13009-2	TO-220F 3L	Rail

Note:

The Suffix "-TU" means the tube packing method.

Absolute Maximum Ratings⁽²⁾

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_c = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	700	V
V _{CEO}	Collector-Emitter Voltage	400	V
V _{EBO}	Emitter-Base Voltage	9	V
۱ _C	Collector Current (DC)	12	А
I _{CP}	Collector Current (Pulse)	24	Α
Ι _Β	Base Current	6	Α
PD	Total Device Dissipation ($T_C = 25^{\circ}C$)	50	W
ТJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature Range	-65 to +150	°C

Note:

These ratings are based on a maximum junction temperature of 150°C.
These are steady state-limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

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^{1.} The Affix "-H2" means the hFE classification.

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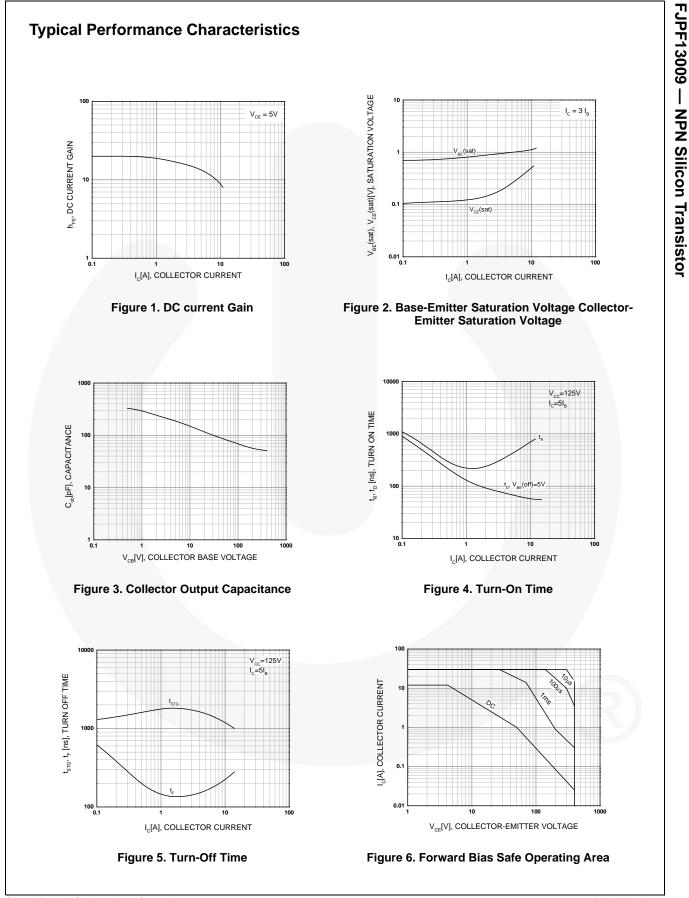
Electrical Characteristics⁽³⁾

Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

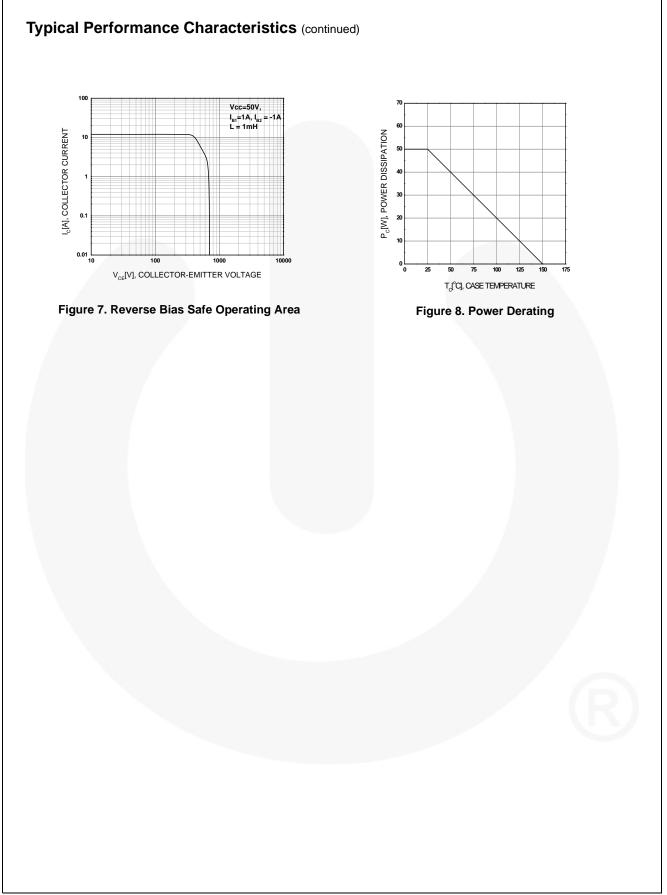
Symbol	Parameter	Conditions	Min.	Тур.	Max	Units
V _{CEO} (sus)	Collector-Emitter Sustaining Voltage	$I_{\rm C} = 10 {\rm mA}, I_{\rm B} = 0$	400			V
I _{EBO}	Emitter Cut-Off Current	$V_{EB} = 9 V, I_{C} = 0$			1	mA
h _{FE}	DC Current Gain	$V_{CE} = 5 \text{ V}, I_{C} = 5 \text{ A} (h_{FE1})$	8		40	
	DC Current Gain	$V_{CE} = 5 \text{ V}, \text{ I}_{C} = 8 \text{ A}$	6		30	
V _{CE} (sat)	Collector-Emitter Saturation	I _C = 5 A, I _B = 1 A			1.0	v
		I _C = 8 A, I _B = 1.6 A			1.5	
	, on age	I _C = 12 A, I _B = 3 A			3.0	
V _{BE} (sat)		I _C = 5 A, I _B = 1 A			1.2	V
	Base-Emitter Saturation Voltage	I _C = 8 A, I _B = 1.6 A			1.6	
C _{ob}	Output Capacitance	V _{CB} = 10 V, f = 0.1 MHz		180		pF
f _T	Current Gain Bandwidth Product	V _{CE} = 10 V, I _C = 0.5 A	4			MHz
t _{ON}	Turn-On Time	V _{CC} = 125 V, I _C = 8 A,			1.1	
t _{STG}	Storage Time	$I_{B1} = -I_{B2} = 1.6 \text{ A},$			3.0	μs
t _F	Fall Time	R _L = 15.6 Ω			0.7	

Note:

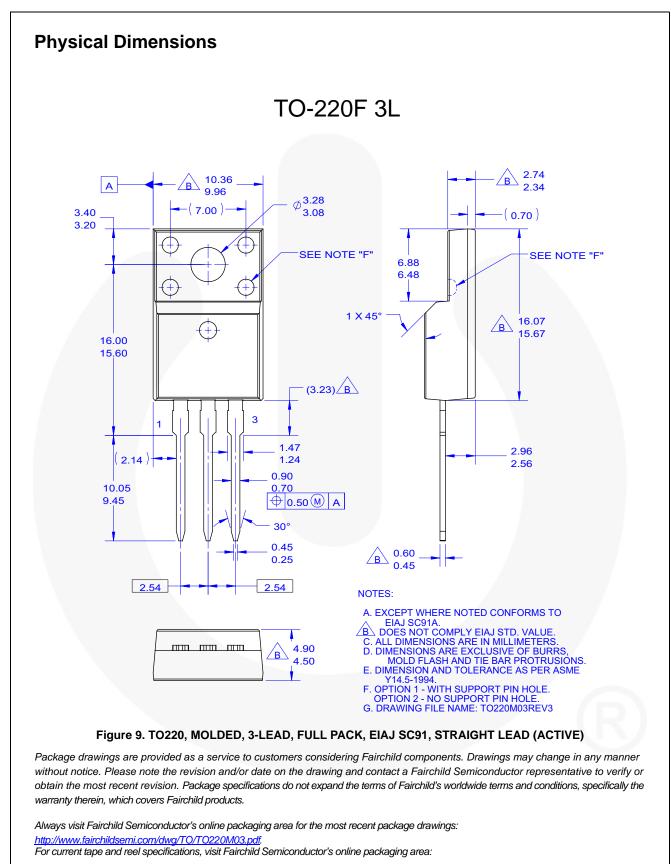
3. Pulse test: pulse width \leq 300 µs, duty cycle \leq 2%.



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http://www.fairchildsemi.com/packing_dwg/PKG-TO220M03_PSTS.pdf.

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