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November 2014

TIP30C PNP Epitaxial Silicon Transistor

Features

- · Medium Power Linear Switching Applications
- Complementary to TIP29 Series



1.Base 2.Collector 3.Emitter

Ordering Information

Part Number	Top Mark	Package	Packing Method
TIP30C	TIP30C	TO-220 3L (Single Gauge)	Bulk
TIP30CTU	TIP30C	TO-220 3L (Single Gauge)	Rail

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage	-100	V
V _{CEO}	Collector-Emitter Voltage	-100	V
V_{EBO}	Emitter-Base Voltage	-5	V
I _C	Collector Current (DC)	-1	Α
I _{CP}	Collector Current (Pulse)	-3	Α
I _B	Base Current	-0.4	Α
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature Range	-65 to 150	°C

Thermal Characteristics

Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter Va		Unit
Po	Collector Dissipation (T _A = 25°C)	2	W
PC	Collector Dissipation (T _C = 25°C)	30	VV

Electrical Characteristics

Values are at $T_C = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
V _{CEO} (sus)	Collector-Emitter Sustaining Voltage ⁽¹⁾	$I_C = -30 \text{ mA}, I_B = 0$	-100		V
I _{CEO}	Collector Cut-Off Current	$V_{CE} = -60 \text{ V}, I_{B} = 0$		-0.3	mA
I _{CES}	Collector Cut-Off Current	$V_{CE} = -100 \text{ V}, V_{EB} = 0$		-200	μΑ
I _{EBO}	Emitter Cut-Off Current	$V_{EB} = -5 \text{ V}, I_{C} = 0$		-1.0	mA
h _{FE}	DC Current Gain ⁽¹⁾	$V_{CE} = -4 \text{ V}, I_{C} = -0.2 \text{ A}$	40		
	DC Current Gain	$V_{CE} = -4 \text{ V}, I_{C} = -1 \text{ A}$	15	75	
V _{CE} (sat)	Collector-Emitter Saturation Voltage ⁽¹⁾	$I_C = -1 \text{ A}, I_B = -125 \text{ mA}$		-0.7	V
V _{BE} (on)	Base-Emitter On Voltage ⁽¹⁾	$V_{CE} = -4 \text{ V}, I_{C} = -1 \text{ A}$		-1.3	V
f _T	Current Gain Bandwidth Product	$V_{CE} = -10 \text{ V}, I_{C} = -200 \text{ mA},$ f = 1 MHz	3.0		MHz

Note:

1. Pulse test: $pw \le 300 \mu s$, duty cycle $\le 2\%$.

Typical Performance Characteristics

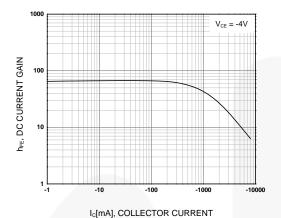


Figure 1. DC Current Gain

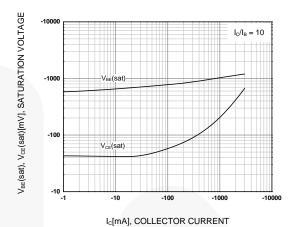


Figure 2. Base-Emitter Saturation Voltage and Collector-Emitter Saturation Voltage

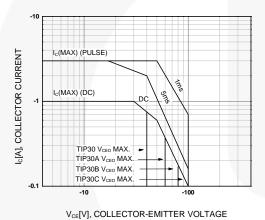


Figure 3. Safe Operating Area

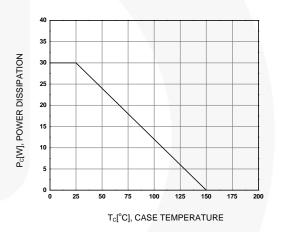
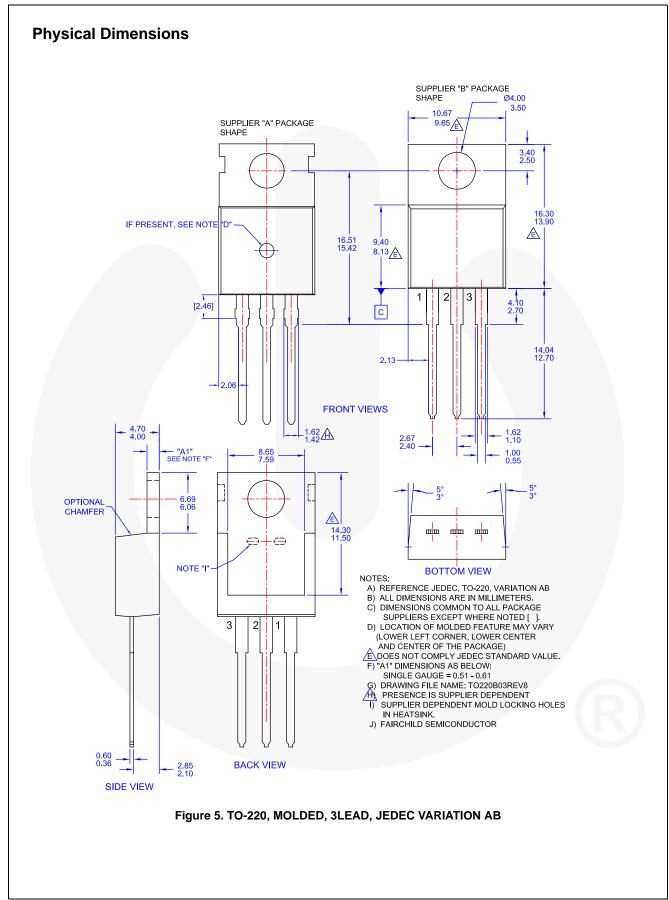


Figure 4. Power Derating







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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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